

MOS FIELD EFFECT TRANSISTOR **2SK3503**

N-CHANNEL MOS FIELD EFFECT TRANSISTOR FOR HIGH SPEED SWITCHING

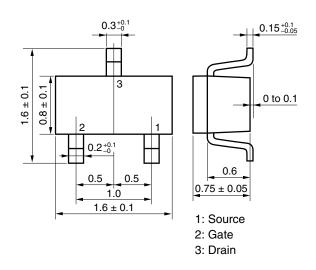
DESCRIPTION

The 2SK3503 is an N-channel vertical MOS FET. Because it can be driven by a voltage as low as 1.5 V and it is not necessary to consider a drive current, this FET is ideal as an actuator for low-current portable systems such as headphone stereos and video cameras.

FEATURES

- Automatic mounting supported
- Gate can be driven by a 1.5 V power source
- Because of its high input impedance, there's no need to consider a drive current
- Since bias resistance can be omitted, the number of components required can be reduced

★ PACKAGE DRAWING (Unit: mm)



ORDERING INFORMATION

PART NUMBER	PACKAGE
2SK3503	SC-75 (USM)

Marking: E1

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (Vgs = 0 V)	VDSS	16	V
Gate to Source Voltage (VDs = 0 V)	Vgss	±7.0	V
Drain Current (DC) (Tc = 25°C)	ID(DC)	±0.1	А
Drain Current (pulse) ^{Note1}	D(pulse)	±0.4	А
Total Power Dissipation (Tc = 25°C) Note2	Рт	200	mW
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	–55 to +150	°C

Notes 1. PW \leq 10 μ s, Duty Cycle \leq 1%

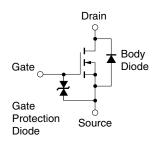
2. Mounted on ceramic substrate of $3.0 \text{ cm}^2 \times 0.64 \text{ mm}$

Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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EQUIVALENT CIRCUIT

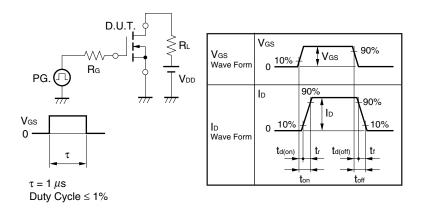


ELECTRICAL CHARACTERISTICS (TA = 25°C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	Idss	V _{DS} = 16 V, V _{GS} = 0 V			1.0	μA
Gate Leakage Current	Igss	V _{GS} = ±7.0 V, V _{DS} = 0 V			±3.0	μA
Gate Cut-off Voltage	V _{GS(off)}	V _{DS} = 3.0 V, I _D = 10 μA	0.5	0.8	1.1	V
Forward Transfer Admittance Note	y _{fs}	V _{DS} = 3.0 V, I _D = 10 mA	20			mS
Drain to Source On-state Resistance Note	RDS(on)1	V _{GS} = 1.5 V, I _D = 1.0 mA		20	50	Ω
	RDS(on)2	V _{GS} = 2.5 V, I _D = 10 mA		7.0	15	Ω
	RDS(on)3	V _{GS} = 4.0 V, I _D = 10 mA		5.0	12	Ω
Input Capacitance	Ciss	V _{GS} = 0 V		10		pF
Output Capacitance	Coss	V _{DS} = 3.0 V		13		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		3.0		pF
Turn-on Delay Time	td(on)	V _{DD} = 3.0 V, I _D = 10 mA		15		ns
Rise Time	tr	V _{GS} = 3.0 V		70		ns
Turn-off Delay Time	td(off)	R _G = 10 Ω		100		ns
Fall Time	tr			110		ns

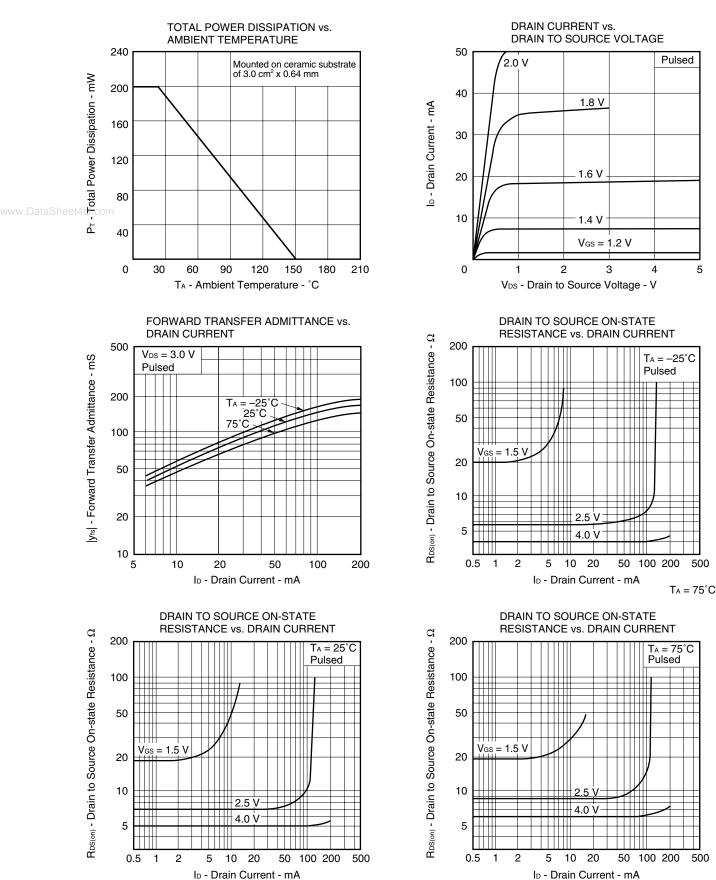
Note Pulsed

TEST CIRCUIT SWITCHING TIME



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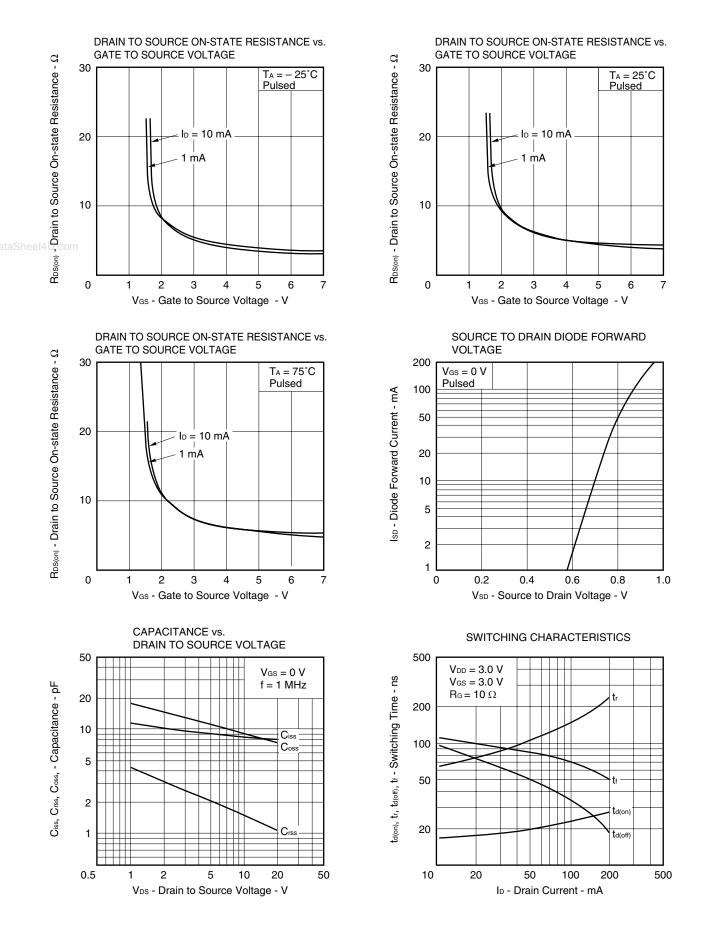
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Data Sheet D15395EJ2V0DS

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