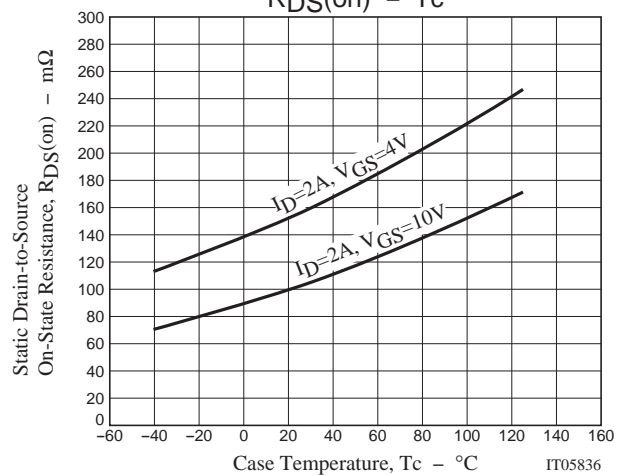
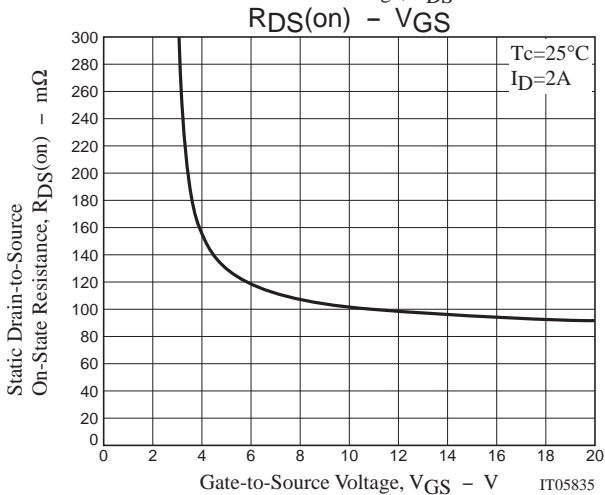
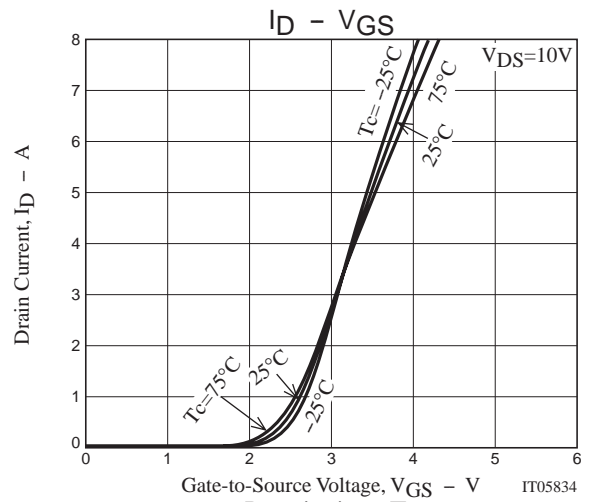
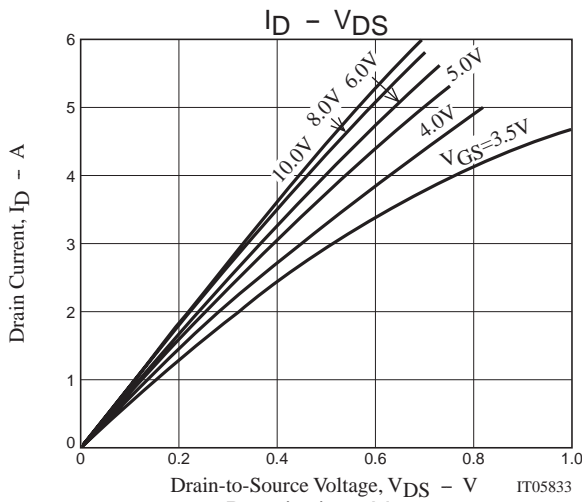
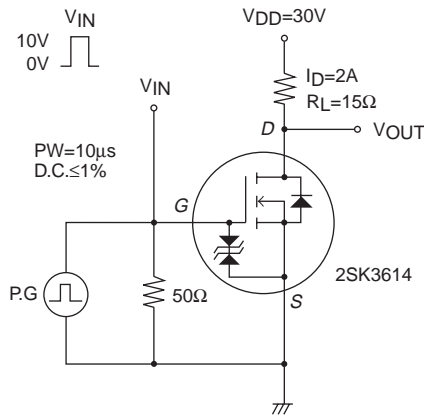


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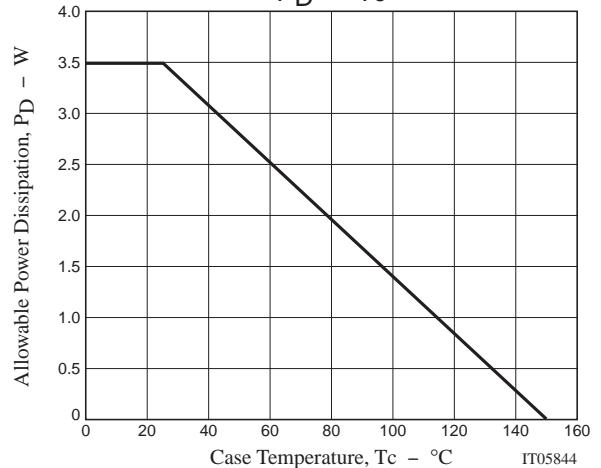
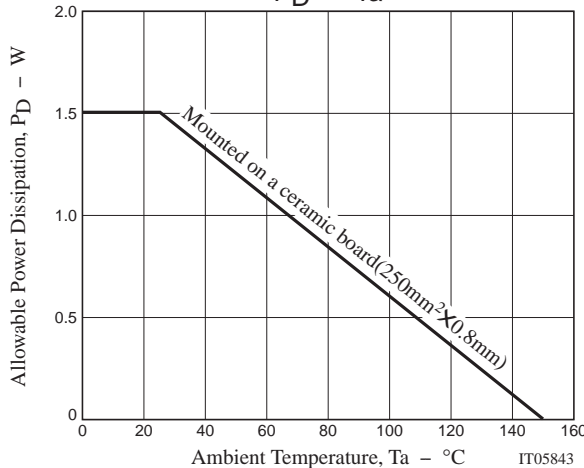
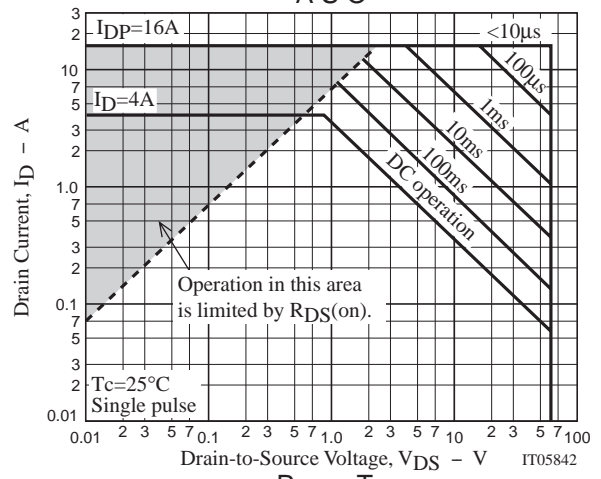
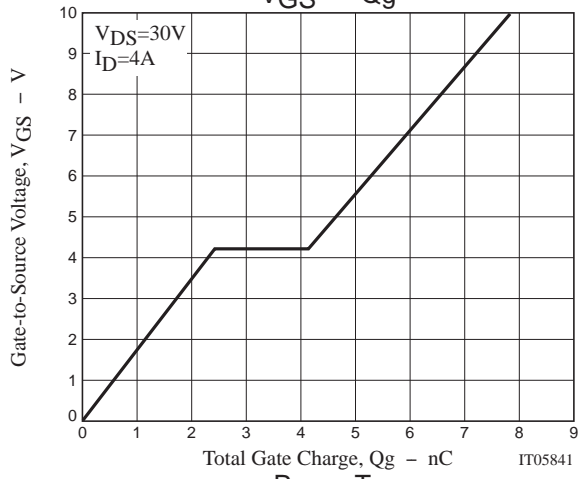
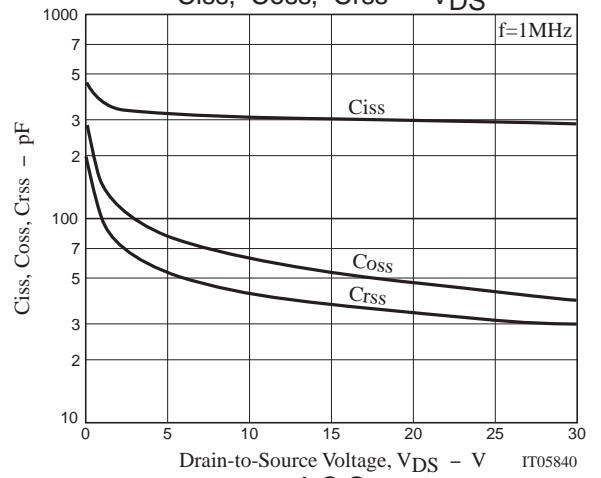
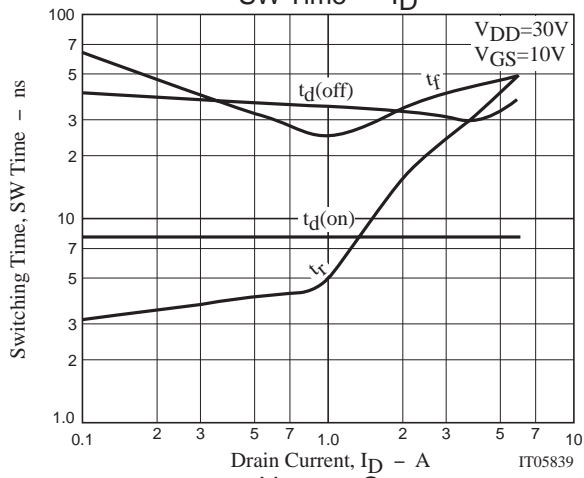
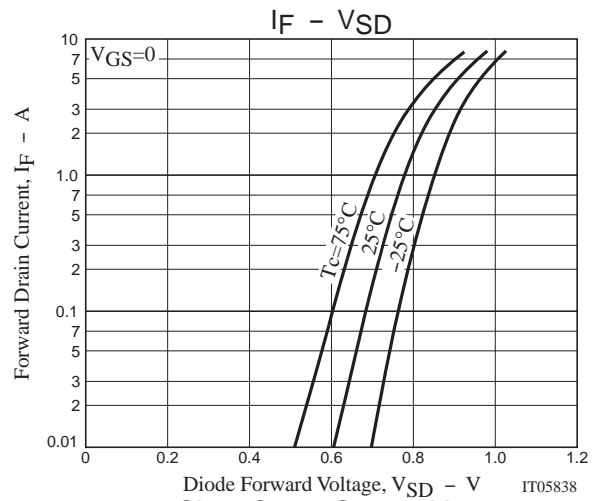
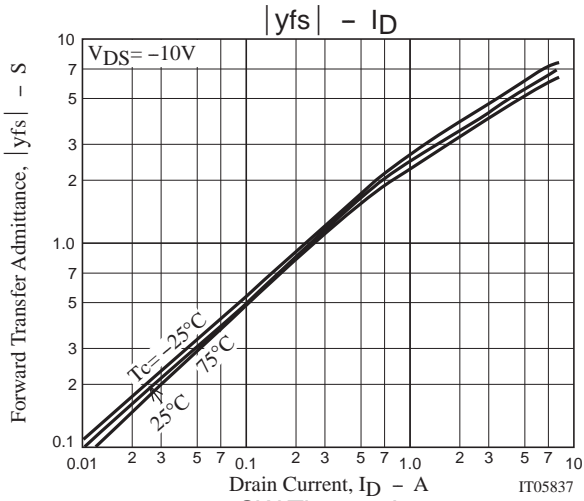
Continued from preceding page.

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Input Capacitance	C_{iss}	$V_{DS}=20V, f=1MHz$		300		pF
Output Capacitance	C_{oss}	$V_{DS}=20V, f=1MHz$		54		pF
Reverse Transfer Capacitance	C_{rss}	$V_{DS}=20V, f=1MHz$		34		pF
Turn-ON Delay Time	$t_d(on)$	See specified Test Circuit.		8		ns
Rise Time	t_r	See specified Test Circuit.		16		ns
Turn-OFF Delay Time	$t_d(off)$	See specified Test Circuit.		32		ns
Fall Time	t_f	See specified Test Circuit.		34		ns
Total Gate Charge	Q_g	$V_{DS}=30V, V_{GS}=10V, I_D=4A$		7.8		nC
Gate-to-Source Charge	Q_{gs}	$V_{DS}=30V, V_{GS}=10V, I_D=4A$		2.4		nC
Gate-to-Drain "Miller" Charge	Q_{gd}	$V_{DS}=30V, V_{GS}=10V, I_D=4A$		1.7		nC
Diode Forward Voltage	V_{SD}	$I_S=4A, V_{GS}=0$		0.86	1.2	V

Switching Time Test Circuit



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