

# MOS FIELD EFFECT TRANSISTOR 2SK3899

# SWITCHING N-CHANNEL POWER MOS FET

#### **DESCRIPTION**

The 2SK3899 is N-channel MOS Field Effect Transistor designed for high current switching applications.

# **ORDERING INFORMATION**

PART NUMBER	PACKAGE		
2SK3899-ZK	TO-263 (MP-25ZK)		

#### **FEATURES**

• Super low on-state resistance

 $R_{DS(on)1} = 5.3 \text{ m}\Omega \text{ MAX.} \text{ (Vgs} = 10 \text{ V, I}_D = 42 \text{ A})$ 

 $R_{DS(on)2} = 6.5 \text{ m}\Omega \text{ MAX.} \text{ (Vgs = 4.5 V, ID = 42 A)}$ 

- Low Ciss: Ciss = 5500 pF TYP.
- Built-in gate protection diode

(TO-263)



# ABSOLUTE MAXIMUM RATINGS ( $T_A = 25$ °C)

Drain to Source Voltage (Vgs = 0 V)	VDSS	60	V
Gate to Source Voltage (V <sub>DS</sub> = 0 V)	Vgss	±20	V
Drain Current (DC) (Tc = 25°C)	ID(DC)	±84	Α
Drain Current (pulse) Note1	D(pulse)	±336	Α
Total Power Dissipation (Tc = 25°C)	P <sub>T1</sub>	146	W
Total Power Dissipation (T <sub>A</sub> = 25°C)	P <sub>T2</sub>	1.5	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	-55 to +150	°C
Single Avalanche Energy Note2	Eas	245	mJ
Repetitive Avalanche Current Note3	lar	49.5	Α
Repetitive Avalanche Energy Note3	Ear	245	mJ

**Notes 1.** PW  $\leq$  10  $\mu$ s, Duty Cycle  $\leq$  1%

- **2.** Starting T<sub>ch</sub> = 25°C, V<sub>DD</sub> = 30 V, R<sub>G</sub> = 25  $\Omega$ , V<sub>GS</sub> = 20  $\rightarrow$  0 V, L = 100  $\mu$ H
- 3. Rg = 25  $\Omega$ , Tch(peak)  $\leq$  150°C

The information in this document is subject to change without notice. Before using this document, please confirm that this is the latest version.

Not all products and/or types are available in every country. Please check with an NEC Electronics sales representative for availability and additional information.



# **ELECTRICAL CHARACTERISTICS (TA = 25°C)**

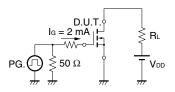
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V			10	μΑ
Gate Leakage Current	Igss	V <sub>GS</sub> = ±20 V, V <sub>DS</sub> = 0 V			±10	μΑ
Gate Cut-off Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	1.5	2.0	2.5	V
Forward Transfer Admittance Note	y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 42 A	35	70		S
Drain to Source On-state Resistance Note	R <sub>DS(on)1</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 42 A		4.2	5.3	mΩ
	R <sub>DS(on)2</sub>	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 42 A		4.9	6.5	mΩ
Input Capacitance	Ciss	V <sub>DS</sub> = 10 V		5500		pF
Output Capacitance	Coss	V <sub>GS</sub> = 0 V		1050		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		350		pF
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = 30 V, I <sub>D</sub> = 42 A		19		ns
Rise Time	tr	V <sub>GS</sub> = 10 V		13		ns
Turn-off Delay Time	<b>t</b> d(off)	R <sub>G</sub> = 0 Ω		91		ns
Fall Time	tf			10		ns
Total Gate Charge	Q <sub>G</sub>	V <sub>DD</sub> = 48 V		96		nC
Gate to Source Charge	Q <sub>G</sub> s	V <sub>GS</sub> = 10 V		18		nC
Gate to Drain Charge	Q <sub>GD</sub>	I <sub>D</sub> = 84 A		23.5		nC
Body Diode Forward Voltage Note	V <sub>F(S-D)</sub>	I <sub>F</sub> = 84 A, V <sub>GS</sub> = 0 V		0.92	1.5	V
Reverse Recovery Time	trr	I <sub>F</sub> = 84 A, V <sub>GS</sub> = 0 V		49		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/μs		70		nC

Note Pulsed

# **TEST CIRCUIT 1 AVALANCHE CAPABILITY**

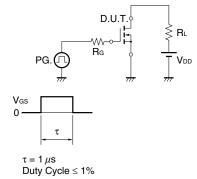
# $V_{GS} = 20 \rightarrow 0 \text{ V}$ $V_{DD}$ D.U.T. $R_{G} = 25 \Omega$ $V_{DD}$ $V_{DD}$ $R_{G} = 25 \Omega$ $R_{G} = 25 \Omega$

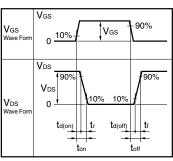
# TEST CIRCUIT 3 GATE CHARGE



Starting Tch

# **TEST CIRCUIT 2 SWITCHING TIME**

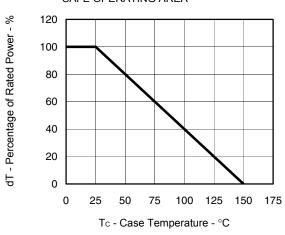




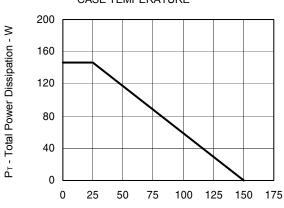


# TYPICAL CHARACTERISTICS (TA = 25°C)

# DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA

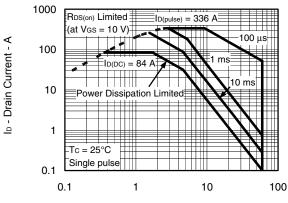


# TOTAL POWER DISSIPATION vs. CASE TEMPERATURE



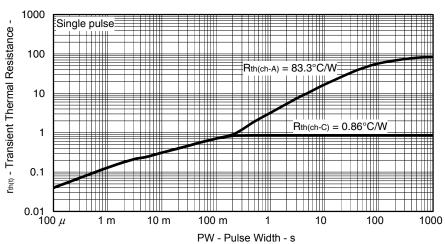
Tc - Case Temperature - °C

# FORWARD BIAS SAFE OPERATING AREA



## VDS - Drain to Source Voltage - V

## TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



3

0

VGS(off) - Gate Cut-off Voltage - V

0

1

# DRAIN TO SOURCE VOLTAGE 400 $V_{GS} = 10 V$ Ip - Drain Current - A 300 4.5 V 200 100

DRAIN CURRENT vs.

VDS - Drain to Source Voltage - V

3

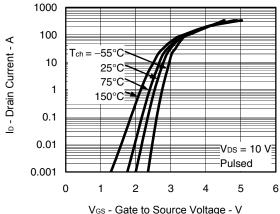
2

Pulsed

5

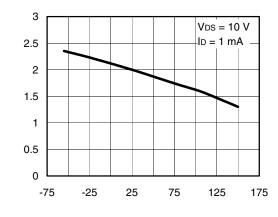
4

# FORWARD TRANSFER CHARACTERISTICS



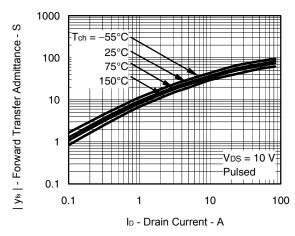
V<sub>GS</sub> - Gate to Source Voltage - V

#### GATE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE

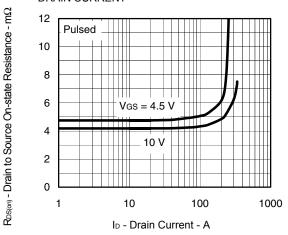


Tch - Channel Temperature - °C

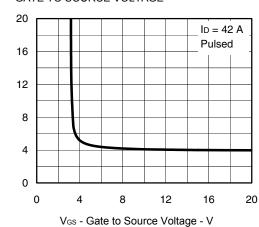
#### FORWARD TRANSFER ADMITTANCE vs. **DRAIN CURRENT**



DRAIN TO SOURCE ON-STATE RESISTANCE vs. **DRAIN CURRENT** 



DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE

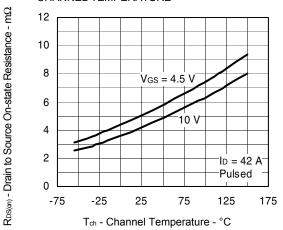


RDS(on) - Drain to Source On-state Resistance - m\Omega

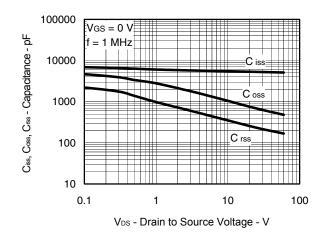
td(on), tr, td(off), tr - Switching Time - ns

IF - Diode Forward Current - A

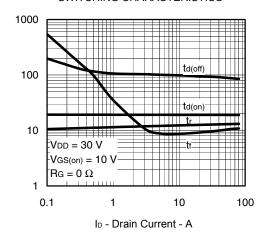
# DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



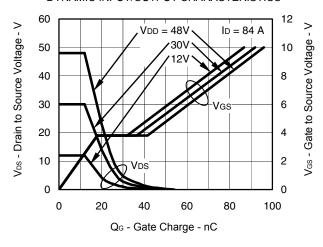
#### CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



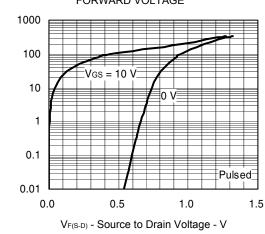
#### SWITCHING CHARACTERISTICS



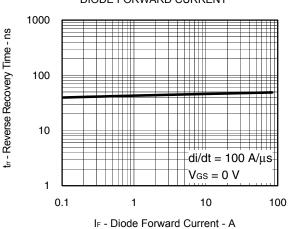
#### DYNAMIC INPUT/OUTPUT CHARACTERISTICS

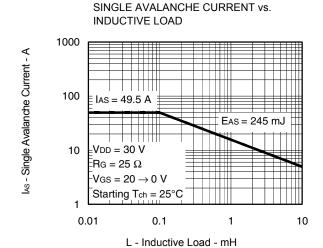


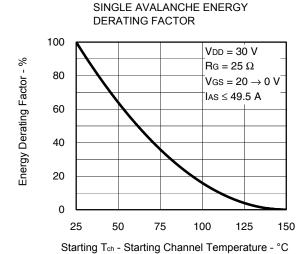
# SOURCE TO DRAIN DIODE FORWARD VOLTAGE



# REVERSE RECOVERY TIME vs. DIODE FORWARD CURRENT



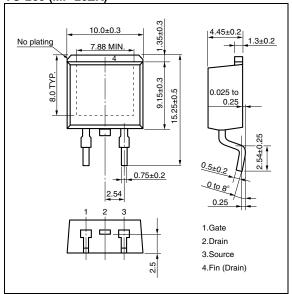




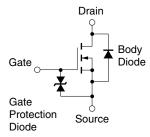


# PACKAGE DRAWING (Unit: mm)

# TO-263 (MP-25ZK)



# **EQUIVALENT CIRCUIT**



**Remark** The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

Data Sheet D17174EJ1V0DS 7

- The information in this document is current as of May, 2004. The information is subject to change
  without notice. For actual design-in, refer to the latest publications of NEC Electronics data sheets or
  data books, etc., for the most up-to-date specifications of NEC Electronics products. Not all
  products and/or types are available in every country. Please check with an NEC Electronics sales
  representative for availability and additional information.
- No part of this document may be copied or reproduced in any form or by any means without the prior written consent of NEC Electronics. NEC Electronics assumes no responsibility for any errors that may appear in this document.
- NEC Electronics does not assume any liability for infringement of patents, copyrights or other intellectual property rights of third parties by or arising from the use of NEC Electronics products listed in this document or any other liability arising from the use of such products. No license, express, implied or otherwise, is granted under any patents, copyrights or other intellectual property rights of NEC Electronics or others.
- Descriptions of circuits, software and other related information in this document are provided for illustrative
  purposes in semiconductor product operation and application examples. The incorporation of these
  circuits, software and information in the design of a customer's equipment shall be done under the full
  responsibility of the customer. NEC Electronics assumes no responsibility for any losses incurred by
  customers or third parties arising from the use of these circuits, software and information.
- While NEC Electronics endeavors to enhance the quality, reliability and safety of NEC Electronics products, customers agree and acknowledge that the possibility of defects thereof cannot be eliminated entirely. To minimize risks of damage to property or injury (including death) to persons arising from defects in NEC Electronics products, customers must incorporate sufficient safety measures in their design, such as redundancy, fire-containment and anti-failure features.
- NEC Electronics products are classified into the following three quality grades: "Standard", "Special" and "Specific".
  - The "Specific" quality grade applies only to NEC Electronics products developed based on a customer-designated "quality assurance program" for a specific application. The recommended applications of an NEC Electronics product depend on its quality grade, as indicated below. Customers must check the quality grade of each NEC Electronics product before using it in a particular application.
  - "Standard": Computers, office equipment, communications equipment, test and measurement equipment, audio and visual equipment, home electronic appliances, machine tools, personal electronic equipment and industrial robots.
  - "Special": Transportation equipment (automobiles, trains, ships, etc.), traffic control systems, anti-disaster systems, anti-crime systems, safety equipment and medical equipment (not specifically designed for life support).
  - "Specific": Aircraft, aerospace equipment, submersible repeaters, nuclear reactor control systems, life support systems and medical equipment for life support, etc.

The quality grade of NEC Electronics products is "Standard" unless otherwise expressly specified in NEC Electronics data sheets or data books, etc. If customers wish to use NEC Electronics products in applications not intended by NEC Electronics, they must contact an NEC Electronics sales representative in advance to determine NEC Electronics' willingness to support a given application.

#### (Note)

- (1) "NEC Electronics" as used in this statement means NEC Electronics Corporation and also includes its majority-owned subsidiaries.
- (2) "NEC Electronics products" means any product developed or manufactured by or for NEC Electronics (as defined above).