TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (π -MOS VI)

2SK4016

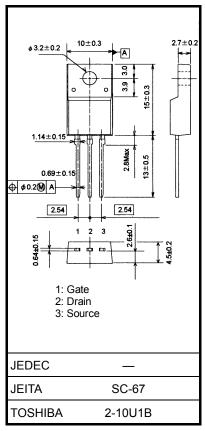
Switching Regulator Applications

Unit: mm

- Low drain-source ON-resistance: RDS (ON) = 0.33 Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 10 \text{ S (typ.)}$
- Low leakage current: $I_{DSS} = 100 \, \mu \, A \, (V_{DS} = 600 \, V)$
- Enhancement model: $V_{th} = 2.0 \sim 4.0 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA)}$

Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	600	V	
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V_{DGR}	600	V	
Gate-source voltage		V_{GSS}	±30	V	
Drain current	DC (Note 1)	ID	13	А	
	Pulse (t = 1 ms) (Note 1)	I _{DP}	52		
Drain power dissipation (Tc = 25°C)		P _D	50	W	
Single-pulse avalanche energy (Note 2)		E _{AS}	1033	mJ	
Avalanche current		I _{AR}	13	Α	
Repetitive avalanche energy (Note 3)		E _{AR}	5.0	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	



Weight : 1.7 g (typ.)

Thermal Characteristics

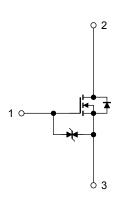
Characteristic	Symbol	Max	Unit	
Thermal resistance, channel to case	R _{th (ch-c)}	2.5	°C/W	
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W	

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $~V_{DD}=90~V,~T_{ch}=25^{\circ}C$ (initial), $L=10.7~mH,~I_{AR}=13~A,~R_{G}=25~\Omega$

Note 3: Repetitive rating: Pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



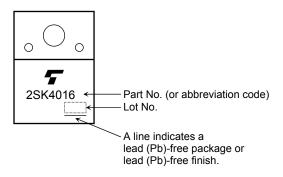
Electrical Characteristics (Ta = 25°C)

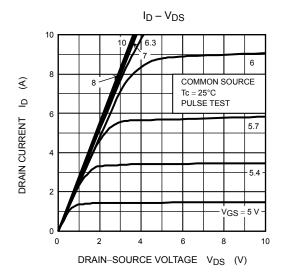
Char	acteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μΑ
Gate-source breakdown voltage		V (BR) GSS	$I_G = \pm 10 \ \mu A, \ V_{DS} = 0 \ V$	±30	_	_	V
Drain cutoff current		I _{DSS}	V _{DS} = 600 V, V _{GS} = 0 V	_	_	100	μΑ
Drain-source bre	akdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	600	_		V
Gate threshold v	oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	V
Drain-source ON	l-resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 6.5 A	_	0.33	0.50	Ω
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 6.5 A	5.0	10		S
Input capacitance		C _{iss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	_	3100		pF
Reverse transfer capacitance		C _{rss}		_	20	_	
Output capacitance		Coss			270		
Switching time	Rise time	t _r	V_{GS} $V_{DD} \simeq 200 \text{ V}$		60	_	
	Turn-on time	t _{on}		_	110	_	ns
	Fall time	t _f		_	50		
	Turn-off time	t _{off}	Duty \leq 1%, $t_W = 10 \ \mu s$	_	215	_	
Total gate charge		Qg		_	62	_	
Gate-source charge		Q _{gs}	$V_{DD} \simeq 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 13 \text{ A}$	_	40	_	nC
Gate-drain charge		Q _{gd}		_	22	_	

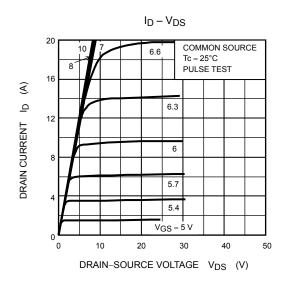
Source-Drain Ratings and Characteristics (Ta = 25°C)

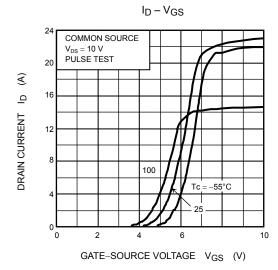
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1	I _{DR}	_	_	_	13	Α
Pulse drain reverse current (Note 1) I _{DRP}	_	_	_	52	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 13 A, V _{GS} = 0 V	_	_	-1.7	٧
Reverse recovery time	t _{rr}	I _{DR} = 13 A, V _{GS} = 0 V,	_	220	_	ns
Reverse recovery charge	Q _{rr}	dl _{DR} /dt = 100 A/μs	_	8.0	_	μС

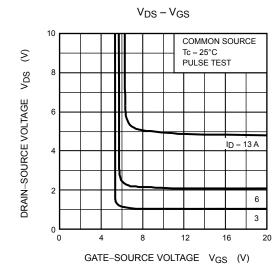
Marking

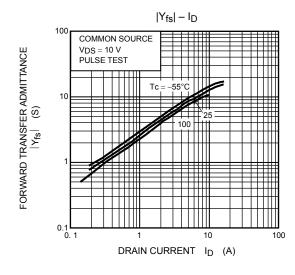


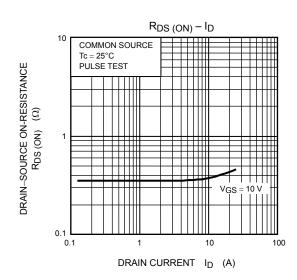


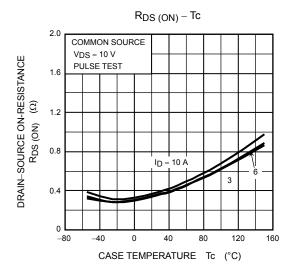


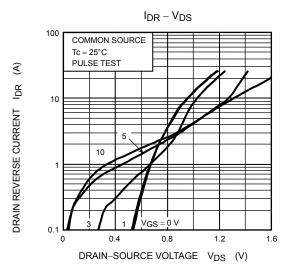


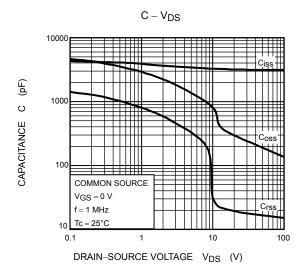


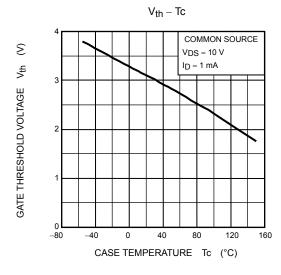


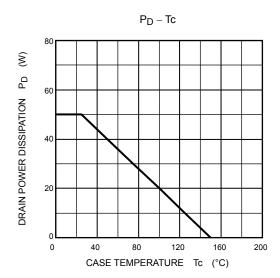


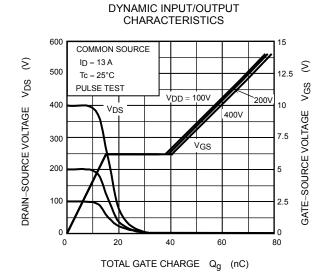


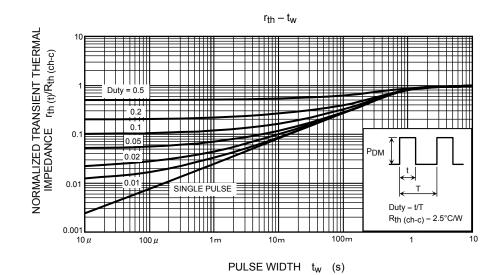


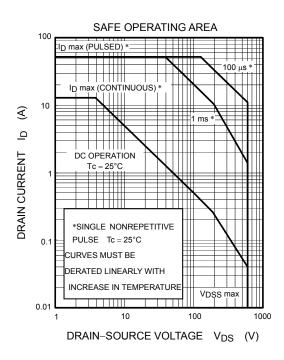


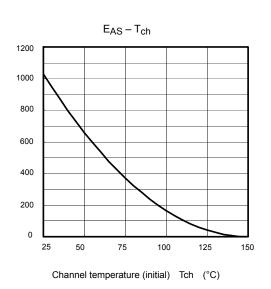


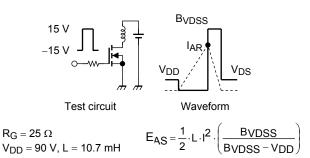












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