

# SANYO Semiconductors DATA SHEET

## 2SK4099LS-

N-Channel Silicon MOSFET

### General-Purpose Switching Device Applications

#### **Features**

- · Low ON-resistance, low input capacitance, ultrahigh-speed switching.
- · Adoption of high reliability HVP process.
- · Attachment workability is good by Mica-less package.
- · Avalanche resistance guarantee.

#### **Specifications**

#### Absolute Maximum Ratings at Ta=25°C

| Parameter                          | Symbol                | Conditions   | Ratings     | Unit |
|------------------------------------|-----------------------|--|-------------|------|
| Drain-to-Source Voltage            | VDSS                  |  | 600         | V    |
| Gate-to-Source Voltage             | VGSS                  |  | ±30         | ٧    |
| Drain Current (DC)                 | I <sub>Dc</sub> *1    | Limited only by maximum temperature                | 8.5         | Α    |
|                                    | I <sub>Dpack</sub> *2 | SANYO's ideal heat dissipation condition           | 6.9         | Α    |
| Drain Current (Pulse)              | IDP                   | PW≤10μs, duty cycle≤1%                             | 34          | Α    |
| Allowable Power Dissipation        | Do                    |  | 2.0         | W    |
|                                    | PD                    | Tc=25°C (SANYO's ideal heat dissipation condition) | 35          | W    |
| Channel Temperature                | Tch                   |  | 150         | °C   |
| Storage Temperature                | Tstg                  |  | -55 to +150 | °C   |
| Avalanche Energy (Single Pulse) *3 | EAS                   |  | 215         | mJ   |
| Avalanche Current *4               | IAV                   |  | 8.5         | Α    |

<sup>\*1</sup> Shows chip capability

Marking: K4099

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<sup>\*2</sup> Package limited

<sup>\*3</sup>  $V_{DD}$ =99V, L=5mH,  $I_{AV}$ =8.5A

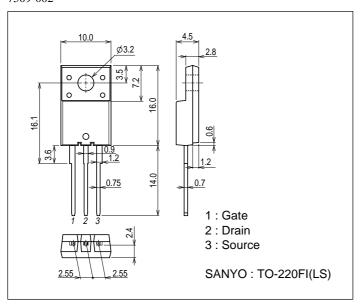
<sup>\*4</sup> L≤5mH, single pulse

#### Electrical Characteristics at Ta=25°C

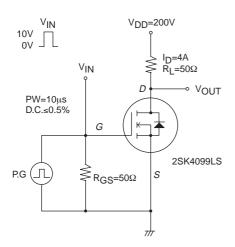
| Parameter                                  | Symbol              | Conditions  | Ratings |      |      | Unit  |
|--|---------------------|---|---------|------|------|-------|
|  |                     |   | min     | typ  | max  | Offic |
| Drain-to-Source Breakdown Voltage          | V(BR)DSS            | ID=10mA, VGS=0V   | 600     |      |      | V     |
| Zero-Gate Voltage Drain Current            | IDSS                | V <sub>DS</sub> =480V, V <sub>GS</sub> =0V                        |         |      | 100  | μΑ    |
| Gate-to-Source Leakage Current             | IGSS                | VGS=±30V, VDS=0V  |         |      | ±100 | nA    |
| Cutoff Voltage                             | VGS(off)            | VDS=10V, ID=1mA   | 3       |      | 5    | V     |
| Forward Transfer Admittance                | yfs                 | V <sub>DS</sub> =10V, I <sub>D</sub> =4A                          | 2.7     | 5.4  |      | S     |
| Static Drain-to-Source On-State Resistance | RDS(on)             | ID=4A, VGS=10V  |         | 0.72 | 0.94 | Ω     |
| Input Capacitance                          | Ciss                | V <sub>DS</sub> =30V, f=1MHz                                      |         | 750  |      | pF    |
| Output Capacitance                         | Coss                | V <sub>DS</sub> =30V, f=1MHz                                      |         | 140  |      | pF    |
| Reverse Transfer Capacitance               | Crss                | V <sub>DS</sub> =30V, f=1MHz                                      |         | 31   |      | pF    |
| Turn-ON Delay Time                         | t <sub>d</sub> (on) | See specified Test Circuit.                                       |         | 16   |      | ns    |
| Rise Time                                  | t <sub>r</sub>      | See specified Test Circuit.                                       |         | 37   |      | ns    |
| Turn-OFF Delay Time                        | td(off)             | See specified Test Circuit.                                       |         | 106  |      | ns    |
| Fall Time                                  | tf                  | See specified Test Circuit.                                       |         | 41   |      | ns    |
| Total Gate Charge                          | Qg                  | V <sub>DS</sub> =200V, V <sub>GS</sub> =10V, I <sub>D</sub> =8.5A |         | 29   |      | nC    |
| Gate-to-Source Charge                      | Qgs                 | VDS=200V, VGS=10V, ID=8.5A  |         | 5.2  |      | nC    |
| Gate-to-Drain "Miller" Charge              | Qgd                 | V <sub>DS</sub> =200V, V <sub>GS</sub> =10V, I <sub>D</sub> =8.5A |         | 16.5 |      | nC    |
| Diode Forward Voltage                      | V <sub>SD</sub>     | IS=8.5A, VGS=0V   |         | 0.9  | 1.2  | V     |

#### **Package Dimensions**

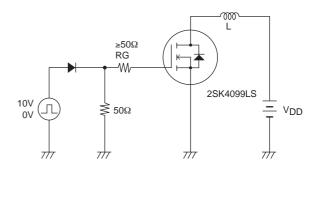
unit : mm (typ) 7509-002

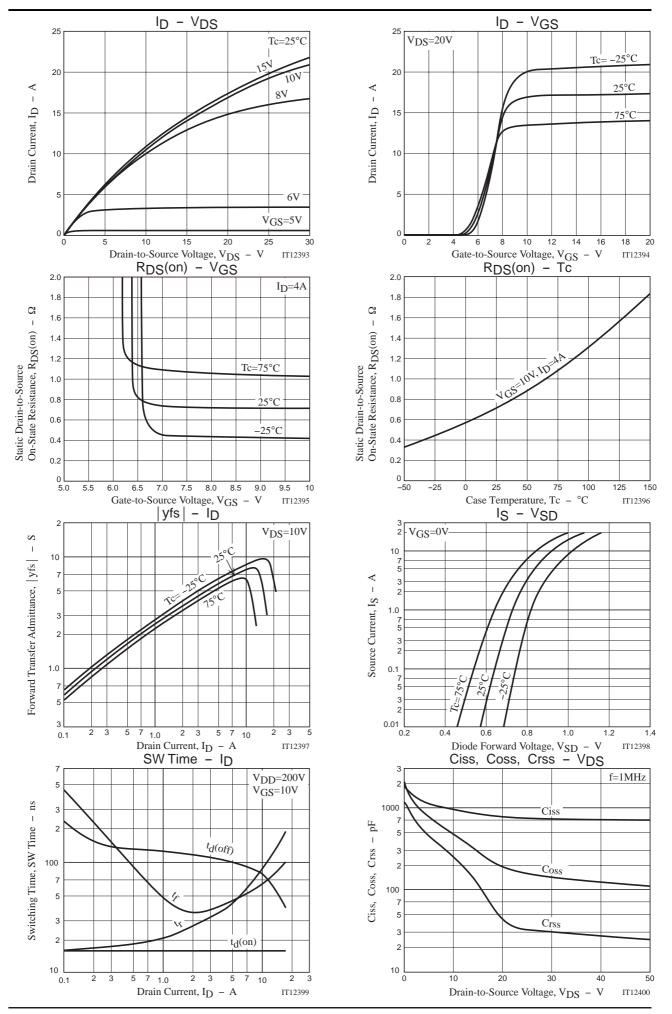


#### **Switching Time Test Circuit**

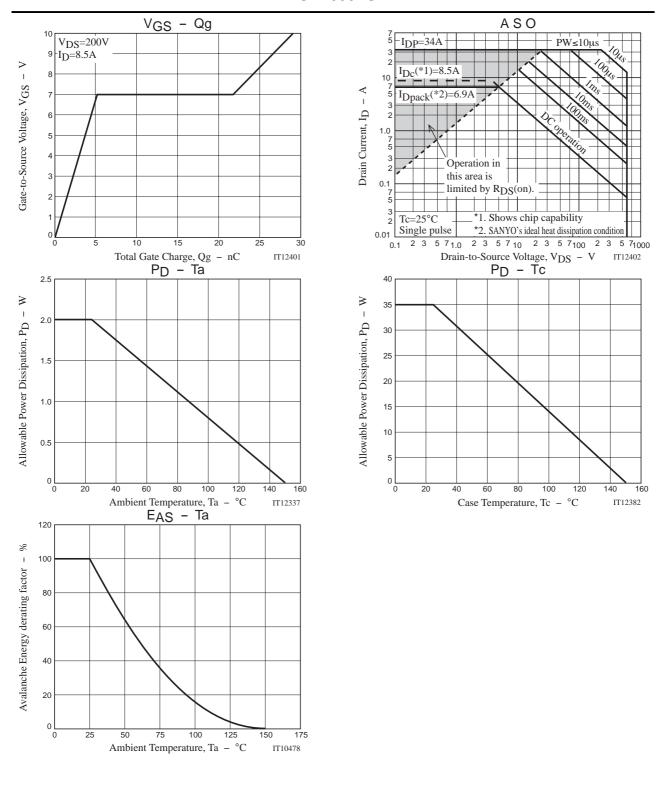


#### **Avalanche Resistance Test Circuit**





#### 2SK4099LS



Note on usage: Since the 2SK4099LS is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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