

## SANYO Semiconductors DATA SHEET

N-Channel Silicon MOSFET

# 2SK4118LS — General-Purpose Switching Device **Applications**

#### **Features**

- · Low ON-resistance, low input capacitance, ultrahigh-speed switching.
- · Adoption of high reliability HVP process.
- · Attachment workability is good by Mica-less package.
- · Avalanche resistance guarantee.

#### **Specifications**

#### Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		400	V
Gate-to-Source Voltage	VGSS		±30	V
Drain Current (DC)	I <sub>Dc</sub> *1	Limited only by maximum temperature	18	Α
	I <sub>Dpack</sub> *2	SANYO's ideal heat dissipation condition	11.9	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	60	Α
Allowable Power Dissipation	Do		2.0	W
	PD	Tc=25°C (SANYO's ideal heat dissipation condition)	37	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C
Avalanche Energy (Single Pulse) *3	EAS		427	mJ
Avalanche Current *4	IAV		18	Α

<sup>\*1</sup> Shows chip capability

Marking: K4118

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<sup>\*2</sup> Package limited

<sup>\*3</sup> VDD=99V, L=2mH, IAV=18A

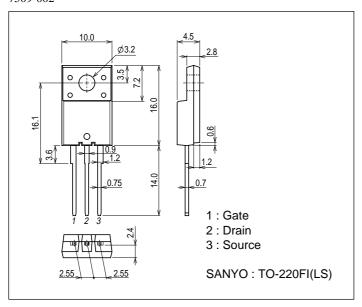
<sup>\*4</sup> L≤2mH, single pulse

#### Electrical Characteristics at Ta=25°C

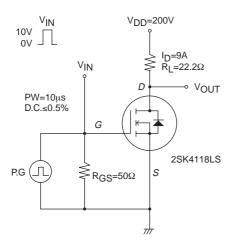
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=10mA, VGS=0V	400			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =320V, V <sub>GS</sub> =0V			100	μΑ
Gate-to-Source Leakage Current	IGSS	VGS=±30V, VDS=0V			±100	nA
Cutoff Voltage	VGS(off)	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA	3		5	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> =10V, I <sub>D</sub> =9A	4	8		S
Static Drain-to-Source On-State Resistance	RDS(on)	ID=9A, VGS=10V		0.26	0.34	Ω
Input Capacitance	Ciss	V <sub>DS</sub> =30V, f=1MHz		1000		рF
Output Capacitance	Coss	V <sub>DS</sub> =30V, f=1MHz		240		pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =30V, f=1MHz		52		pF
Turn-ON Delay Time	t <sub>d</sub> (on)	See specified Test Circuit.		25		ns
Rise Time	t <sub>r</sub>	See specified Test Circuit.		100		ns
Turn-OFF Delay Time	td(off)	See specified Test Circuit.		108		ns
Fall Time	tf	See specified Test Circuit.		49		ns
Total Gate Charge	Qg	V <sub>DS</sub> =200V, V <sub>GS</sub> =10V, I <sub>D</sub> =18A		38.8		nC
Gate-to-Source Charge	Qgs	V <sub>DS</sub> =200V, V <sub>GS</sub> =10V, I <sub>D</sub> =18A		6.7		nC
Gate-to-Drain "Miller" Charge	Qgd	V <sub>DS</sub> =200V, V <sub>GS</sub> =10V, I <sub>D</sub> =18A		25		nC
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =18A, V <sub>GS</sub> =0V		0.9	1.2	V

#### **Package Dimensions**

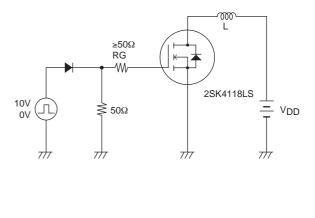
unit : mm (typ) 7509-002

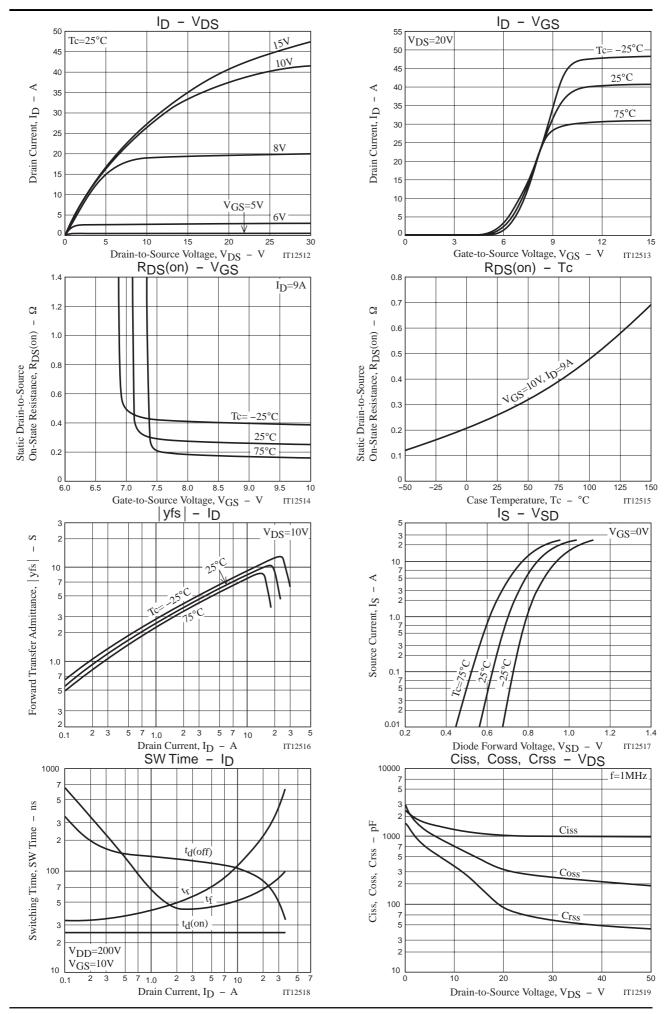


#### **Switching Time Test Circuit**

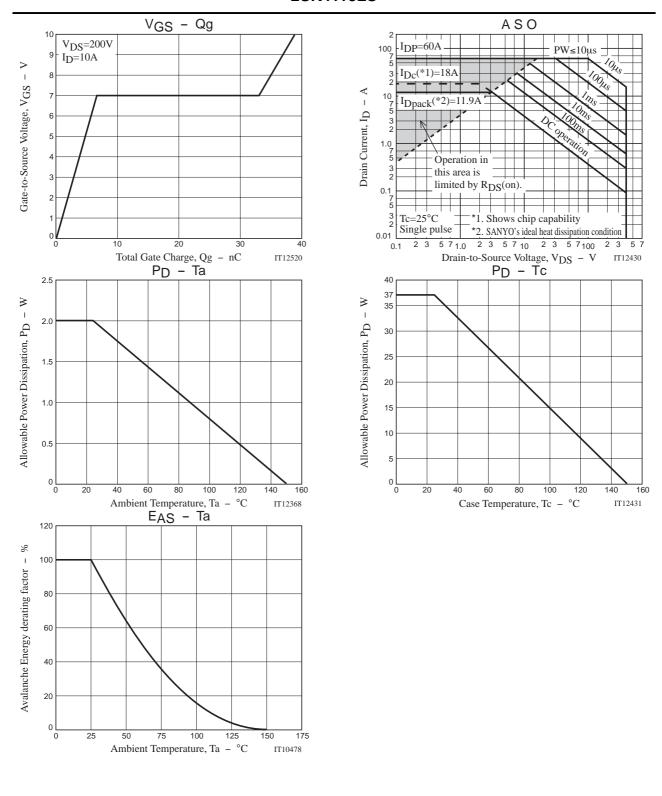


### **Avalanche Resistance Test Circuit**





#### 2SK4118LS



Note on usage : Since the 2SK4118LS is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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