

isc Silicon NPN Power Transistor

2ST31A

: V _{CEO} = • Good L • Minimu	PTION or-Emitter sustaining Voltage 60V(Min) .inearity of h _{FE} Im Lot-to-Lot variations for robust de nance and reliable operation	1 2 3	2 1 3 PIN 1. BASE 2.COLLECTOR 3. BMITTER TO-220C packa		
• Linear	Q-				F TS
SYMBOL	PARAMETER	VALUE	UNIT	<u>к</u>	
V _{сво}	Collector-Base Voltage	60	V		15,00 ^{-D}
V _{CEO}	Collector-Emitter Voltage	60	V	c	
V_{EBO}	Emitter-Base Voltage	5	V		mm MIN MAX
lc	Collector Current-Continuous	3	А	A B C	15.50 15.90 9.80 10.20 4.20 4.50
I _B	Base Current- Continuous	1	А	D F G	0.70 0.90 3.40 3.70 4.98 5.18
Pc	Total Power Dissipation @ Tc=25℃	40	W	H J K	2.68 2.90 0.44 0.60 12.80 13.40
TJ	Junction Temperature	150	°C	L Q R	1.20 1.45 2.70 2.90 2.30 2.70
T _{stg}	Storage Temperature Range	-65~150	°C	S U V	1.29 1.35 6.45 6.65 8.66 8.86



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ELECTRICAL CHARACTERISTICS

Tc=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	МАХ	UNIT
V _{CEO(sus)}	Collector-Emitter Breakdown Voltage	I _C =30mA ; I _B = 0	60			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 3A; I _B = 375mA			1.2	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 3A; I _B = 375mA			1.45	V
I _{CEO}	Collector Cutoff Current	V _{CE} = 30V ; I _E = 0			0.3	mA
I _{EBO}	Emitter Cutoff Current	V _{EB} =5V; I _C = 0			1.0	mA
h _{FE-1}	DC Current Gain	I _C =20mA ; V _{CE} =4V	100		150	
h _{FE-2}	DC Current Gain	I _C =1A ; V _{CE} =4V	25			

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