

isc Silicon NPN Power Transistor

2STD1665

DESCRIPTION

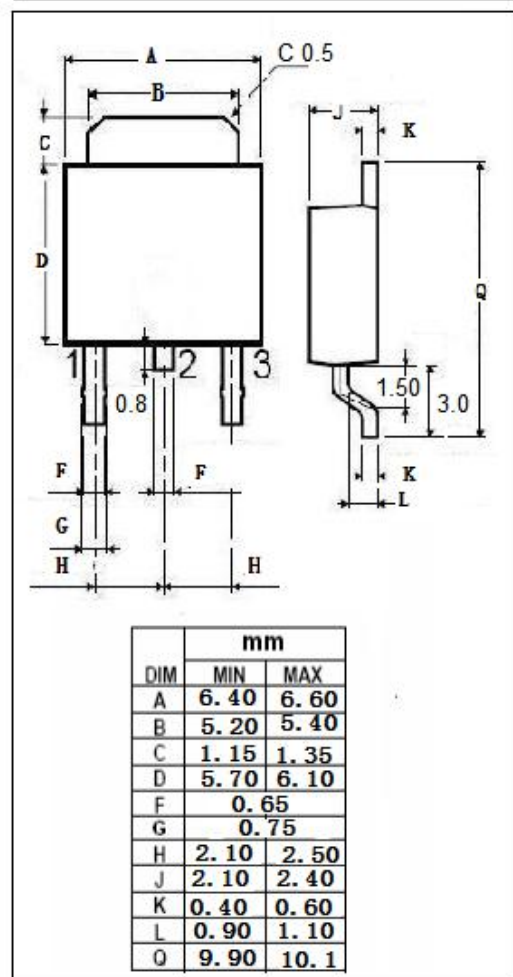
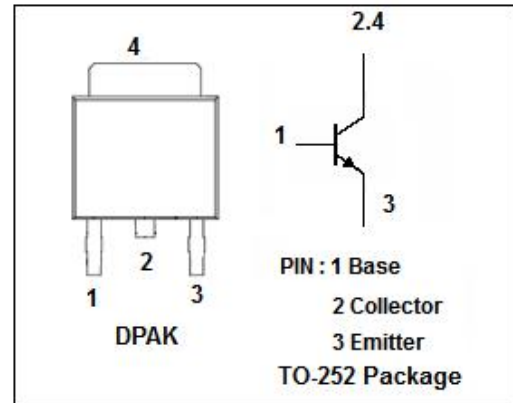
- Low collector saturation voltage
- High current gain characteristics
- Fast-switching speed
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Voltage regulators
- High efficiency low voltage switching

ABSOLUTE MAXIMUM RATINGS($T_a=25^{\circ}\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	150	V
V_{CEO}	Collector-Emitter Voltage	65	V
V_{EBO}	Emitter-Base Voltage	7	V
I_C	Collector Current-Continuous	6	A
I_{CM}	Collector Peak Current	20	A
P_C	Collector Power Dissipation	15	W
T_J	Junction Temperature	150	$^{\circ}\text{C}$
T_{stg}	Storage Temperature Range	-55~150	$^{\circ}\text{C}$



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ELECTRICAL CHARACTERISTICS

T_c=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CE(sat)-1} ^{NOTE}	Collector-Emitter Saturation Voltage	I _C = 0.1A; I _B = 5mA			50	mV
V _{CE(sat)-2} ^{NOTE}	Collector-Emitter Saturation Voltage	I _C = 1.0A; I _B = 50mA			0.12	V
V _{CE(sat)-3} ^{NOTE}	Collector-Emitter Saturation Voltage	I _C = 2.0A; I _B = 50mA			0.20	V
V _{CE(sat)-4} ^{NOTE}	Collector-Emitter Saturation Voltage	I _C = 6.0A; I _B = 150mA			0.60	V
V _{CE(sat)-5} ^{NOTE}	Collector-Emitter Saturation Voltage	I _C = 6A; I _B = 300mA			0.38	V
V _{BE(sat)} ^{NOTE}	Base-Emitter Saturation Voltage	I _C = 4A; I _B = 200mA			1.15	V
V _{BE(ON)} ^{NOTE}	Base-Emitter On Voltage	I _C = 4A; V _{CE} =1V			1.0	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 120V; I _E = 0			50	nA
I _{EBO}	Emitter Cutoff Current	V _{EB} = 7V; I _C = 0			10	nA
h _{FE-1}	DC Current Gain	I _C = 10mA; V _{CE} = 1V	150			
h _{FE-2}	DC Current Gain	I _C = 2A; V _{CE} = 1V	150		350	
h _{FE-3}	DC Current Gain	I _C = 5A; V _{CE} = 1V	90			
h _{FE-4}	DC Current Gain	I _C = 10A; V _{CE} = 1V	30			
C _{OB}	Output Capacitance	I _E = 0; V _{CB} = 10V; f= 1.0MHz		45		pF

NOTE:Pulse test PW≤300us,duty cycle ≤2%

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