

## Phase Control Thyristors

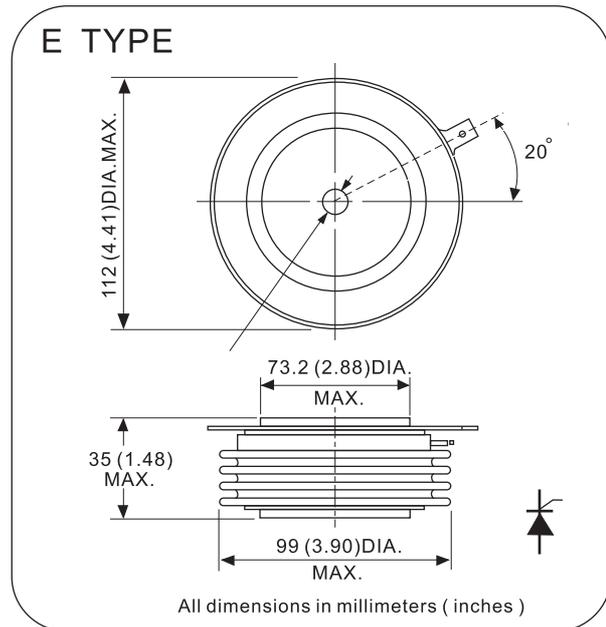
### Features

1. Center amplifying gate.
2. Metal Case With Ceramic insulator.
3. Typical application
  - DC motor control
  - Controlled DC power supplies
  - AC controllers

### Ordering code

<b>3050</b>	<b>PT</b>	<b>16</b>	<b>E</b>	<b>O</b>
(1)	(2)	(3)	(4)	(5)

- (1) Maximum average on-state current , A
- (2) For Phase Control Thyristor
- (3) Voltage code , code x 100 =  $V_{RRM} / V_{DRM}$
- (4) package style : A , B , C , D , E , EX for Disk Type
- (5) Terminal types  
0 - for eyelet



### Electrical Characteristics

Symbol	Parameter	Condition	Value			Unit
			Min.	Type	Max.	
$I_{T(AV)}$	Mean on-state current	180° half sine wave , 50Hz Double side cooled , $T_C = 85^\circ C$			3050	A
$I_{T(RMS)}$	Max. RMS on-state current	Double side cooled , $T_{hs} = 55^\circ C$			4420	A
$V_{RRM}$ $V_{DRM}$	Repetitive peak off-state voltage Repetitive peak reverse voltage	$V_{DRM} \& V_{RRM} \ t_p = 10ms$ $V_{DsM} \& V_{RsM} = V_{DRM} \& V_{RRM} + 100V$	2000		2600	V
$I_{TSM}$	Surge on-state current	10 ms half sine wave			46	KA
$I_t^2$	For fusing coordination	$V_R = 0.6V_{RRM}$			$13 \times 10^6$	$A^2 s$
$V_{T(TO)}$	Threshold voltage				0.92	V
$r_t$	On-state slope resistance				0.16	$m\Omega$
$V_{TM}$	Max. Forward voltage drop	$I_{TM} = 5000A$ , $F = 35KN$			1.4	V
$I_H$	Holding current	$V_A = 12V$ , $I_A = 1A$			1000	mA
$di/dt$	Critical rate of rise of turned-on current	Gate drive 20V , 20 $\Omega$ , $t_r \leq 0.5\mu s$			250	A/ $\mu s$
$I_{RRM}$ $I_{DRM}$	Repetitive peak reverse current	$V_R = V_{RRM}$ $V_D = V_{DRM}$			200	mA
$dv/dt$	Critical rate of rise of off-state voltage	$V_{DM} = 0.67 V_{DRM}$			1000	V/ $\mu s$
$P_G$	Max. average gate power	Square wavepulse width 100 $\mu s$			5	W
$P_{GM}$	Max. peak gate power square				30	W
$I_{GT}$	Gate trigger current	$V_A = 12V$ , $I_A = 1A$			300	mA
$V_{GT}$	Gate trigger voltage				3.0	V
$V_{GD}$	DC voltage not to trigger	At 67% $V_{DRM}$ , $T_j = T_j \text{ max.}$			0.25	V
$I_{FGM}$	Max. peak positive gate current	$T_j = T_j \text{ max.}$ , $t_p \leq 3s$			5	mA
$V_{FGM}$	Max. peak positive gate voltage				30	V
$V_{RGM}$	Max. peak negative gate voltage				0.25	V
$T_j$	Max. operating temperature range				125	$^\circ C$
$T_{stg}$	Storage temperature		- 40		150	$^\circ C$
$R_{th(j-h)}$	Thermal resistance(junction to heatsink)	Double side cooled , clamping force 35 KN			0.011	K/W
$F_m$	Mounting force		27		47	KN
$W_t$	Approximate weight				1100	g

$$i_T = f(v_T)$$

$t_{vj} = 125^\circ\text{C}$

Fig. 2

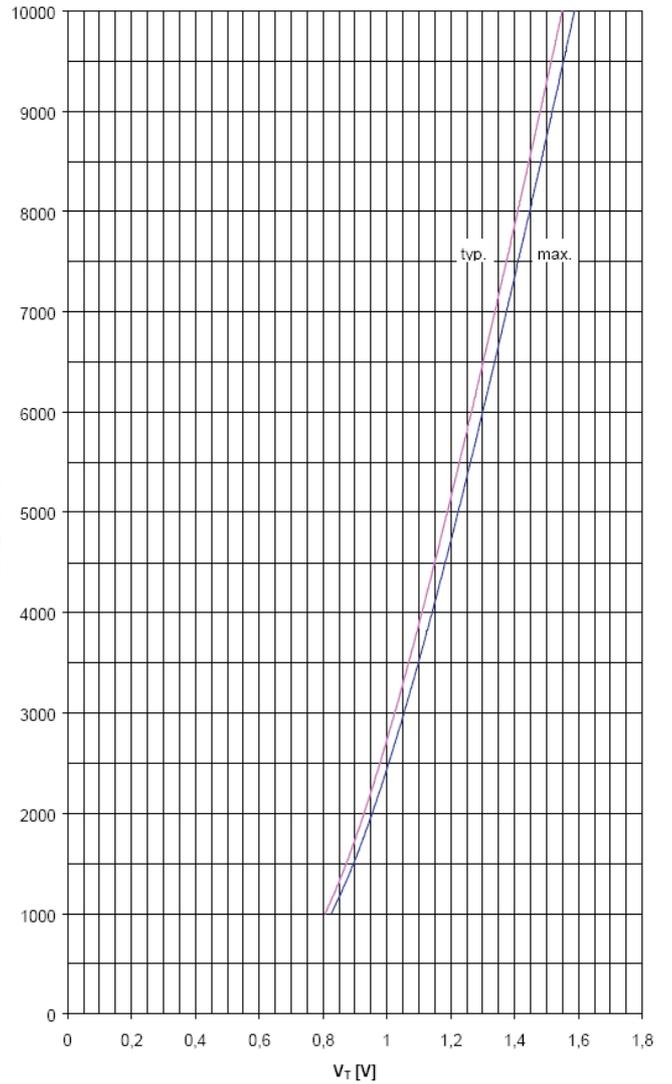
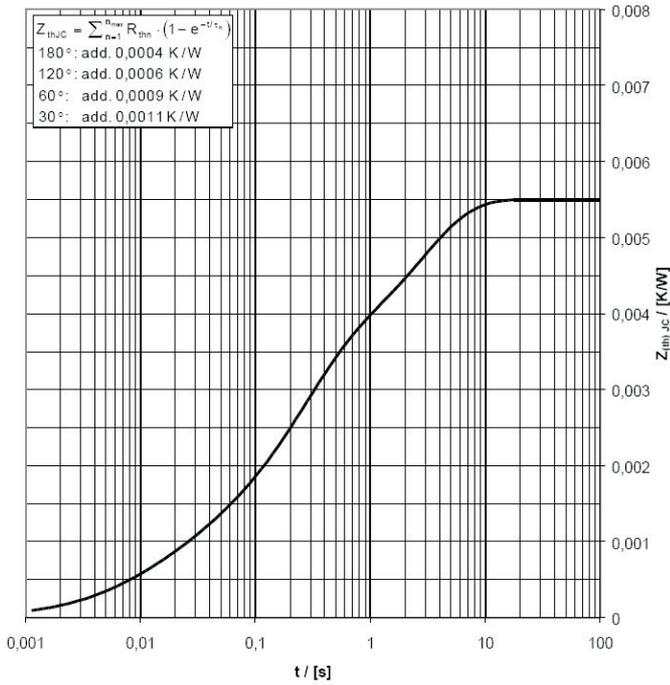


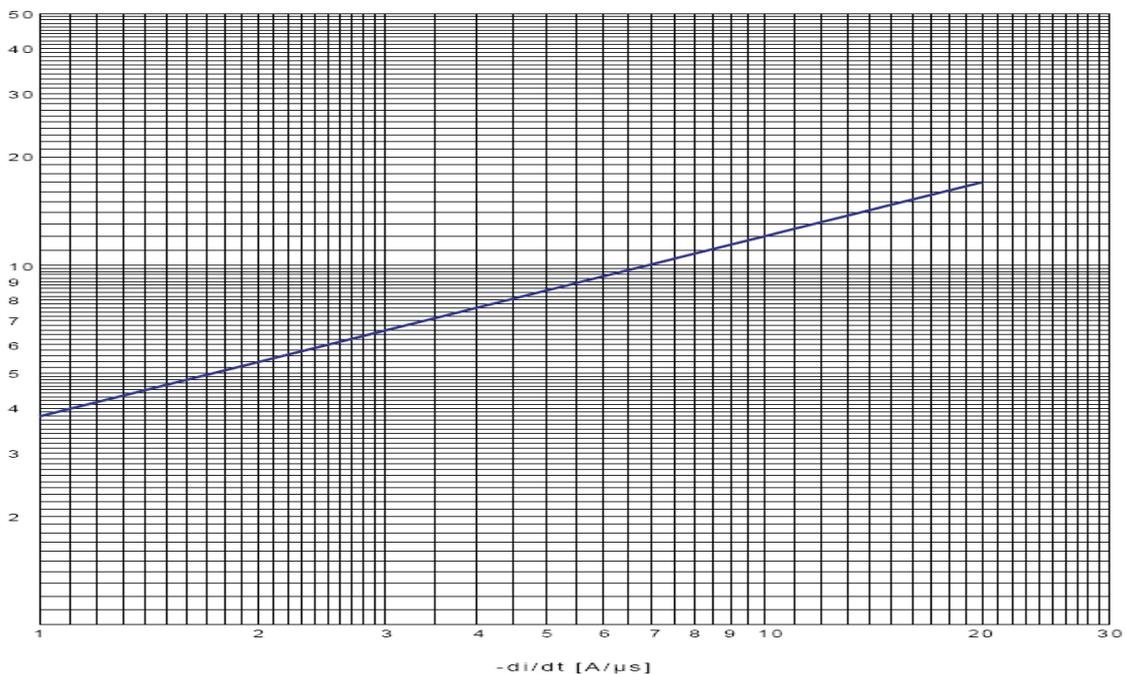
Fig. 1



$$Q_{rr} = f(-di/dt)$$

Fig. 3

$t_{vj} = 125^\circ\text{C}, I_{TM} = 2000\text{A}, V_R = 0,5 \cdot V_{RRM}, V_{RM} = 0,8 \cdot V_{RRM}$



-di/dt [A/μs]