

isc Silicon NPN Power Transistor

3DD401

DESCRIPTION

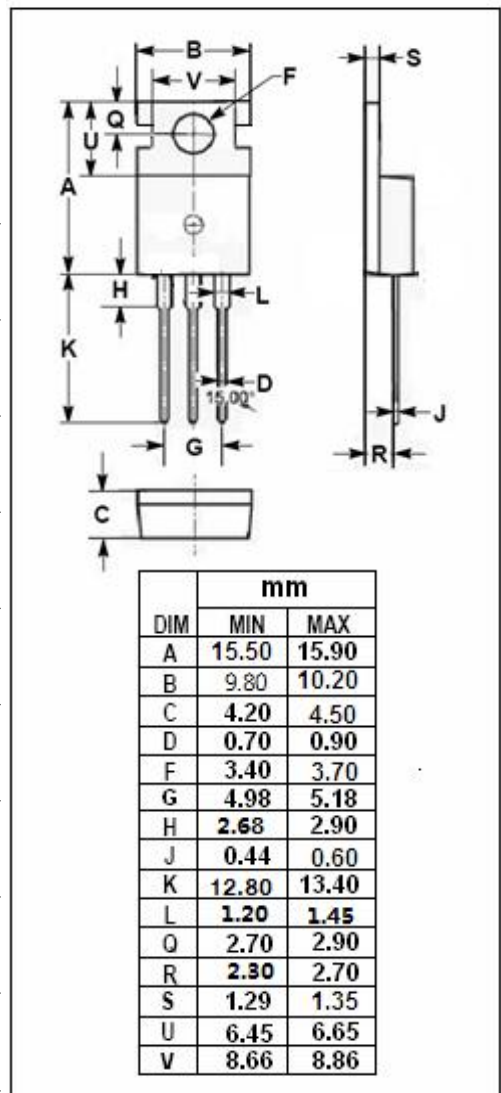
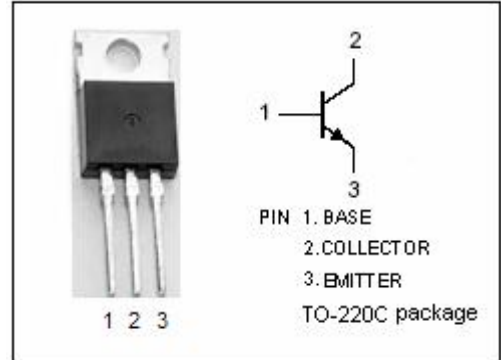
- Collector-Emitter Breakdown Voltage-
:V_{(BR)CEO}= 150V(Min)
- Wide Area of Safe Operation
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Power amplifier applications.
- Vertical output applications.

ABSOLUTE MAXIMUM RATINGS(T_a=25°C)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	200	V
V _{CEO}	Collector-Emitter Voltage	150	V
V _{EBO}	Emitter-Base Voltage	5	V
I _C	Collector Current-Continuous	2	A
I _B	Base Current-Continuous	0.5	A
P _C	Collector Power Dissipation @ T _C =25°C	25	W
T _J	Junction Temperature	150	°C
T _{stg}	Storage Temperature Range	-55~150	°C



isc Silicon NPN Power Transistor**3DD401****ELECTRICAL CHARACTERISTICS****T_C=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 10mA; I _B = 0	150			V
V _{(BR)CBO}	Collector-Base Breakdown Voltage	I _C = 0.5mA; I _E = 0	200			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = 0.5mA; I _C = 0	5			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 500mA; I _B = 50mA			1	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 500mA; I _B = 50mA			1.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 100V ; I _E = 0			50	μ A
I _{EBO}	Emitter Cutoff Current	V _{EB} = 5V; I _C = 0			10	μ A
h _{FE}	DC Current Gain	I _C = 0.4A ; V _{CE} = 10V	40		240	

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