

3FT1N2

主要参数 MAIN CHARACTERISTICS

$I_{T(RMS)}$	1.0A
V_{DRM}	800V
I_{GT}	10mA

用途

- 交流开关
- 相位控制

产品特性

- 玻璃钝化芯片, 高可靠性和一致性
- 低通态电流和高浪涌电流能力
- 环保 RoHS 产品

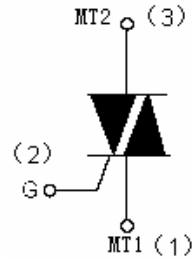
APPLICATIONS

- AC switching
- Phase control

FEATURES

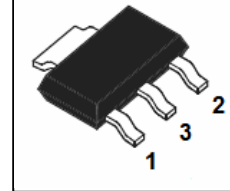
- Glass-passivated mesa chip for high reliability and uniform
- Low on-state voltage and High I_{TSM}
- RoHS products

封装 Package



序号 Pin	引线名称 Description
1	主电极 1 MT1
2	门极 G
3	主电极 2 MT2

SOT-223



订货信息 ORDER MESSAGES

订货型号 Order code	印记 Marking	封装 Package	包装 Packaging
3FT1N2-O-N-B-A	3FT1N2	SOT-223	编带 Tape

绝对最大额定值 ABSOLUTE RATINGS ($T_c=25^\circ\text{C}$)

项 目 Parameter	符 号 Symbol	试 验 条 件 Condition	数 值 Value	单 位 Unit
重复峰值断态电压 Repetitive peak off-state voltage	V_{DRM}		± 800	V
通态方均根电流 On-state RMS current	$I_{\text{T(RMS)}}$	full sine wave	1.0	A
非重复浪涌峰值通态电流 Non-repetitive surge peak on-state current	I_{TSM}	full sine wave , $t=20\text{ms}$	10	A
		full sine wave , $t=16.7\text{ms}$	11	A
	I^2t	$t=10\text{ms}$	0.5	A^2s
通态电流临界上升率 Repetitive rate of rise of on-state current after triggering	di/dt	MT1(-),MT2(+),G(+); MT1(-),MT2(+),G(-); MT1(+),MT2(-),G(-)	50	$\text{A}/\mu\text{s}$
峰值门极电流 Peak gate current	I_{GM}		2	A
平均门极功率 Average gate power	$P_{\text{G(AV)}}$		0.5	W
存储温度 Storage temperature	T_{stg}		-40~150	$^\circ\text{C}$
操作结温 Operation junction temperature	T_{VJ}		125	$^\circ\text{C}$



电特性 ELECTRICAL CHARACTERISTIC ($T_c=25^\circ\text{C}$)

项 目 Parameter	符 号 Symbol	测 试 条 件 Condition	最小 Min	典型 Typ	最大 Max	单位 Unit
峰值重复断态电流 Peak Repetitive Blocking Current	I_{DRM}	$V_{\text{DM}}=V_{\text{DRM}}$, $T_j=125^\circ\text{C}$, gate open	-	-	0.5	mA
峰值通态电压 Peak on-state voltage	V_{TM}	$I_{\text{TM}}=2\text{A}$	-	-	1.5	V
门极触发电流 Gate trigger current	I_{GT}	$V_{\text{DM}}=12\text{V}$, $R_L=100\ \Omega$	-	-	10	MT1(-),MT2(+),G(+)
						MT1(-),MT2(+),G(-)
						MT1(+),MT2(-),G(-)
		MT1(+),MT2(-),G(+)	-	-	25	mA
门极触发电压 Gate trigger voltage	V_{GT}	$V_{\text{DM}}=12\text{V}$, $R_L=100\ \Omega$	-	-	1.5	V
维持电流 Holding current	I_{H}	$V_{\text{DM}}=12\text{V}$, $I_{\text{GT}}=0.1\text{A}$	-	-	15	mA
擎住电流 Latching current	I_{L}	$V_{\text{DM}}=12\text{V}$, $I_{\text{GT}}=0.1\text{A}$	-	-	20	mA
断态临界电压上升率 Rise of off- state voltage	dV/dt	$V_{\text{DM}}=67\% V_{\text{DRM(MAX)}}$, $T_j=125^\circ\text{C}$, gate open	-	30	-	V/ μs

热特性 THERMAL CHARACTERISTIC

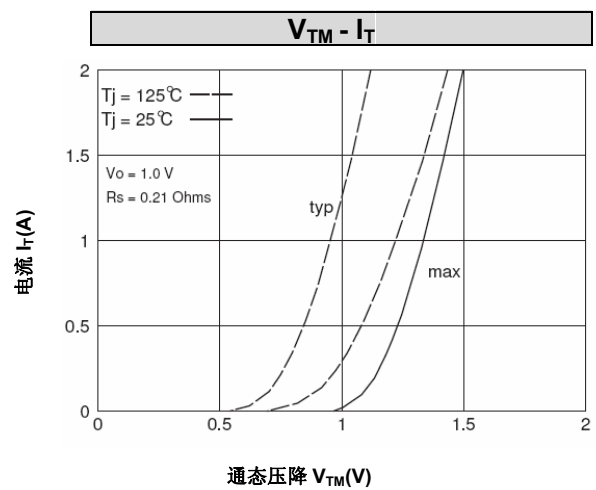
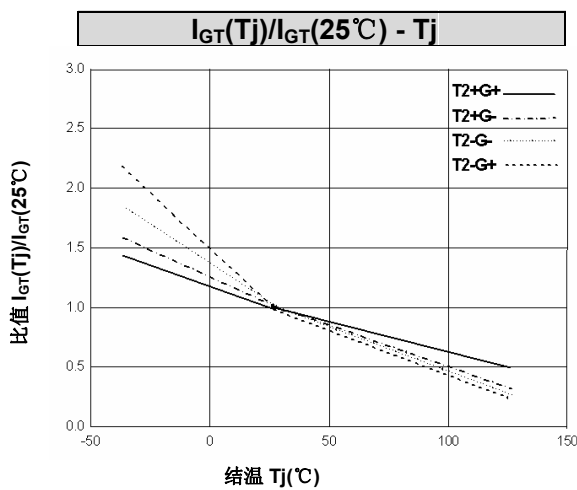
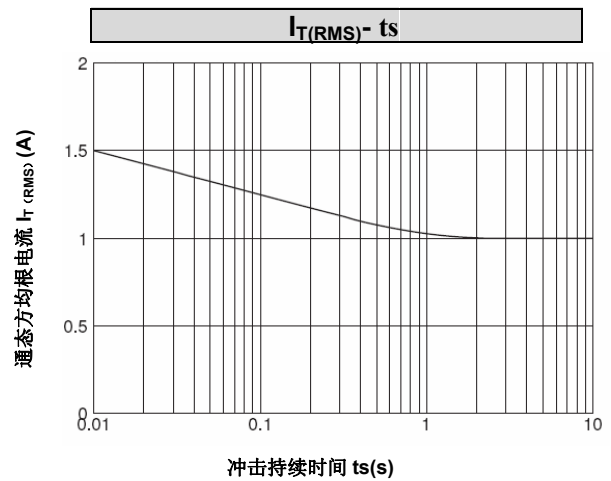
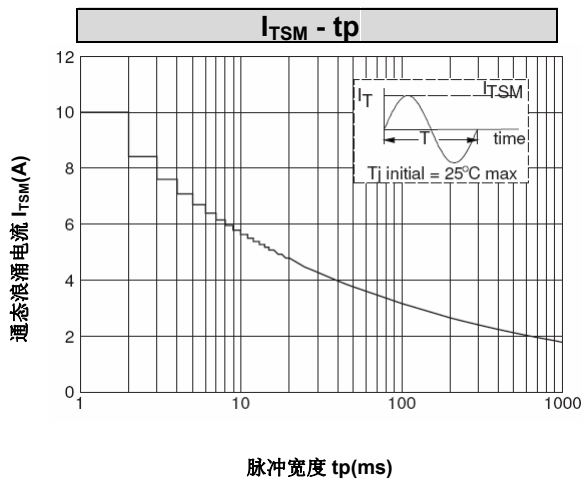
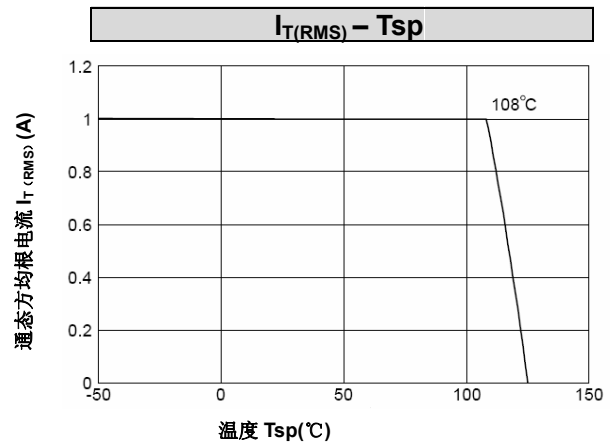
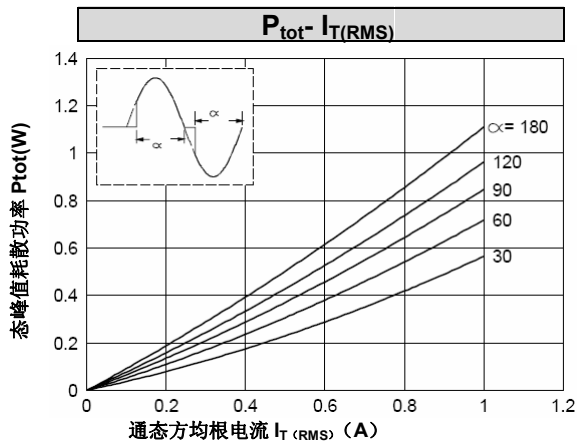
项 目 Parameter	符 号 Symbol	条 件 Condition	最小 Min	典型 Typ	最大 Max	单位 Unit
结到引线的热阻 Thermal resistance junction to solder point	$R_{\text{th(j-sp)}}$	full cycle (SOT-223)	-	-	15	$^\circ\text{C/W}$

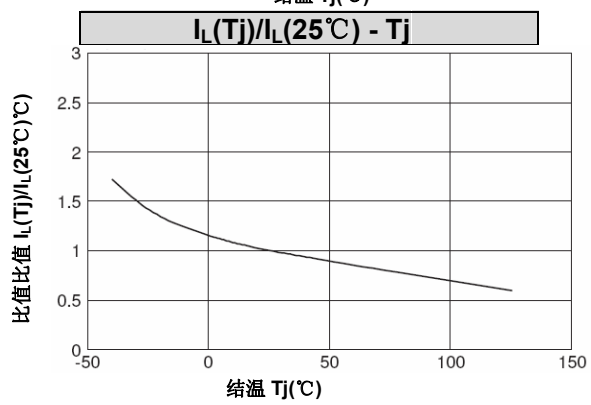
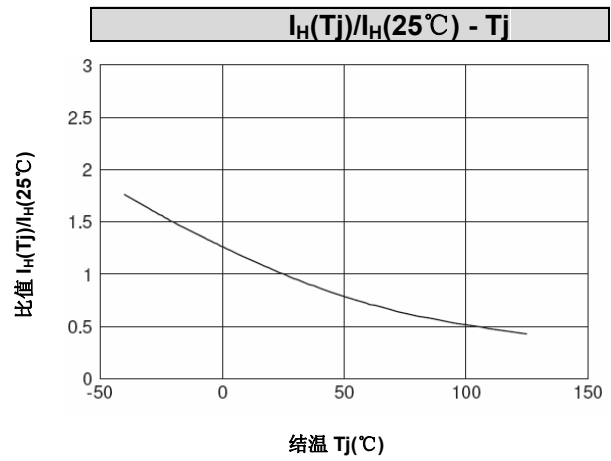
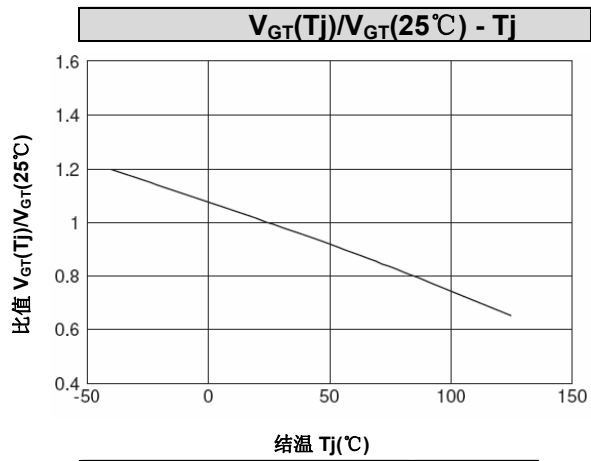




特征曲线 ELECTRICAL CHARACTERISTICS (curves)

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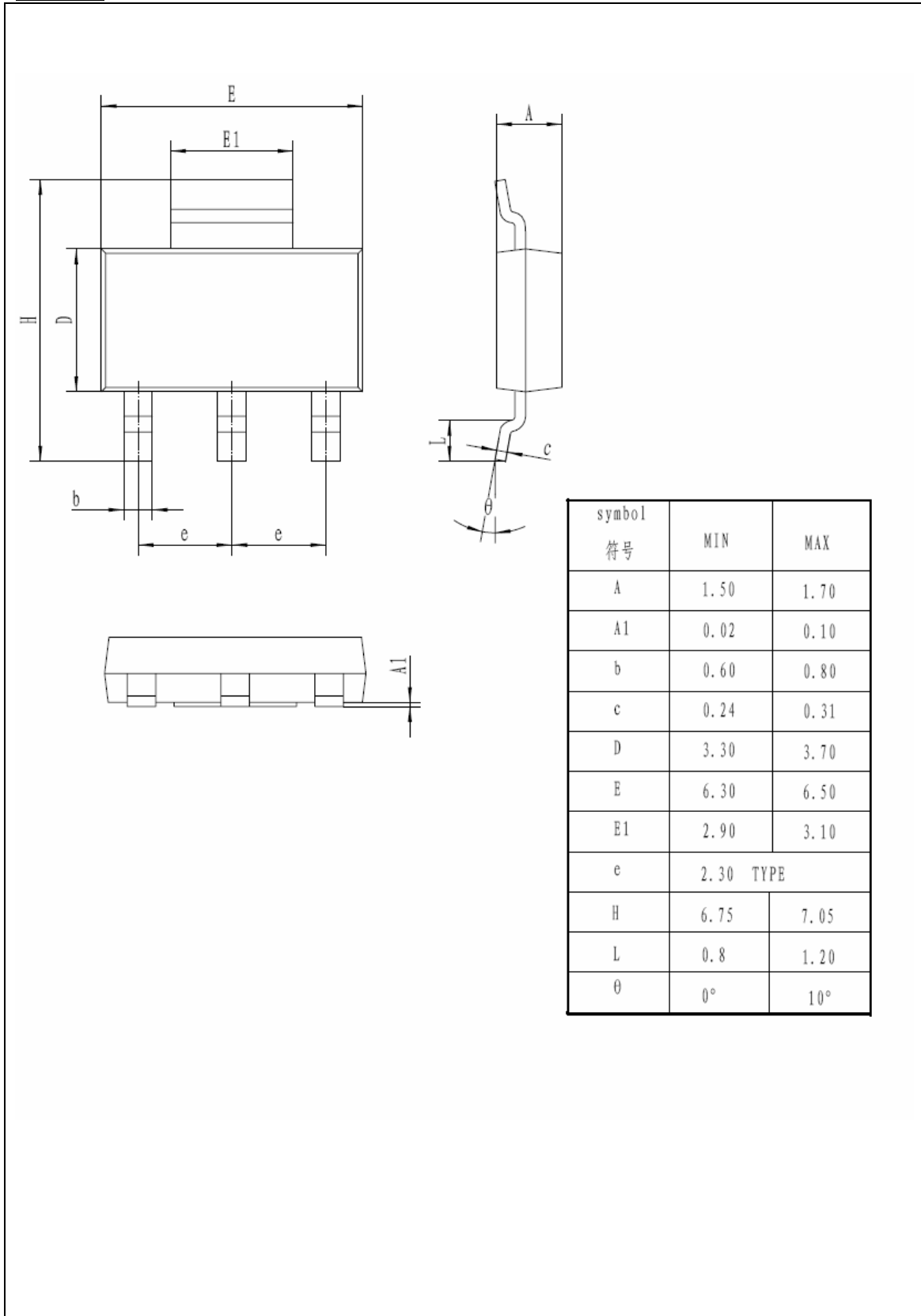


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外形尺寸 PACKAGE MECHANICAL DATA

SOT-223

单位Unit : mm



**注意事项**

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3. 在电路设计时请不要超过器件的绝对最大额定值，否则会影响整机的可靠性。
4. 本说明书如有版本变更不另外告知

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3. Please do not exceed the absolute maximum ratings of the device when circuit designing.
4. Jilin Sino-microelectronics co., Ltd reserves the right to make changes in this specification sheet and is subject to change without prior notice.

联系方式**吉林华微电子股份有限公司**

公司地址：吉林省吉林市深圳街 99 号

邮编：132013

总机：86-432-64678411

传真：86-432-64665812

网址：www.hwdz.com.cn**销售业务部**

地址：吉林省吉林市深圳街 99 号

邮编：132013

电话：86-432-64675588

64675688

64678411

传真：86-432-64671533

CONTACT**JILIN SINO-MICROELECTRONICS CO., LTD.**ADD: No.99 Shenzhen Street, Jilin City,
Jilin Province, China.

Post Code: 132013

Tel: 86-432-64678411

Fax: 86-432-64665812

Web Site: www.hwdz.com.cn**MARKET DEPARTMENT**ADD: No.99 Shenzhen Street, Jilin City, Jilin
Province, China.

Post Code: 132013

Tel: 86-432-64675588

64675688

64678411

Fax: 86-432-64671533

附录 (Appendix)：修订记录 (Revision History)

日期 Date	旧版本 Last Rev.	新版本 New Rev.	修订内容 Description of Changes
2015-3-27	201412A	201503B	增加电参数曲线图

