

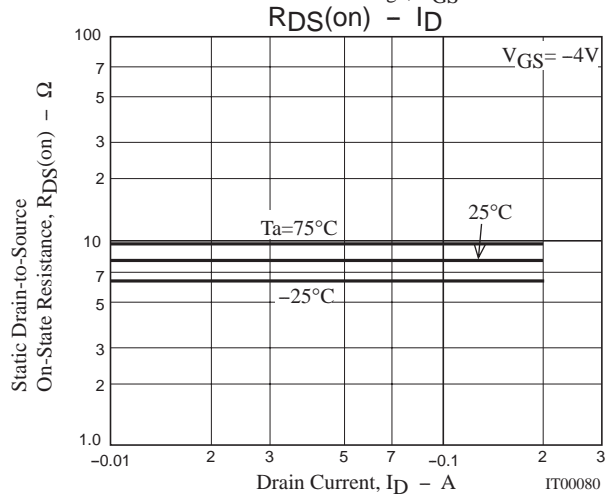
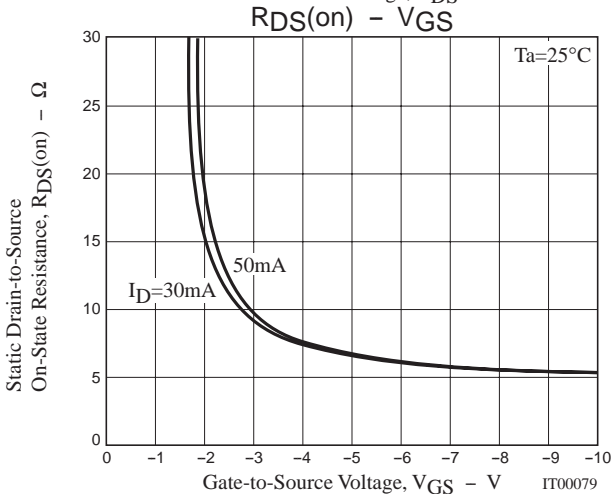
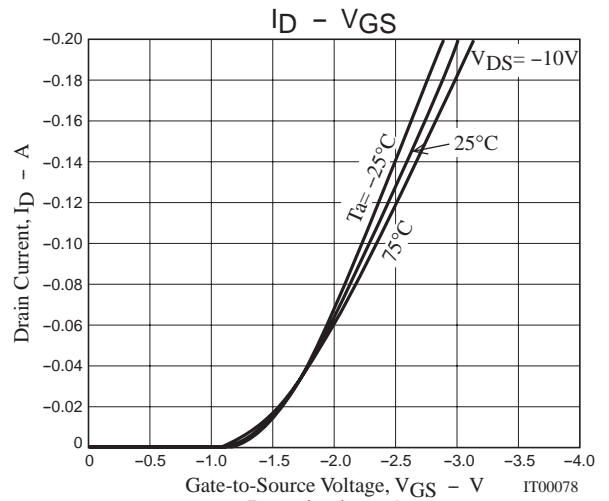
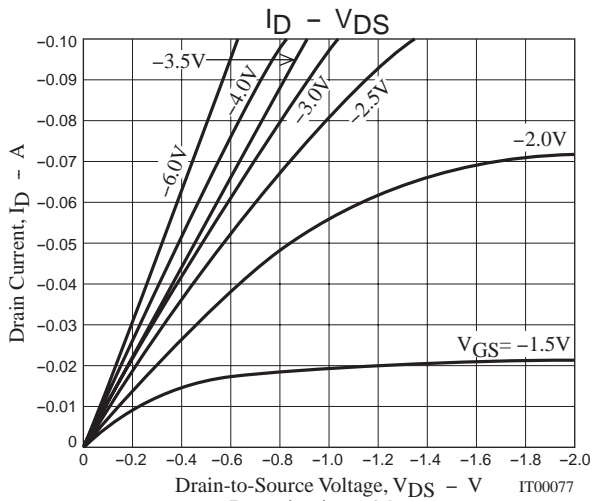
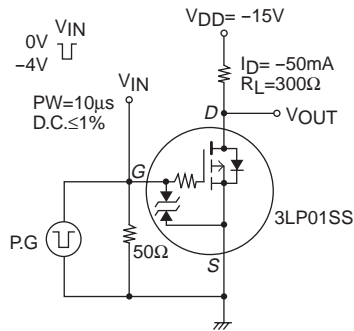
3LP01SS

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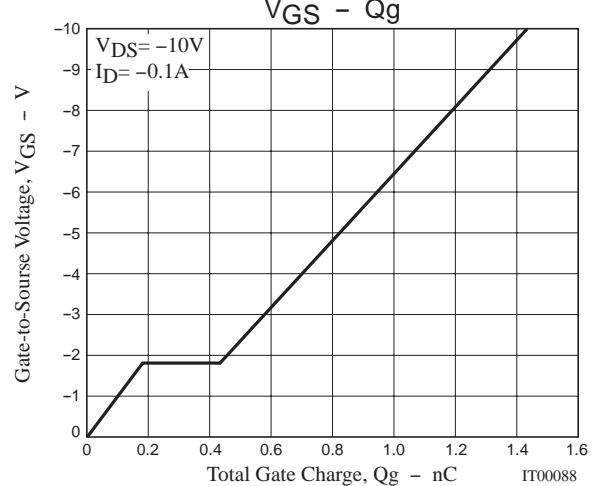
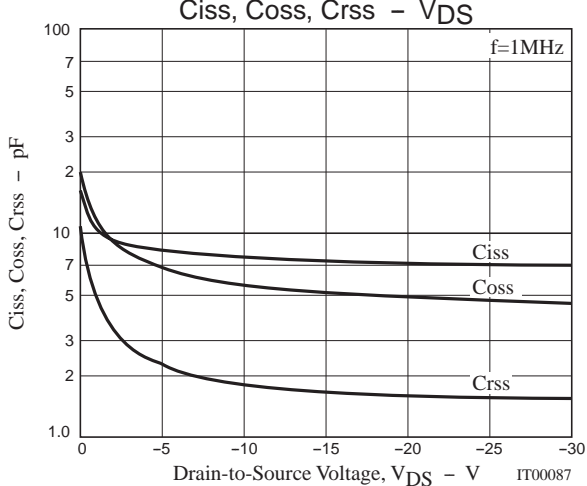
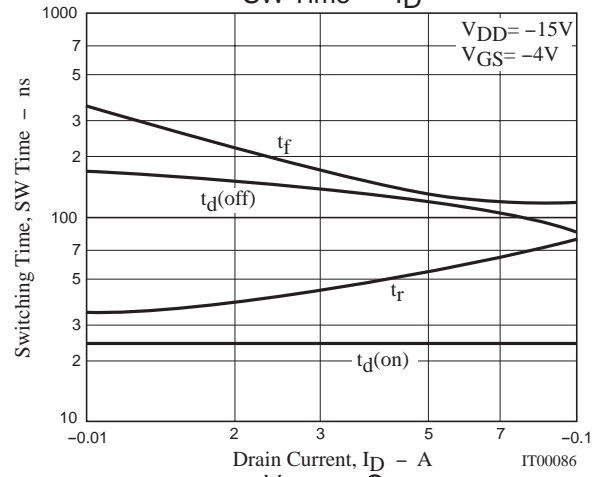
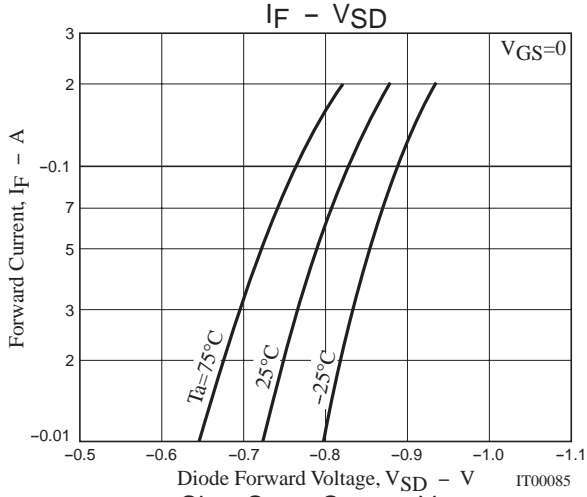
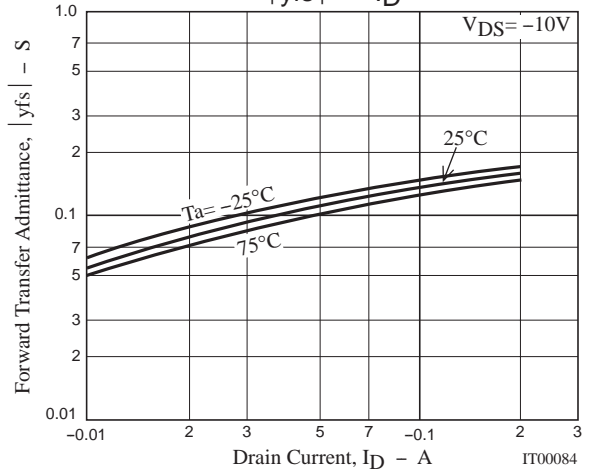
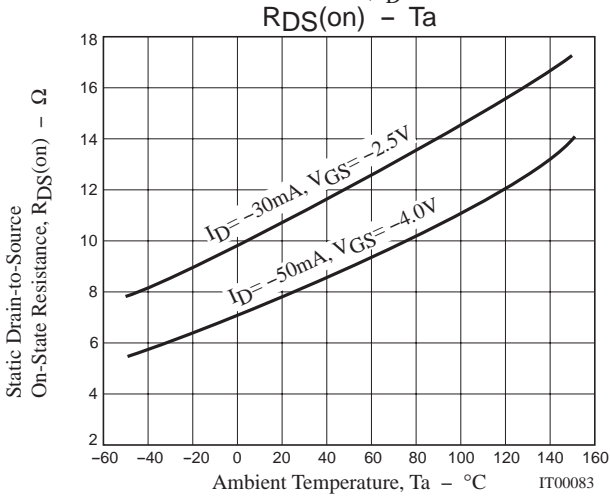
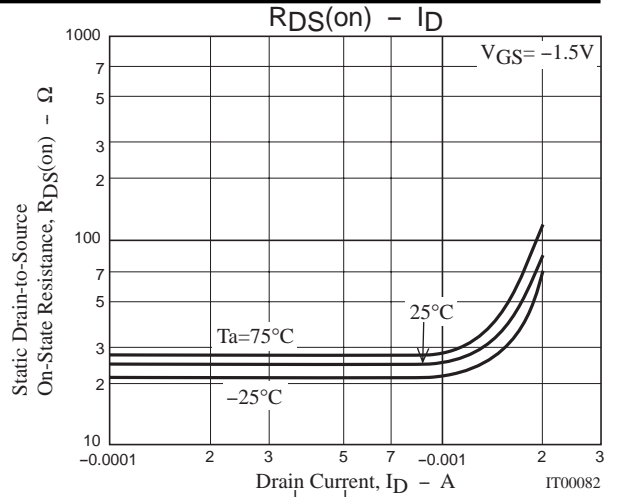
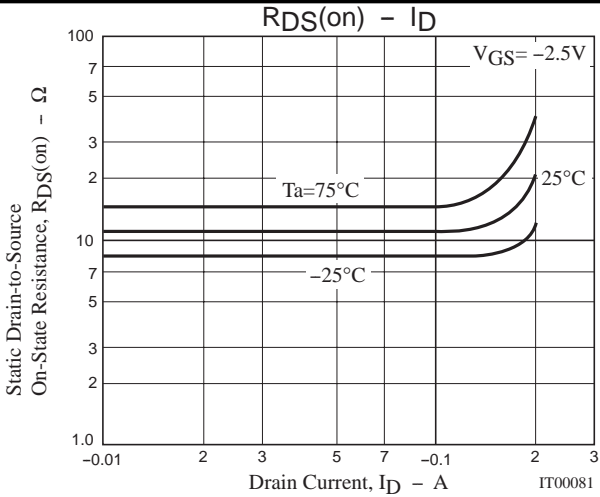
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Static Drain-to-Source On-State Resistance	$R_{DS(on)1}$	$I_D = -50\text{mA}, V_{GS} = -4\text{V}$		8	10.4	Ω
	$R_{DS(on)2}$	$I_D = -30\text{mA}, V_{GS} = -2.5\text{V}$		11	15.4	Ω
	$R_{DS(on)3}$	$I_D = -1\text{mA}, V_{GS} = -1.5\text{V}$		27	54	Ω
Input Capacitance	C_{iss}	$V_{DS} = -10\text{V}, f = 1\text{MHz}$		7.5		pF
Output Capacitance	C_{oss}	$V_{DS} = -10\text{V}, f = 1\text{MHz}$		5.7		pF
Reverse Transfer Capacitance	C_{rss}	$V_{DS} = -10\text{V}, f = 1\text{MHz}$		1.8		pF
Turn-ON Delay Time	$t_d(on)$	See specified Test Circuit		24		ns
Rise Time	t_r	See specified Test Circuit		55		ns
Turn-OFF Delay Time	$t_d(off)$	See specified Test Circuit		120		ns
Fall Time	t_f	See specified Test Circuit		130		ns
Total Gate Charge	Q_g	$V_{DS} = -10\text{V}, V_{GS} = -10\text{V}, I_D = -100\text{mA}$		1.43		nC
Gate-to-Source Charge	Q_{gs}	$V_{DS} = -10\text{V}, V_{GS} = -10\text{V}, I_D = -100\text{mA}$		0.18		nC
Gate-to-Drain "Miller" Charge	Q_{gd}	$V_{DS} = -10\text{V}, V_{GS} = -10\text{V}, I_D = -100\text{mA}$		0.25		nC
Diode Forward Voltage	V_{SD}	$I_S = -100\text{mA}, V_{GS} = 0$		0.83	1.2	V

Marking : XA

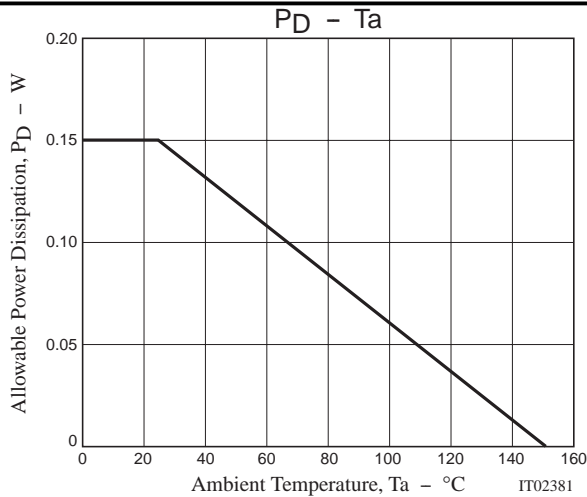
Switching Time Test Circuit



3LP01SS



3LP01SS



Note on usage : Since the 3LP01SS is designed for high-speed switching applications, please avoid using this device in the vicinity of highly charged objects.

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