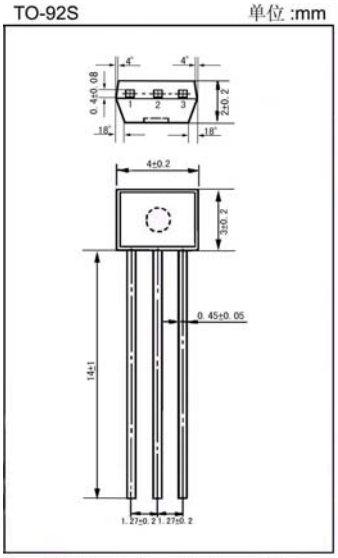
用途:用于开关、反相电路、界面电路以及驱动电路中。

Purpose: Switching, inverter circuit, interface circuit and driver circuit applications.

特点:内装偏置电阻，简化线路设计，减少元件和制造流程。

Features: With built-in bias resistors, simplify circuit design, reduce a quantity of parts and manufacturing process.



引脚 :1.E　2.C　3.B

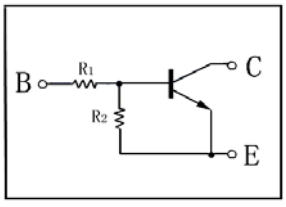
极限参数/Absolute maximum ratings(Ta=25℃)

参数符号 Symbol	数值 Rating	单位 Unit
V_{CBO}	50	V
V_{CEO}	50	V
V_{EBO}	10	V
I_C	100	mA
P_C	300	mW
T_j	150	℃
T_{stg}	-55～150	℃

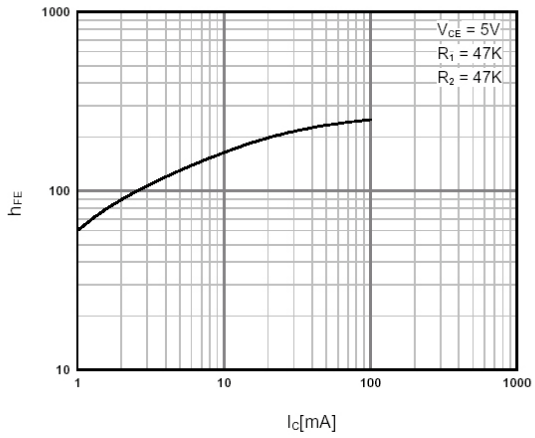
电性能参数/Electrical characteristics(Ta=25℃)

参数符号 Symbol	测试条件 Test condition	数值 Rating			单位 Unit
		最小值 Min	典型值 Typ	最大值 Max	
V_{CBO}	I_C=10μA　I_E=0	50			V
V_{CEO}	I_C=100μA　I_B=0	50			V
I_{CBO}	V_{CB}=40V　I_E=0			0.1	μA
h_{FE}	V_{CE}=5.0V　I_C=5.0mA	68			
$V_{CE(sat)}$	I_C=10mA　I_B=0.5mA			0.3	V
f_T	V_{CE}=5.0mA　I_C=10V		250		MHz
C_{ob}	V_{CB}=10V　I_E=0　f=1.0MHz		3.7		pF
$V_{I(off)}$	V_{CE}=5.0V　I_C=100μA	0.5			V
$V_{I(on)}$	V_{CE}=0.3V　I_C=5.0mA			3.0	V
R_1		32	47	62	KΩ
R_1/R_2		0.9	1.0	1.1	

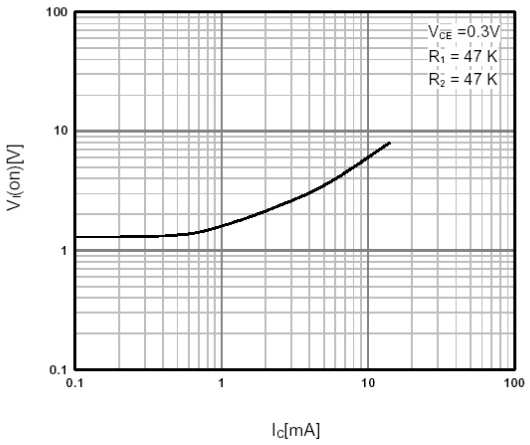
等效电路图



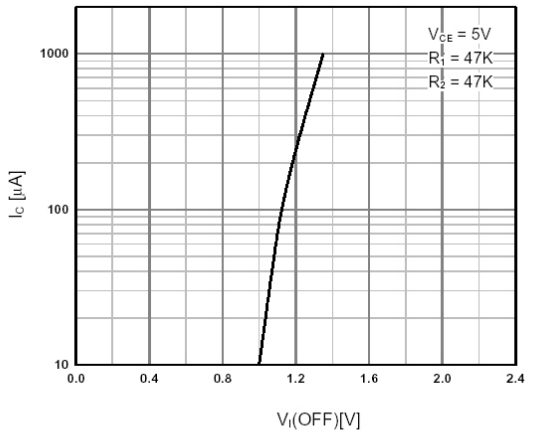
h_{FE} – I_C



$V_{I(on)}$ – I_c



I_c – $V_{I(OFF)}$



P_c – T_a