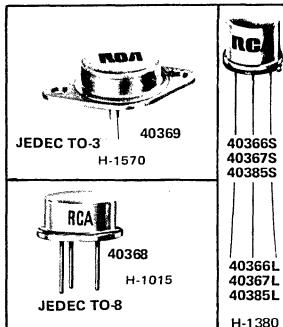




Power Transistors

**40366-40369
40385**



High-Reliability Silicon N-P-N Power Transistors

For Power Switching and Amplifier Applications

Features

- High reliability assured by five preconditioning steps
- Group A test data included*
- Transistors utilize JEDEC hermetic packages;

40369 — TO-3

40368 — TO-8

40366, 40367 { See Note at right
40385 }

These devices are available with either 1½-inch leads (TO-3 package) or ½-inch leads (TO-8 package). The longer-lead versions are specified by suffix "L" after the type number; the shorter-lead versions are specified by suffix "S" after the type number.

RCA-40366—40369 and 40385 are silicon n-p-n power transistors derived from JEDEC types 2N2102, 2N1482, 2N1486, 2N1490, and 2N3439. They are specially preconditioned for use in power-switching and amplifier applications in those instances where high reliability is a requisite.

■ High voltage ratings:

V_{CER} = 80 V max. (40366)

V_{CEV} = 100 V max. (40367, 40368 & 40369)

V_{CEO} = 350 V max. (40385)

■ High power-dissipation capability:

P_T = 5 W max. (40366, 40367 & 40385)

= 25 W max. (40368)

= 75 W max. (40369)

* Group A test data shown on pages 2 & 3.

MAXIMUM RATINGS, Absolute-Maximum Values:

	40366	40367	40368	40369	40385	
COLLECTOR-TO-BASE VOLTAGE	V_{CBO}	120	100	100	100	V
COLLECTOR-TO-EMITTER VOLTAGE:						
With external base-to-emitter resistance						
(R_{BE}) \leq 10 Ω	V_{CER}	80	—	—	—	V
With -1.5 V (V_{BE}) of reverse bias	V_{CEV}	—	100	100	100	V
With base open	V_{CEO}	65	55	55	55	V
EMITTER-TO-BASE VOLTAGE	V_{EBO}	7	12	12	10	V
CONTINUOUS COLLECTOR CURRENT	I_C	1	1.5	3	6	A
CONTINUOUS BASE CURRENT	I_B	—	1	1.5	3	A
TRANSISTOR DISSIPATION:	P_T					
At case temperature up to 25°C	5	5	25	75	10	W
At free-air temperature up to 25°C	1	1	—	—	1	W
At temperatures above 25°C				← Derate linearly to 0 watts at 200°C →		
TEMPERATURE RANGE:				← —65 to 200 — →		$^\circ\text{C}$
Storage & Operating (Junction)						
PIN OR LEAD TEMPERATURE (During soldering):						
At distances \geq 1/32 in. (0.79 mm) from seating plane for 10 s max.	255	255	235	235	255	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS, At Case Temperature (T_C) = 25°C

Characteristic	Symbol	TEST CONDITIONS						LIMITS								Units	
		Voltage V dc			Current mA dc		40366		40367		40368		40369		40385		
		V _{CB}	V _{CE}	V _{EB}	I _C	I _B	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	
Collector-Cutoff Current	I _{CHO}	30 60					— —	— 2.0	— —	4.0	— —	9.0	— —	10	— —	— —	μA nA
	I _{CEO}		300			0	—	—	—	—	—	—	—	—	—	20	μA
	I _{CEV}		450	1.5			—	—	—	—	—	—	—	—	—	500	μA
Emitter-Cutoff Current	I _{EBO}			5 6	0 0		—	5.0	—	—	—	—	—	—	—	—	nA μA
				10	0		—	—	—	—	—	—	—	—	—	—	20 μA
				12	0		—	—	—	2.0	—	5.0	—	—	—	—	μA
DC Forward-Current Transfer Ratio	h _{FE}	4	200				—	—	35	100	—	—	—	—	—	—	
		4	750				—	—	—	—	35	100	—	—	—	—	
		4	1500				—	—	—	—	—	—	25	75	—	—	
		10	0.01				10	—	—	—	—	—	—	—	—	—	
		10	0.1				20	—	—	—	—	—	—	—	—	—	
		10	2				—	—	—	—	—	—	—	—	30	—	
		10	20				—	—	—	—	—	—	—	—	40	160	
		10	150*				40	120	—	—	—	—	—	—	—	—	
		10	500*				25	—	—	—	—	—	—	—	—	—	
		10	1000*				10	—	—	—	—	—	—	—	—	—	
Collector-to-Base Breakdown Voltage	B _V _{CBV}		1.5	0.1			120	—	—	—	—	—	—	—	—	—	V
Collector-to-Emitter Breakdown Voltage	B _V _{CEV}		1.5	0.25			—	—	100	—	100	—	100	—	—	—	V
Emitter-to-Base Breakdown Voltage (I _E = 0.1 mA)	B _V _{EBO}						7.0	—	—	—	—	—	—	—	—	—	V
Collector-to-Emitter Sustaining Voltage: With external base-to-emitter resistance (R _{BE}) = 10 Ω With base open	V _{CER(sus)}				100*		80	—	—	—	—	—	—	—	—	—	.V
	V _{CEO(sus)}				50	0	—	—	55	—	—	—	—	—	350	—	V
					100*	0	65	—	—	—	—	—	—	—	—	—	
Collector-to-Emitter Saturation Voltage	V _{CE(sat)}				100	0	—	—	—	—	55	—	55	—	350	—	
					50	4	—	—	—	—	—	—	—	—	—	0.5	
					150*	15	—	0.5	—	—	—	—	—	—	—	—	
					200	10	—	—	—	1.4	—	—	—	—	—	—	V
Base-to-Emitter Saturation Voltage	V _{BE(sat)}				750	40	—	—	—	—	—	0.75	—	—	—	—	
					1300	100	—	—	—	—	—	—	1.0	—	—	—	
					150*	15	—	1.1	—	—	—	—	—	—	—	1.3	V
Base-to-Emitter Voltage	V _{BE}	4	200				—	—	—	—	3.0	—	—	—	—	—	
		4	750				—	—	—	—	—	2.5	—	—	—	—	V
		4	1500				—	—	—	—	—	2.5	—	—	—	—	

* Pulsed; pulse duration = 300 μs, duty factor = 1.8%.

GROUP-A TESTS (IN ACCORDANCE WITH MIL-S-19500)

TEST METHOD PER MIL-STD-750	EXAMINATION OR TEST	CONDITIONS	LTPD*	LIMITS										UNITS	
				40366		40367		40368		40369		40385			
				Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.		
2071	Subgroup 1 Visual and Mechanical Examination	—	10	—	—	—	—	—	—	—	—	—	—	—	
3036D	Subgroup 2 ICBO	$V_{CB} = 30V, I_E = 0$ $V_{CB} = 60V, I_E = 0$	5	—	—	—	4.0	—	9.0	—	10	—	—	μA nA	
3041A	I _{CEV}	$V_{CE} = 450V$, $V_{BE} = -1.5V$	—	—	—	—	—	—	—	—	—	—	500	μA	
3041D	I _{CEO}	$V_{CE} = 300V$, $I_E = 0$	—	—	—	—	—	—	—	—	—	—	20	μA	
3061D	I _{EBO}	$V_{EB} = 5V, I_C = 0$	—	—	5.0	—	—	—	—	—	—	—	—	nA	
		$V_{EB} = 6V, I_C = 0$	—	—	—	—	—	—	—	—	—	—	20	μA	
		$V_{EB} = 10V, I_C = 0$	—	—	—	—	—	—	—	—	6.0	—	—	μA	
		$V_{EB} = 12V, I_C = 0$	—	—	—	2.0	—	5.0	—	—	—	—	—	μA	
3001A	BVCBV	$I_C = 100\mu A$, $V_{EB} = 1.5V$	—	120	—	—	—	—	—	—	—	—	—	v	
3026D	BVEBO	$I_E = 100\mu A, I_C = 0$	—	7.0	—	—	—	—	—	—	—	—	—	v	
3011A	BVCEV	$I_C = 0.25mA$, $V_{EB} \approx 1.5V$	—	—	100	—	100	—	—	—	—	—	—	v	
		$I_C = 0.5mA$, $V_{EB} = 1.5V$	—	—	—	—	—	—	100	—	—	—	—	v	
		$I_C = 50mA, I_B = 0$ $I_C = 100mA^a, I_B = 0$ $I_C = 100mA, I_B = 0$	—	—	55	—	—	—	—	—	350	—	—	v	
3011D	VCEO(sus)	$I_C = 50mA, I_B = 0$ $I_C = 100mA^a, I_B = 0$ $I_C = 100mA, I_B = 0$	—	65	—	—	—	—	—	—	—	—	—	v	
		$I_C = 100mA^a$, $R_{BE} = 10\Omega$	—	80	—	—	—	—	—	—	—	—	—	v	
		$I_C = 50mA$, $I_B = 4mA$	—	—	—	—	—	—	—	—	—	—	0.5	v	
3071	VCE(sat)	Subgroup 3 $I_C = 150mA^a$, $I_B = 15mA$	5	—	0.5	—	—	—	—	—	—	—	—	v	
		$I_C = 200mA$, $I_B = 10mA$	—	—	—	—	1.4	—	—	—	—	—	—	v	
		$I_C = 750mA$, $I_B = 40mA$	—	—	—	—	—	0.75	—	—	—	—	—	v	
		$I_C = 1.5A$, $I_B = 100mA$	—	—	—	—	—	—	—	1.0	—	—	—	v	
		$I_C = 50mA$, $I_B = 4mA$	—	—	—	—	—	—	—	—	—	—	1.3	v	
3066A	V _{BE} (sat)	$I_C = 150mA^a$, $I_B = 15mA$	—	—	—	—	—	—	—	—	—	—	—	v	
		$I_C = 200mA$, $V_{CE} = 4V$	—	—	1.1	—	—	—	—	—	—	—	—	v	
3066A	V _{BE}	$I_C = 750mA$, $V_{CE} = 4V$	—	—	—	—	3.0	—	—	2.5	—	—	—	v	

GROUP-A TESTS (CONT.)

TEST METHOD PER MIL-STD-750	EXAMINATION OR TEST	CONDITIONS	LTPD ¹	LIMITS								UNITS	
				40366		40367		40368		40369			
				Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.		
3076	hFE	$I_C = 0.01\text{ mA}$, $V_{CE} = 10\text{ V}$	-	10	-	-	-	-	-	-	-	-	
		$I_C = 0.1\text{ mA}$, $V_{CE} = 10\text{ V}$	-	20	-	-	-	-	-	-	-	-	
		$I_C = 2\text{ mA}$, $V_{CE} = 10\text{ V}$	-	-	-	-	-	-	-	-	30	-	
		$I_C = 20\text{ mA}$, $V_{CE} = 10\text{ V}$	-	-	-	-	-	-	-	-	40	60	
		$I_C = 150\text{ mA}^*$, $V_{CE} = 10\text{ V}$	-	40	120	-	-	-	-	-	-	-	
		$I_C = 200\text{ mA}$, $V_{CE} = 4\text{ V}$	-	-	-	35	100	-	-	-	-	-	
		$I_C = 500\text{ mA}^*$, $V_{CE} = 10\text{ V}$	-	25	-	-	-	-	-	-	-	-	
		$I_C = 750\text{ mA}$, $V_{CE} = 4\text{ V}$	-	-	-	-	-	35	100	-	-	-	
		$I_C = 1\text{ A}^*$, $V_{CE} = 10\text{ V}$	-	10	-	-	-	-	-	-	-	-	
		$I_C = 1.5\text{ A}$, $V_{CE} = 4\text{ V}$	-	-	-	-	-	-	-	25	75	-	

•Pulsed; pulse duration = $300\mu\text{s}$, duty factor = 1.8%.

*Lot tolerance per cent defective.

The RCA-40366, 40367, 40368, 40369, and 40385 are high-reliability versions of the RCA-2N2102, 2N1482, 2N1486, 2N1490 and 2N3439*, respectively. These transistors are intended for medium- and high-power switching and amplifier applications in military and industrial equipment.

The 40366 and 40385 are silicon n-p-n types with a power-dissipation capability of 5 watts each. The 40367 is a silicon n-p-n hometaxial type with a power-dissipation capability of 5 watts. These devices are available with either 1½-inch leads (TO-5 package) or ½-inch leads (TO-39 package).

The 40368 is a silicon n-p-n hometaxial type in a JEDEC TO-8 package with a power-dissipation capability of 25 watts.

The 40369 is a silicon n-p-n hometaxial type in the popular JEDEC TO-3 package and has a dissipation capability of 75 watts.

The 40366, the high-reliability version of the 2N2102, features linear beta characteristics which are controlled over a wide range of collector currents (0.01 mA to 1 A).

The 40367, 40368, and 40369, the high-reliability versions of the 2N1482, 2N1486, and 2N1490, respectively, feature rugged construction, low saturation voltage, and high beta at high currents, and are designed to assure freedom from forward-bias second breakdown when operated with specified limits.

Typical applications for these transistors include: power-switching circuits such as dc-to-dc converters, inverters, choppers, solenoid- and relay-controls; oscillator, regulator, and pulse-amplifier circuits; Class A and Class B push-pull audio- and servo-amplifiers.

* Complete data for types 2N1482, 2N1486, 2N1490, 2N2102 and 2N3439 are given in separate technical bulletins (Files 135, 137, 139, 106, and 64, respectively). Bulletins are available upon request from RCA Solid State Division, Box 3200, Somerville, N.J. 08876.

RELIABILITY TESTING

Each RCA-40366, 40367, 40368, 40369 and 40385 is subjected to the following preconditioning steps:

1. Temperature Cycling-Method 102A of MIL-STD-202, 5 cycles, -65°C to 200°C
2. Bake, 72 hours min., 200°C
3. Helium Leak, 1×10^{-8} cc/s max.
4. (a) Methanol Bomb, 70 psig, 16 hours min. (For 40366)
 (b) Bubble Test (Per MIL-STD-202, COND. A), 125°C min.,
 1 minute, ethylene glycol (For 40367, 40368, 40369 & 40385)
5. Serialization
6. (a) Record I_{CBO} and h_{FE} (150 mA) (For 40366)
 (b) Record I_{CBO} and h_{FE} (For 40367, 40368, & 40369)
 (c) Record I_{CEV} and h_{FE} (20 mA) (For 40385)
7. (a) Power Age, $T_{\text{FA}} = 25^{\circ}\text{C}$, $V_{\text{CB}} = 60\text{ V}$, $t = 168$ hours,
 $P_T = 1\text{ W}$, free-air (For 40366 & 40367)
 (b) Power Age, $T_C = 125^{\circ}\text{C}$, $V_{\text{CB}} = 24\text{ V}$, $t = 168$ hours,
 $P_T = 10.5\text{ W}$, with heat-sink (For 40368)
 $P_T = 32\text{ W}$, with heat-sink (For 40369)
 (c) Power Age, $T_{\text{FA}} = 25^{\circ}\text{C}$, $V_{\text{CB}} = 200\text{ V}$, $t = 168$ hours,
 $P_T = 800\text{ mW}$, free air (For 40385)
8. (a) For 40366, \dagger record I_{CBO} , h_{FE} (150 mA), BV_{CBV} , $V_{\text{CEO}}(\text{sus})$, BV_{EBO} ,
 $V_{\text{CE}}(\text{sat})$. Data furnished with transistor.
 (b) For 40367, 40368, & 40369, \dagger record I_{CBO} , h_{FE} , BV_{CEV} , $V_{\text{CEO}}(\text{sus})$, I_{EBO} ,
 $V_{\text{CE}}(\text{sat})$. Data furnished with transistors.
 (c) For 40385, \dagger record I_{CEO} , I_{EBO} , $V_{\text{CEO}}(\text{sus})$, I_{CEV} , $V_{\text{CE}}(\text{sat})$, and h_{FE} (20 mA).
 Data furnished with transistor.

\dagger Delta criteria after 168 hours Power Age:

$$\Delta h_{\text{FE}} \pm 25\% \text{ (For all types)} \quad \Delta I_{\text{CBO}} + 1\text{ }\mu\text{A} \text{ (For 40367, 40368, & 40369)}$$

TERMINAL CONNECTIONS

FOR 40366, 40367,
AND 40385

Pin 1 – Emitter
 Pin 2 – Base
 Case, Pin 3 – Collector

TERMINAL CONNECTIONS
FOR 40368

Lead 1 – Emitter
 Lead 2 – Base
 Case, Lead 3 – Collector

TERMINAL CONNECTIONS
FOR 40369

Pin 1 – Base
 Pin 2 – Emitter
 Case – Collector
 Mounting Flange – Collector