RF Transistors

40915



Solid State Division

0.2-to-1.4-GHz Low-Noise Silicon N-P-N Transistor

For High-Gain Small-Signal Applications

Features:

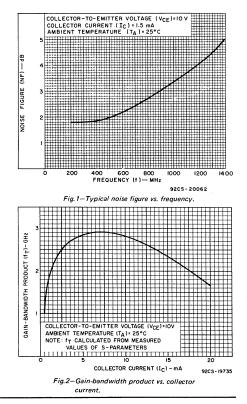


RCA-40915^{*} is an epitaxial silicon n-p-n planar transistor intended for low-power, small-signal applications where both low noise and high gain are desirable. It utilizes a hermetically sealed four-lead JEDEC TO-72 package. All of the elements of the transistor are insulated from the case, which may be grounded by means of the fourth lead.

*Formerly RCA Dev. No. TA8104.

MAXIMUM RATINGS, Absolute-Maximum Values:

Collector-to-Base Voltage	V _{CBO}	35	v	
Collector-to-Emitter Voltage	V _{CEO}	15	v	
Emitter-to-Base Voltage	V _{EBO}	3.5	v	
Collector Current (Continuous)	lC	40	mΑ	
Transistor Dissipation:	Рт			
At ambient temperatures up				
to 25°C		200	mW	
At ambient temperatures above				
25°C	De	rate lir	early	
	at 1.14 mW/°C			
Temperature Range:				
Storage and Operating				
(Junction)	-65 to +200 °C			
N				



ELECTRICAL CHARACTERISTICS at Ambient Temperature $(T_{\Delta}) = 25^{\circ}C$

ELECTRICAL CHARACTERISTICS at Allibert Temperature (TA) = 20 C										
	SYMBOL	TEST CONDITIONS								
CHARACTERISTIC		DC COLLECTOR VOLTAGE (V)		DC CURRENT (mA)		LIMITS		UNITS		
		v _{CB}	V _{CE}	۱E	1 _B	۱c	MIN.	MAX.		
STATIC										
Collector Cutoff Current	I _{СВО}	10		0			-	20	nA	
Collector-to-Base Breakdown Voltage	V _{(BR)CBO}			0		0.01	35	-	v	
Collector-to-Emitter Breakdown Voltage	V _{(BR)CEO}				0	0.1	15	-	v	
Emitter-to-Base Breakdown Voltage	V _{(BR)EBO}			0.01		0	3.5	-	V	
DC Forward-Current Transfer Ratio	^h FE		10			3	20	-	-	
Thermal Resistance: (Junction-to-Ambient)	R _{∉JA}						-	880	°C/W	
DYNAMIC										
Device Noise Figure (f = 450 MHz)	NF		10			1.5	-	2.5	dB	
Small-Signal Common-Emitter Power Gain (f = 450 MHz) Unneutralized Amplifier	G _{PE}		10			1.5	14	-	dB	
At minimum noise figure	G _{PE}		10			1.5	11.0	-	dB	
Collector-to-Base Output Capacitance (f = 1 MHz)	C _{obo}	10		0			-	1.0	pF	

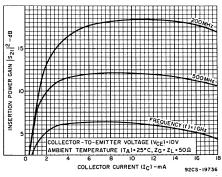


Fig.3-Typical insertion power gain vs. collector current.

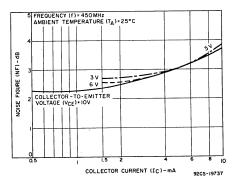
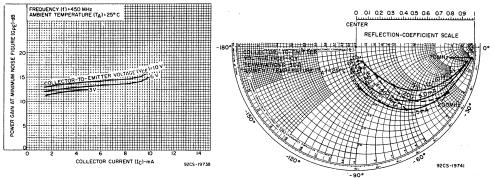
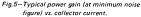
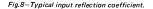


Fig.4-Typical noise figure vs. collector current.





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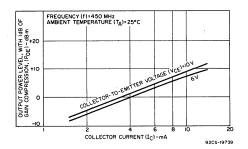


Fig.6-Typical output power level (with 1 dB of gain compression) vs. collector current.

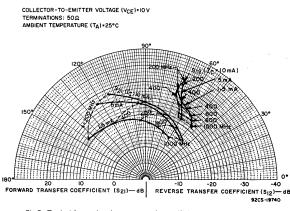
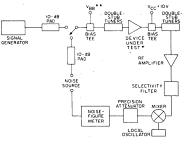
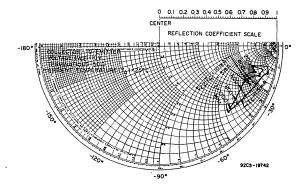


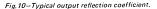
Fig.7-Typical forward and reverse transfer coefficients.

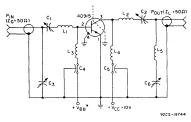


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- In General Radio type 1607-P44 transistor mount, or equivalent.
- ** VBB adjusted for IC = 1.5 mA.
- Fig.9-Block diagram of test setup for measurement of power gain and noise figure.







- C₁: 1.0- 30 pF C₂,C₃: 1.0-20 pF C₄,C₅: 0.04 μF
 - C₆: 1-10 pF
 - L₁: 2 turns No. 18 wire, 3/16 in. (0.188 mm) ID, 0.10 in. (2.54 mm) long
 - L₂: 3 turns No. 18 wire, 3/16 in. (0.188 mm) ID, 0.15 in. (3.81 mm) long
- L3,L4: 0.22-µH rf choke
- L₅: 3 turns No. 18 wire, 3/16 in. (0.188 mm) ID, 0.15 in. (3.81 mm) long
- * V_{BB} adjusted for I_C = 1.5 mA
- Fig. 11-Circuit diagram of 450-MHz amplifier (unneutralized) used for measurement of power gain and noise figure.

TERMINAL CONNECTIONS

- Lead 1 Emitter Lead 2 – Base Lead 3 – Collector
- Lead 4 Case
- Lead 4 Case