

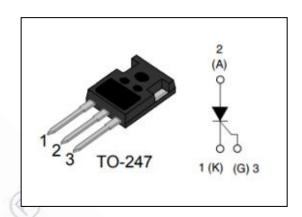
isc Thyristors 40TPS08A

DESCRIPTION

- Long-term stability
- Thyristor for line frequency
- Planar passivated chip
- Minimum Lot-to-Lot variations for robust device performance and reliable operation



 Be used with Vishay HPP input diodes, switches and output rectifiers which are available in identical package outlines.



ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	MIN	UNIT
V_{DRM}	Repetitive peak off-state voltage	1200	V
V _{RRM}	Repetitive peak reverse voltage	1200	V
I _{T(AV)}	Average forward current @Tc=79°C	35	Α
I _{T(RMS)}	RMS on-state current	55	Α
I _{TSM}	Surge non-repetitive on-state current @tp=10ms	500	Α
P _{G(AV)}	Average gate power dissipation	1	W
T _j	Operating junction temperature	-40~125	$^{\circ}$ C
T _{stg}	Storage temperature	-40~150	$^{\circ}\!\mathbb{C}$

ELECTRICAL CHARACTERISTICS (Tc=25℃ unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS		MIN	MAX	UNIT
I _{RRM}	Repetitive peak reverse current	V _{RM} =V _{RRM}	T _j =25℃		0.5	mA
I _{DRM}	Repetitive peak off-state current	V _{DM} =V _{DRM}	Tj=125℃		10	IIIA
V_{TM}	On-state voltage	I _{TM} = 110A			1.85	V
I _{GT}	Gate-trigger current	V _D =12V R _L =33Ω			150	mA
V _{GT}	Gate-trigger voltage	V _D =12V R _L =33Ω			2.5	V
R _{th(j-c)}	Thermal resistance	Junction to case			0.6	°C/W

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