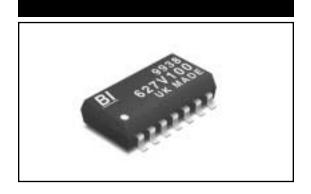
MODEL 627 V100

Designed For Pentium® Power Supply Surface Mount Resistor Networks



APPLICATION

• Designed for P54C, P54C-VR and P54C-VRE Pentium® Processors when used in conjunction with Linear Technology Models LT 1266/1267 or LT 1584/1585 voltage regulator IC's.

ELECTRICAL

| Standard Resistance Tolerance, at 25°C | ±2% |
|--|-----------------|
| Operating Temperature Range | -55°C to +125°C |
| Temperature Coefficient of Resistance | ±100ppm/°C |
| Temperature Coefficient of Resistance Tracking | 50ppm/°C |
| Voltage Coefficient of Resistance | ±100ppm/V |
| Maximum Operating Voltage | 25Vdc |
| Insulation Resistance, Minimum | 10,000 Megohms |

ENVIRONMENTAL (PER MIL-R-83401)

| Thermal Shock plus Power Conditioning | ΔR 0.70% |
|--|-------------------------|
| Short Time Overload | ΔR 0.25% |
| Moisture Resistance | ∆R 0.50% |
| Mechanical Shock | ∆R 0.25% |
| Vibration Shock | ΔR 0.25% |
| Low Temperature Operation | ∆R 0.25% |
| High Temperature Exposure | ΔR 0.50% |
| Load Life, 2,000 Hours (≤33 Ohms = ±0.5 Ohm) | ΔR 0.50% |
| Resistance to Solder Heat | ΔR 0.25% |
| Dielectric Withstanding Voltage | 200V for 1 minute |
| Temperature Exposure, Maximum | 215°C for 3 minutes |
| Marking Permanency | MIL-STD-202, Method 215 |
| Lead Solderability | MIL-STD-202, Method 208 |
| Flammability | UL-94V-O Rated |
| Storage Temperature Range | -55°C to +150°C |

Specifications subject to change without notice.

Pentium® is a registered trademark of Intel Corporation.



MECHANICAL

| Lead Material | Copper Alloy, 60/40 Tin-Lead (Dipped) |
|--------------------|---------------------------------------|
| Lead Configuration | Gull Wing |
| Lead Coplanarity | ±0.002 in. (0.057mm) |
| Substrate Material | Alumina |
| Resistor Material | Cermet |
| Body Material | Ероху |

MATCHING (VOLTAGE RATING)

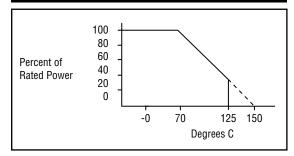
$$\frac{V2}{V1} = 2.174 \pm 1\% \text{ (Sets LTC1266 VFB} = 1.150V \pm 1\%)$$

$$\frac{V3}{V2} = 1.320 \pm 1\% \text{ (Sets } 3.300V \pm 1\% \text{ for P54C})$$

$$\frac{V4}{V2} = 1.353 \pm 0.5\% \text{ (Sets } 3.383V \pm 0.5\% \text{ for P54C-VR})$$

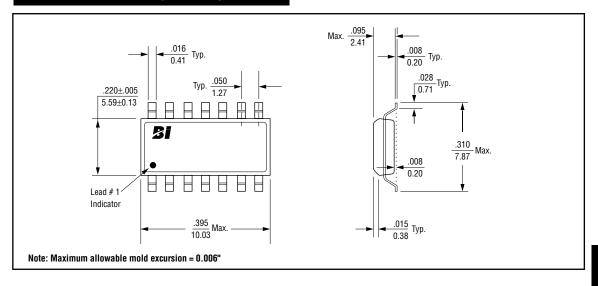
$$\frac{V5}{V2} = 1.410 \pm 0.25\% \text{ (Sets } 3.525V \pm 0.25\% \text{ for P54C-VRE})$$

POWER DERATING CURVE

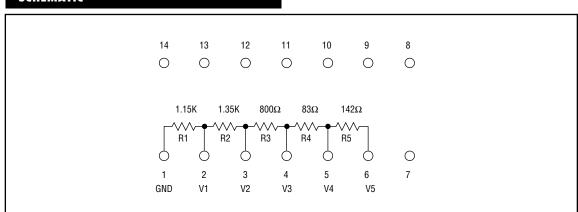


POWER DISSIPATION, WATTS @ 70°C

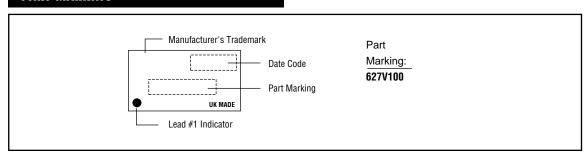
| Model | Package | Per Resistor |
|----------|---------|--------------|
| 627 V100 | 0.625 | 0.125 |
| | | |
| | | |
| | | |
| | | |
| | | |







PART MARKING



PACKAGING

Standard: Tape & Reel

Conforms to requirements of EIA-481.

All units oriented with lead #1 to the left of direction of feed.

| Tape: | Width | = | 24mm | |
|-------|----------|---|------------------------------|--|
| | Pocket | = | Embossed Plastic, Antistatic | |
| | Pitch | = | 12mm | |
| Reel: | Diameter | = | 13" (330mm) Maximum | |
| | Capacity | = | 2.000 Units | |

Option: Magazines

Conforms to EIA and JEDEC standards.

All units oriented with lead #1 to the same side.

Magazine: Capacity = 50 Units

ORDERING INFORMATION

