

74368

Hex buffer/line driver; 3-state; inverting

[PDF datasheet](#)

	Pin	Symbol	Description
\overline{OE}_1 1 • 16 Vcc	1	\overline{OE}_1	output enable (active low)
1A 2 15 \overline{OE}_2	2	1A	data input
1Y 3 14 6A	3	1Y	bus output
2A 4 13 6Y	4	2A	data input
2Y 5 12 5A	5	2Y	bus output
3A 6 11 5Y	6	3A	data input
3Y 7 10 4A	7	3Y	bus output
GND 8 9 4Y	8	GND	ground
	9	4Y	bus output
	10	4A	data input
	11	5Y	bus output
	12	5A	data input
	13	6Y	bus output
	14	6A	data input
	15	\overline{OE}_2	output enable (active low)
	16	Vcc	supply voltage

Specifications

(typical values under recommended operating conditions, unless specified)

Parameter	Value	Unit
Propagation delay, nA to nY	9 (74HC) 11 (74HCT)	ns

Notes

- Setting \overline{OE}_1 or \overline{OE}_2 high causes the outputs to assume a high-impedance off state.

Note: Data is maintained by an independent source and accuracy is not guaranteed. Check with the manufacturer's datasheet for up-to-date information.