

74AC11873
DUAL 4-BIT D-TYPE LATCH
WITH 3-STATE OUTPUTS

SCAS095 – JANUARY 1990 – REVISED APRIL 1993

- 3-State Buffer-Type Outputs Drive Bus Lines Directly
- Bus-Structured Pinout
- Flow-Through Architecture Optimizes PCB Layout
- Center-Pin V_{CC} and GND Configurations Minimize High-Speed Switching Noise
- EPIC™ (Enhanced-Performance Implanted CMOS) 1- μ m Process
- 500-mA Typical Latch-Up Immunity at 125°C

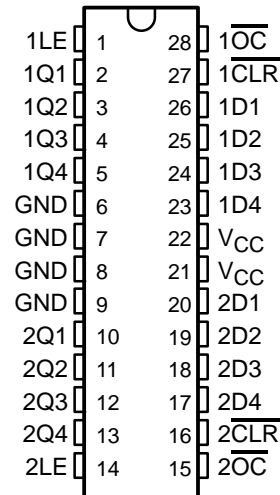
description

This dual 4-bit transparent D-type latch features 3-state outputs designed specifically for bus driving. This makes these devices particularly suitable for implementing buffer registers, I/O ports, bidirectional bus drivers, and working registers.

When the latch-enable (1LE or 2LE) input is high, the Q outputs will follow the data (D) inputs in true form, according to the function table. When LE is taken low, the outputs will be latched. When the clear ($\overline{1CLR}$ or $\overline{2CLR}$) input goes low, the Q outputs go low independently of LE. The outputs are in a high-impedance state when the output-control ($\overline{1OC}$ or $\overline{2OC}$) input is at a high logic level.

The 74AC11873 is characterized for operation from -40°C to 85°C.

DW PACKAGE
(TOP VIEW)



FUNCTION TABLE
(each 4-bit latch)

| INPUTS | | | | OUTPUT Q |
|-----------------|------------------|----|---|-------------|
| \overline{OC} | \overline{CLR} | LE | D | |
| L | L | X | X | L |
| L | H | H | H | H |
| L | H | H | L | L |
| L | H | L | X | Q_0 |
| H | X | X | X | Z |

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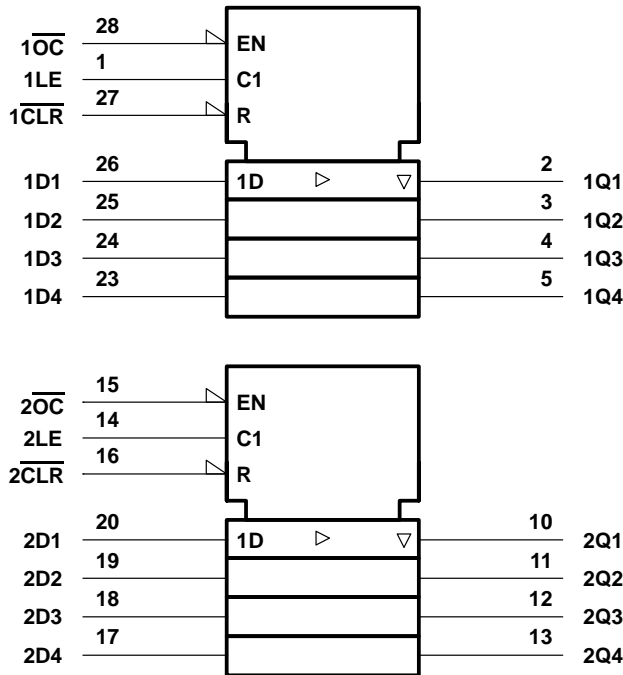
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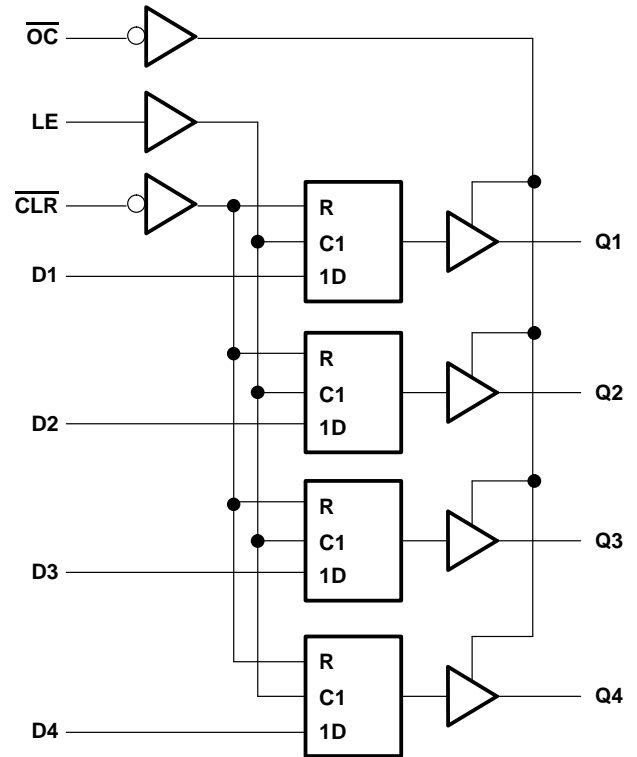
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logic symbol†



† This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

logic diagram, each quad latch (positive logic)



absolute maximum ratings over operating free-air temperature range (unless otherwise noted)‡

| | |
|--|----------------------------|
| Supply voltage range, V_{CC} | -0.5 V to 7 V |
| Input voltage range, V_I (see Note 1) | -0.5 V to $V_{CC} + 0.5$ V |
| Output voltage range, V_O (see Note 1) | -0.5 V to $V_{CC} + 0.5$ V |
| Input clamp current, I_{IK} ($V_I < 0$ or $V_I > V_{CC}$) | ± 20 mA |
| Output clamp current, I_{OK} ($V_O < 0$ or $V_O > V_{CC}$) | ± 50 mA |
| Continuous output current, I_O ($V_O = 0$ to V_{CC}) | ± 50 mA |
| Continuous current through V_{CC} or GND | ± 200 mA |
| Storage temperature range | -65°C to 150°C |

‡ Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

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recommended operating conditions

| | | MIN | NOM | MAX | UNIT |
|-----------------|------------------------------------|-------------------------|------|-----------------|------|
| V _{CC} | Supply voltage | 3 | 5 | 5.5 | V |
| V _{IH} | High-level input voltage | V _{CC} = 3 V | 2.1 | | V |
| | | V _{CC} = 4.5 V | 3.15 | | |
| | | V _{CC} = 5.5 V | 3.85 | | |
| V _{IL} | Low-level input voltage | V _{CC} = 3 V | | 0.9 | V |
| | | V _{CC} = 4.5 V | | 1.35 | |
| | | V _{CC} = 5.5 V | | 1.65 | |
| V _I | Input voltage | 0 | | V _{CC} | V |
| V _O | Output voltage | 0 | | V _{CC} | V |
| I _{OH} | High-level output current | V _{CC} = 3 V | | -4 | mA |
| | | V _{CC} = 4.5 V | | -24 | |
| | | V _{CC} = 5.5 V | | -24 | |
| I _{OL} | Low-level output current | V _{CC} = 3 V | | 12 | mA |
| | | V _{CC} = 4.5 V | | 24 | |
| | | V _{CC} = 5.5 V | | 24 | |
| Δt/Δv | Input transition rise or fall rate | 0 | | 10 | ns/V |
| T _A | Operating free-air temperature | -40 | | 85 | °C |

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

| PARAMETER | TEST CONDITIONS | V _{CC} | T _A = 25°C | | | MIN | MAX | UNIT |
|---------------------------------------|---|-----------------|-----------------------|------|------|------|-----|------|
| | | | MIN | TYP | MAX | | | |
| V _{OH} | I _{OH} = -50 μA | 3 V | 2.9 | | 2.9 | V | | |
| | | 4.5 V | 4.4 | | 4.4 | | | |
| | | 5.5 V | 5.4 | | 5.4 | | | |
| | I _{OH} = -4 mA | 3 V | 2.58 | | 2.48 | | | |
| | | 4.5 V | 3.94 | | 3.8 | | | |
| | | 5.5 V | 4.94 | | 4.8 | | | |
| I _{OH} = -75 mA [†] | 5.5 V | | | 3.85 | | | | |
| V _{OL} | I _{OL} = 50 μA | 3 V | | | 0.1 | 0.1 | | |
| | | 4.5 V | | | 0.1 | 0.1 | | |
| | | 5.5 V | | | 0.1 | 0.1 | | |
| | I _{OL} = 12 mA | 3 V | | | 0.36 | 0.44 | | |
| | | 4.5 V | | | 0.36 | 0.44 | | |
| | | 5.5 V | | | 0.36 | 0.44 | | |
| I _{OL} = 75 mA [†] | 5.5 V | | | | 1.65 | | | |
| I _I | V _I = V _{CC} or GND | 5.5 V | | | ±0.1 | ±1 | μA | |
| I _{OZ} | V _O = V _{CC} or GND | 5.5 V | | | ±0.5 | ±5 | μA | |
| I _{CC} | V _I = V _{CC} or GND, I _O = 0 | 5.5 V | | | 8 | 80 | μA | |
| C _i | V _I = V _{CC} or GND | 5 V | | 4.5 | | | pF | |
| C _o | V _O = V _{CC} or GND | 5 V | | 13.5 | | | pF | |

[†] Not more than one output should be tested at a time, and the duration of the test should not exceed 10 ms.



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timing requirements over recommended operating free-air temperature range, $V_{CC} = 3.3\text{ V} \pm 0.3\text{ V}$ (unless otherwise noted) (see Figure 1)

| | | $T_A = 25^\circ\text{C}$ | | MIN | MAX | UNIT |
|----------|---|-----------------------------|-----|-----|-----|------|
| | | MIN | MAX | | | |
| t_w | Pulse duration | $\overline{\text{CLR}}$ low | 5 | 5 | ns | |
| | | LE high | 5 | 5 | | |
| t_{su} | Setup time, data before LE \downarrow | Hlgh | 3 | 3 | ns | |
| | | Low | 4 | 4 | | |
| t_h | Hold time, data after LE \downarrow | High | 1 | 1 | ns | |
| | | Low | 1 | 1 | | |

timing requirements over recommended operating free-air temperature range, $V_{CC} = 5\text{ V} \pm 0.5\text{ V}$ (unless otherwise noted) (see Figure 1)

| | | $T_A = 25^\circ\text{C}$ | | MIN | MAX | UNIT |
|----------|---|-----------------------------|-----|-----|-----|------|
| | | MIN | MAX | | | |
| t_w | Pulse duration | $\overline{\text{CLR}}$ low | 5 | 5 | ns | |
| | | LE high | 5 | 5 | | |
| t_{su} | Setup time, data before LE \downarrow | Hlgh | 2 | 2 | ns | |
| | | Low | 3 | 3 | | |
| t_h | Hold time, data after LE \downarrow | High | 1 | 1 | ns | |
| | | Low | 1 | 1 | | |

switching characteristics over recommended operating free-air temperature range, $V_{CC} = 3.3\text{ V} \pm 0.3\text{ V}$ (unless otherwise noted) (see Figure 1)

| PARAMETER | FROM (INPUT) | TO (OUTPUT) | $T_A = 25^\circ\text{C}$ | | | MIN | MAX | UNIT |
|-----------|-------------------------|-------------|--------------------------|-----|------|-----|------|------|
| | | | MIN | TYP | MAX | | | |
| t_{PLH} | D | Q | 2.8 | 8.8 | 11.2 | 2.8 | 13 | ns |
| t_{PHL} | | | 2.8 | 9 | 11.2 | 2.8 | 12.7 | |
| t_{PLH} | LE | Q | 3 | 9.4 | 11.8 | 3 | 13.6 | ns |
| t_{PHL} | | | 2.9 | 9.4 | 11.7 | 2.9 | 13.2 | |
| t_{PHL} | $\overline{\text{CLR}}$ | Q | 2.3 | 8.2 | 10.3 | 2.3 | 11.5 | ns |
| t_{PZH} | $\overline{\text{OC}}$ | Q | 1.8 | 6.4 | 8.4 | 1.8 | 9.7 | ns |
| t_{PZL} | | | 2.7 | 9.9 | 12.5 | 2.7 | 14.4 | |
| t_{PHZ} | $\overline{\text{OC}}$ | Q | 3.8 | 6.8 | 8.4 | 3.8 | 9 | ns |
| t_{PLZ} | | | 3.5 | 6.8 | 8.5 | 3.5 | 9.1 | |

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switching characteristics over recommended operating free-air temperature range,
 $V_{CC} = 5\text{ V} \pm 0.5\text{ V}$ (unless otherwise noted) (see Figure 1)

| PARAMETER | FROM (INPUT) | TO (OUTPUT) | $T_A = 25^\circ\text{C}$ | | | MIN | MAX | UNIT |
|-----------|-------------------------|----------------|--------------------------|-----|-----|-----|-----|------|
| | | | MIN | TYP | MAX | | | |
| t_{PLH} | D | Q | 2.2 | 5.5 | 7.3 | 2.2 | 8.4 | ns |
| t_{PHL} | | | 2.1 | 5.5 | 7.2 | 2.1 | 8.2 | |
| t_{PLH} | LE | Q | 2.4 | 5.9 | 7.8 | 2.4 | 8.9 | ns |
| t_{PHL} | | | 2.2 | 5.8 | 7.6 | 2.2 | 8.7 | |
| t_{PHL} | $\overline{\text{CLR}}$ | Q | 1.7 | 5.1 | 6.8 | 1.7 | 7.6 | ns |
| t_{PZH} | $\overline{\text{OC}}$ | Q | 1.2 | 4.1 | 5.6 | 1.2 | 6.4 | ns |
| t_{PZL} | | | 1.9 | 5.5 | 7.3 | 1.9 | 8.5 | |
| t_{PHZ} | $\overline{\text{OC}}$ | Q | 3.5 | 5.9 | 7.4 | 3.5 | 7.9 | ns |
| t_{PLZ} | | | 3.3 | 5.5 | 7 | 3.3 | 7.6 | |

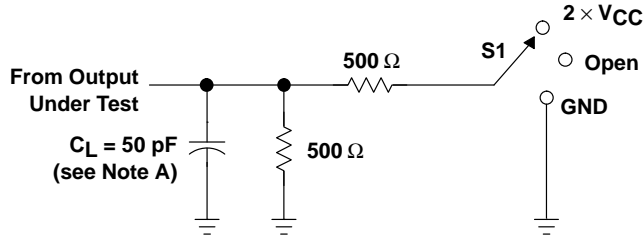
operating characteristics, $V_{CC} = 5\text{ V}$, $T_A = 25^\circ\text{C}$

| PARAMETER | | TEST CONDITIONS | TYP | UNIT |
|-----------|---|------------------|-----|------|
| C_{pd} | Power dissipation capacitance per latch | Outputs enabled | 43 | pF |
| | | Outputs disabled | 9 | |

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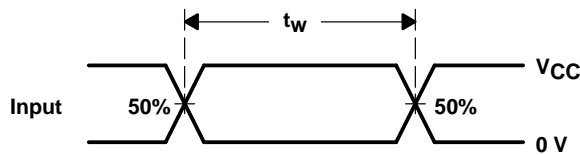
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PARAMETER MEASUREMENT INFORMATION

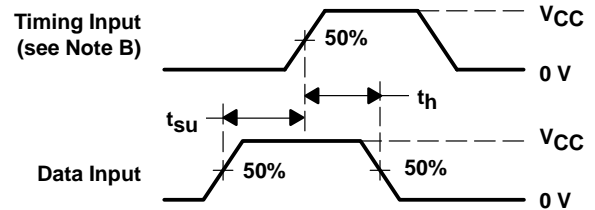


LOAD CIRCUIT

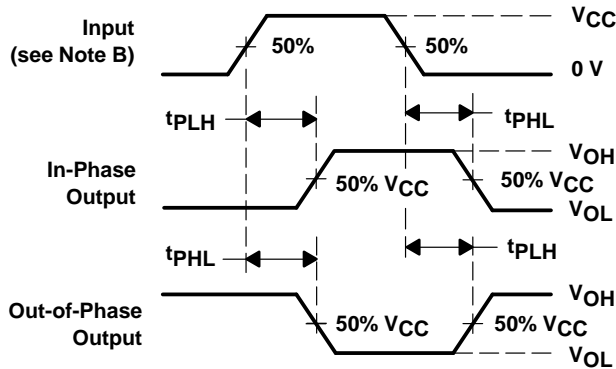
| TEST | S1 |
|-------------------|-------------------|
| t_{PLH}/t_{PHL} | Open |
| t_{PLZ}/t_{PZL} | $2 \times V_{CC}$ |
| t_{PHZ}/t_{PZH} | GND |



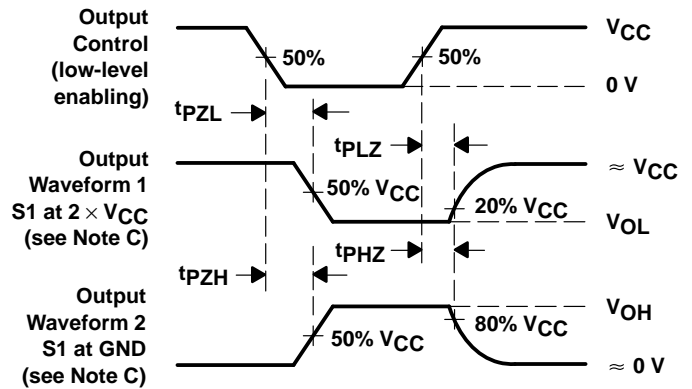
VOLTAGE WAVEFORMS



VOLTAGE WAVEFORMS



VOLTAGE WAVEFORMS



VOLTAGE WAVEFORMS

NOTES: A. C_L includes probe and jig capacitance.

B. All input pulses are supplied by generators having the following characteristics: $PRR \leq 10 \text{ MHz}$, $Z_O = 50 \Omega$, $t_r = 3 \text{ ns}$, $t_f = 3 \text{ ns}$.

C. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control.

Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.

D. The outputs are measured one at a time with one input transition per measurement.

Figure 1. Load Circuit and Voltage Waveforms

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