

74AUP2G38

Low-power dual 2-input NAND gate (open-drain)

Rev. 01 — 16 October 2006

Product data sheet

1. General description

The 74AUP2G38 is a high-performance, low-power, low-voltage, Si-gate CMOS device, superior to most advanced CMOS compatible TTL families.

Schmitt trigger action at all inputs makes the circuit tolerant to slower input rise and fall times across the entire V_{CC} range from 0.8 V to 3.6 V.

This device ensures a very low static and dynamic power consumption across the entire V_{CC} range from 0.8 V to 3.6 V.

This device is fully specified for partial Power-down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

The 74AUP2G38 provides the dual 2-input NAND gate with open-drain output. The output of the device is an open drain and can be connected to other open-drain outputs to implement active-LOW wired-OR or active-HIGH wired-AND functions.

2. Features

- Wide supply voltage range from 0.8 V to 3.6 V
- High noise immunity
- Complies with JEDEC standards:
 - ◆ JESD8-12 (0.8 V to 1.3 V)
 - ◆ JESD8-11 (0.9 V to 1.65 V)
 - ◆ JESD8-7 (1.2 V to 1.95 V)
 - ◆ JESD8-5 (1.8 V to 2.7 V)
 - ◆ JESD8-B (2.7 V to 3.6 V)
- ESD protection:
 - ◆ HBM JESD22-A114-D Class 3A exceeds 5000 V
 - ◆ MM JESD22-A115-A exceeds 200 V
 - ◆ CDM JESD22-C101-C exceeds 1000 V
- Low static power consumption; $I_{CC} = 0.9 \mu\text{A}$ (maximum)
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- Inputs accept voltages up to 3.6 V
- Low noise overshoot and undershoot < 10 % of V_{CC}
- I_{OFF} circuitry provides partial Power-down mode operation
- Multiple package options
- Specified from $-40 \text{ }^\circ\text{C}$ to $+85 \text{ }^\circ\text{C}$ and $-40 \text{ }^\circ\text{C}$ to $+125 \text{ }^\circ\text{C}$

3. Ordering information

Table 1. Ordering information

Type number	Package			Version
	Temperature range	Name	Description	
74AUP2G38DC	-40 °C to +125 °C	VSSOP8	plastic very thin shrink small outline package; 8 leads; body width 2.3 mm	SOT765-1
74AUP2G38GT	-40 °C to +125 °C	XSON8	plastic extremely thin small outline package; no leads; 8 terminals; body 1 × 1.95 × 0.5 mm	SOT833-1
74AUP2G38GM	-40 °C to +125 °C	XQFN8	plastic extremely thin quad flat package; no leads; 8 terminals; body 1.6 × 1.6 × 0.5 mm	SOT902-1

4. Marking

Table 2. Marking

Type number	Marking code
74AUP2G38DC	a38
74AUP2G38GT	a38
74AUP2G38GM	a38

5. Functional diagram

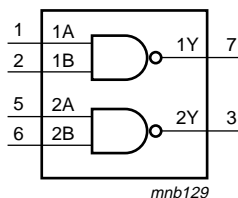


Fig 1. Logic symbol

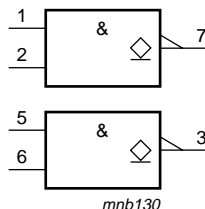


Fig 2. IEC logic symbol

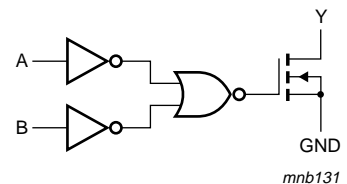


Fig 3. Logic diagram (one gate)

6. Pinning information

6.1 Pinning

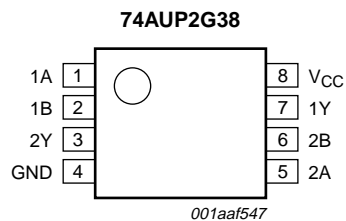


Fig 4. Pin configuration SOT765-1 (VSSOP8)

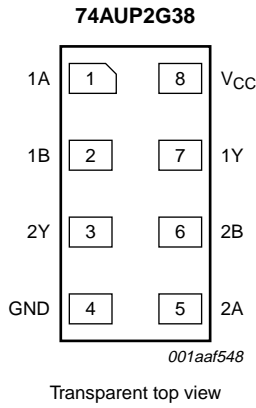


Fig 5. Pin configuration SOT833-1 (XSON8)

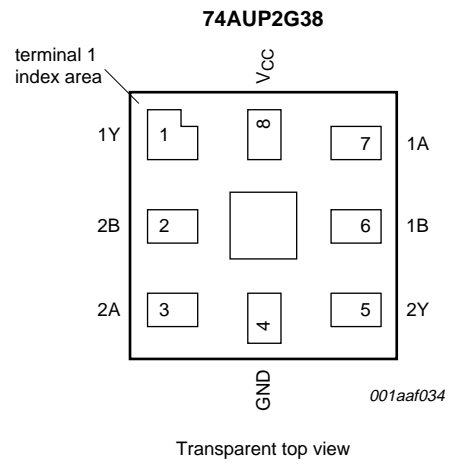


Fig 6. Pin configuration SOT902-1 (XQFN8)

6.2 Pin description

Table 3. Pin description

Symbol	Pin		Description
	SOT765-1 and SOT833-1	SOT902-1	
1A	1	7	data input 1A
1B	2	6	data input 1B
2Y	3	5	data output 2Y
GND	4	4	ground (0 V)
2A	5	3	data input 2A
2B	6	2	data input 2B
1Y	7	1	data output 1Y
V _{CC}	8	8	supply voltage

7. Functional description

Table 4. Function table^[1]

Input		Output
nA	nB	nY
L	L	Z
L	H	Z
H	L	Z
H	H	L

[1] H = HIGH voltage level;
 L = LOW voltage level;
 Z = high-impedance OFF state.

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CC}	supply voltage		-0.5	+4.6	V
I_{IK}	input clamping current	$V_I < 0$ V	-	-50	mA
V_I	input voltage		[1] -0.5	+4.6	V
I_{OK}	output clamping current	$V_O > V_{CC}$ or $V_O < 0$ V	-	± 50	mA
V_O	output voltage	Active mode and Power-down mode	[1] -0.5	+4.6	V
I_O	output current	$V_O = 0$ V to V_{CC}	-	+20	mA
I_{CC}	supply current		-	50	mA
I_{GND}	ground current		-	-50	mA
T_{stg}	storage temperature		-65	+150	°C
P_{tot}	total power dissipation	$T_{amb} = -40$ °C to +125 °C	[2] -	250	mW

[1] The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For VSSOP8 packages: above 110 °C the value of P_{tot} derates linearly with 8.0 mW/K.
For XSON8 and XQFN8 packages: above 45 °C the value of P_{tot} derates linearly with 2.4 mW/K.

9. Recommended operating conditions

Table 6. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CC}	supply voltage		0.8	3.6	V
V_I	input voltage		0	3.6	V
V_O	output voltage	Active mode and Power-down mode	0	3.6	V
T_{amb}	ambient temperature		-40	+125	°C
$\Delta t/\Delta V$	input transition rise and fall rate	$V_{CC} = 0.8$ V to 3.6 V	0	200	ns/V

10. Static characteristics

Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
T_{amb} = 25 °C						
V _{IH}	HIGH-level input voltage	V _{CC} = 0.8 V	0.70 × V _{CC}	-	-	V
		V _{CC} = 0.9 V to 1.95 V	0.65 × V _{CC}	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V	-	-	0.30 × V _{CC}	V
		V _{CC} = 0.9 V to 1.95 V	-	-	0.35 × V _{CC}	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	V
V _{OL}	LOW-level output voltage	V _I = V _{IH} or V _{IL}				
		I _O = 20 μA; V _{CC} = 0.8 V to 3.6 V	-	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.3 × V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.31	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.31	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.31	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.44	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.31	V
I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.44	V		
I _I	input leakage current	V _I = GND to 3.6 V; V _{CC} = 0 V to 3.6 V	-	-	±0.1	μA
I _{OZ}	OFF-state output current	V _I = V _{IH} or V _{IL} ; V _O = 0 V to 3.6 V; V _{CC} = 0 V to 3.6 V	-	-	±0.1	μA
I _{OFF}	power-off leakage current	V _I or V _O = 0 V to 3.6 V; V _{CC} = 0 V	-	-	±0.2	μA
ΔI _{OFF}	additional power-off leakage current	V _I or V _O = 0 V to 3.6 V; V _{CC} = 0 V to 0.2 V	-	-	±0.2	μA
I _{CC}	supply current	V _I = GND or V _{CC} ; I _O = 0 A; V _{CC} = 0.8 V to 3.6 V	-	-	0.5	μA
ΔI _{CC}	additional supply current	V _I = V _{CC} - 0.6 V; I _O = 0 A; V _{CC} = 3.3 V	-	-	40	μA
C _I	input capacitance	V _{CC} = 0 V to 3.6 V; V _I = GND or V _{CC}	-	0.7	-	pF
C _O	output capacitance	V _O = GND; V _{CC} = 0 V	-	0.9	-	pF

Table 7. Static characteristics ...continued

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$T_{amb} = -40\text{ °C to }+85\text{ °C}$						
V_{IH}	HIGH-level input voltage	$V_{CC} = 0.8\text{ V}$	$0.70 \times V_{CC}$	-	-	V
		$V_{CC} = 0.9\text{ V to }1.95\text{ V}$	$0.65 \times V_{CC}$	-	-	V
		$V_{CC} = 2.3\text{ V to }2.7\text{ V}$	1.6	-	-	V
		$V_{CC} = 3.0\text{ V to }3.6\text{ V}$	2.0	-	-	V
V_{IL}	LOW-level input voltage	$V_{CC} = 0.8\text{ V}$	-	-	$0.30 \times V_{CC}$	V
		$V_{CC} = 0.9\text{ V to }1.95\text{ V}$	-	-	$0.35 \times V_{CC}$	V
		$V_{CC} = 2.3\text{ V to }2.7\text{ V}$	-	-	0.7	V
		$V_{CC} = 3.0\text{ V to }3.6\text{ V}$	-	-	0.9	V
V_{OL}	LOW-level output voltage	$V_I = V_{IH}\text{ or }V_{IL}$				
		$I_O = 20\text{ }\mu\text{A}; V_{CC} = 0.8\text{ V to }3.6\text{ V}$	-	-	0.1	V
		$I_O = 1.1\text{ mA}; V_{CC} = 1.1\text{ V}$	-	-	$0.3 \times V_{CC}$	V
		$I_O = 1.7\text{ mA}; V_{CC} = 1.4\text{ V}$	-	-	0.37	V
		$I_O = 1.9\text{ mA}; V_{CC} = 1.65\text{ V}$	-	-	0.35	V
		$I_O = 2.3\text{ mA}; V_{CC} = 2.3\text{ V}$	-	-	0.33	V
		$I_O = 3.1\text{ mA}; V_{CC} = 2.3\text{ V}$	-	-	0.45	V
		$I_O = 2.7\text{ mA}; V_{CC} = 3.0\text{ V}$	-	-	0.33	V
	$I_O = 4.0\text{ mA}; V_{CC} = 3.0\text{ V}$	-	-	0.45	V	
I_I	input leakage current	$V_I = \text{GND to }3.6\text{ V}; V_{CC} = 0\text{ V to }3.6\text{ V}$	-	-	± 0.5	μA
I_{OZ}	OFF-state output current	$V_I = V_{IH}\text{ or }V_{IL}; V_O = 0\text{ V to }3.6\text{ V}; V_{CC} = 0\text{ V to }3.6\text{ V}$	-	-	± 0.5	μA
I_{OFF}	power-off leakage current	$V_I\text{ or }V_O = 0\text{ V to }3.6\text{ V}; V_{CC} = 0\text{ V}$	-	-	± 0.5	μA
ΔI_{OFF}	additional power-off leakage current	$V_I\text{ or }V_O = 0\text{ V to }3.6\text{ V}; V_{CC} = 0\text{ V to }0.2\text{ V}$	-	-	± 0.6	μA
I_{CC}	supply current	$V_I = \text{GND or }V_{CC}; I_O = 0\text{ A}; V_{CC} = 0.8\text{ V to }3.6\text{ V}$	-	-	0.9	μA
ΔI_{CC}	additional supply current	$V_I = V_{CC} - 0.6\text{ V}; I_O = 0\text{ A}; V_{CC} = 3.3\text{ V}$	-	-	50	μA

Table 7. Static characteristics ...continued

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$T_{amb} = -40\text{ °C to }+125\text{ °C}$						
V_{IH}	HIGH-level input voltage	$V_{CC} = 0.8\text{ V}$	$0.75 \times V_{CC}$	-	-	V
		$V_{CC} = 0.9\text{ V to }1.95\text{ V}$	$0.70 \times V_{CC}$	-	-	V
		$V_{CC} = 2.3\text{ V to }2.7\text{ V}$	1.6	-	-	V
		$V_{CC} = 3.0\text{ V to }3.6\text{ V}$	2.0	-	-	V
V_{IL}	LOW-level input voltage	$V_{CC} = 0.8\text{ V}$	-	-	$0.25 \times V_{CC}$	V
		$V_{CC} = 0.9\text{ V to }1.95\text{ V}$	-	-	$0.30 \times V_{CC}$	V
		$V_{CC} = 2.3\text{ V to }2.7\text{ V}$	-	-	0.7	V
		$V_{CC} = 3.0\text{ V to }3.6\text{ V}$	-	-	0.9	V
V_{OL}	LOW-level output voltage	$V_I = V_{IH}\text{ or }V_{IL}$				
		$I_O = 20\text{ }\mu\text{A}; V_{CC} = 0.8\text{ V to }3.6\text{ V}$	-	-	0.11	V
		$I_O = 1.1\text{ mA}; V_{CC} = 1.1\text{ V}$	-	-	$0.33 \times V_{CC}$	V
		$I_O = 1.7\text{ mA}; V_{CC} = 1.4\text{ V}$	-	-	0.41	V
		$I_O = 1.9\text{ mA}; V_{CC} = 1.65\text{ V}$	-	-	0.39	V
		$I_O = 2.3\text{ mA}; V_{CC} = 2.3\text{ V}$	-	-	0.36	V
		$I_O = 3.1\text{ mA}; V_{CC} = 2.3\text{ V}$	-	-	0.50	V
		$I_O = 2.7\text{ mA}; V_{CC} = 3.0\text{ V}$	-	-	0.36	V
	$I_O = 4.0\text{ mA}; V_{CC} = 3.0\text{ V}$	-	-	0.50	V	
I_I	input leakage current	$V_I = \text{GND to }3.6\text{ V}; V_{CC} = 0\text{ V to }3.6\text{ V}$	-	-	± 0.75	μA
I_{OZ}	OFF-state output current	$V_I = V_{IH}\text{ or }V_{IL}; V_O = 0\text{ V to }3.6\text{ V}; V_{CC} = 0\text{ V to }3.6\text{ V}$	-	-	± 0.75	μA
I_{OFF}	power-off leakage current	$V_I\text{ or }V_O = 0\text{ V to }3.6\text{ V}; V_{CC} = 0\text{ V}$	-	-	± 0.75	μA
ΔI_{OFF}	additional power-off leakage current	$V_I\text{ or }V_O = 0\text{ V to }3.6\text{ V}; V_{CC} = 0\text{ V to }0.2\text{ V}$	-	-	± 0.75	μA
I_{CC}	supply current	$V_I = \text{GND or }V_{CC}; I_O = 0\text{ A}; V_{CC} = 0.8\text{ V to }3.6\text{ V}$	-	-	1.4	μA
ΔI_{CC}	additional supply current	$V_I = V_{CC} - 0.6\text{ V}; I_O = 0\text{ A}; V_{CC} = 3.3\text{ V}$	-	-	75	μA

11. Dynamic characteristics

Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V; for test circuit see [Figure 8](#))

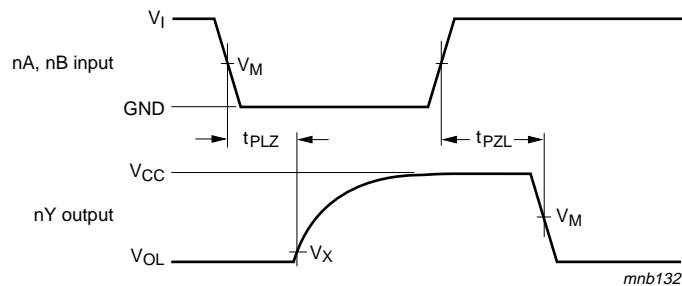
Symbol	Parameter	Conditions	25 °C			-40 °C to +125 °C			Unit
			Min	Typ ^[1]	Max	Min	Max (85 °C)	Max (125 °C)	
$C_L = 5 \text{ pF}$									
t_{pd}	propagation delay	nA or nB to nY; see Figure 7 ^[2]							
		$V_{CC} = 0.8 \text{ V}$	-	13.5	-	-	-	-	ns
		$V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$	1.9	4.6	10.4	1.8	11.4	12.6	ns
		$V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$	1.5	3.3	6.5	1.4	7.4	8.2	ns
		$V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$	1.2	2.9	5.1	1.1	5.9	6.5	ns
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	1.0	2.2	3.8	0.9	4.5	4.9	ns
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$	0.9	2.3	4.0	0.8	4.5	4.9	ns
$C_L = 10 \text{ pF}$									
t_{pd}	propagation delay	nA or nB to nY; see Figure 7 ^[2]							
		$V_{CC} = 0.8 \text{ V}$	-	16.3	-	-	-	-	ns
		$V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$	2.3	5.6	12.3	2.1	13.7	15.1	ns
		$V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$	1.8	4.1	7.6	1.7	8.8	9.7	ns
		$V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$	1.6	3.8	6.1	1.4	7.1	7.8	ns
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	1.4	2.9	4.6	1.2	5.4	5.9	ns
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$	1.3	3.2	5.7	1.1	6.4	7.0	ns
$C_L = 15 \text{ pF}$									
t_{pd}	propagation delay	nA or nB to nY; see Figure 7 ^[2]							
		$V_{CC} = 0.8 \text{ V}$	-	19.0	-	-	-	-	ns
		$V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$	2.6	6.6	14.2	2.4	15.8	17.4	ns
		$V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$	2.1	4.8	8.7	1.9	10.1	11.1	ns
		$V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$	1.9	4.6	7.6	1.7	8.5	9.3	ns
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	1.6	3.6	5.6	1.5	6.3	6.9	ns
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$	1.6	4.1	7.5	1.4	8.3	9.1	ns
$C_L = 30 \text{ pF}$									
t_{pd}	propagation delay	nA or nB to nY; see Figure 7 ^[2]							
		$V_{CC} = 0.8 \text{ V}$	-	27.0	-	-	-	-	ns
		$V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$	3.6	9.5	19.5	3.2	21.8	24.0	ns
		$V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$	2.9	7.0	11.5	2.6	13.6	15.0	ns
		$V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$	2.6	7.0	12.1	2.3	13.3	14.6	ns
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	2.4	5.4	8.9	2.1	9.9	10.9	ns
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$	2.3	6.5	12.7	2.1	13.9	15.3	ns

Table 8. Dynamic characteristics ...continuedVoltages are referenced to GND (ground = 0 V; for test circuit see [Figure 8](#))

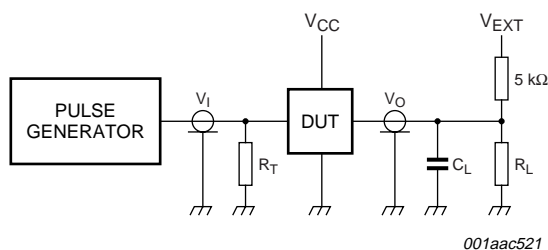
Symbol	Parameter	Conditions	25 °C			-40 °C to +125 °C			Unit
			Min	Typ ^[1]	Max	Min	Max (85 °C)	Max (125 °C)	
$C_L = 5 \text{ pF}, 10 \text{ pF}, 15 \text{ pF}$ and 30 pF									
C_{PD}	power dissipation capacitance	$f = 1 \text{ MHz}; V_I = \text{GND to } V_{CC}$ ^[3]							
		$V_{CC} = 0.8 \text{ V}$	-	0.6	-	-	-	-	pF
		$V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$	-	0.7	-	-	-	-	pF
		$V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$	-	0.8	-	-	-	-	pF
		$V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$	-	0.9	-	-	-	-	pF
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	-	1.1	-	-	-	-	pF
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$	-	1.4	-	-	-	-	pF

- [1] All typical values are measured at nominal V_{CC} .
- [2] t_{pd} is the same as t_{PZL} and t_{PLZ} .
- [3] C_{PD} is used to determine the dynamic power dissipation (P_D in μW).
- $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N$ where:
- f_i = input frequency in MHz;
 - C_L = output load capacitance in pF;
 - V_{CC} = supply voltage in V;
 - N = number of inputs switching.

12. Waveforms

Measurement points are given in [Table 9](#).Logic levels: V_{OL} and V_{OH} are typical output voltage drop that occur with the output load.**Fig 7. The data input (nA or nB) to output (nY) propagation delays****Table 9. Measurement points**

Supply voltage	Input	Output	
V_{CC}	V_M	V_M	V_X
0.8 V to 1.6 V	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$	$V_{OL} + 0.1 \text{ V}$
1.65 V to 2.7 V	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$	$V_{OL} + 0.15 \text{ V}$
3.0 V to 3.6 V	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$	$V_{OL} + 0.3 \text{ V}$



Test data is given in [Table 10](#).

Definitions for test circuit:

R_L = Load resistance.

C_L = Load capacitance including jig and probe capacitance.

R_T = Termination resistance should be equal to the output impedance Z_o of the pulse generator.

V_{EXT} = External voltage for measuring switching times.

Fig 8. Load circuitry for switching times

Table 10. Test data

Supply voltage	Load		V_{EXT}		
V_{CC}	C_L	R_L [1]	t_{PLH} , t_{PHL}	t_{PZH} , t_{PHZ}	t_{PZL} , t_{PLZ}
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 kΩ or 1 MΩ	open	GND	$2 \times V_{CC}$

[1] For measuring enable and disable times $R_L = 5 \text{ k}\Omega$, for measuring propagation delays, setup and hold times and pulse width $R_L = 1 \text{ M}\Omega$.

13. Package outline

VSSOP8: plastic very thin shrink small outline package; 8 leads; body width 2.3 mm

SOT765-1

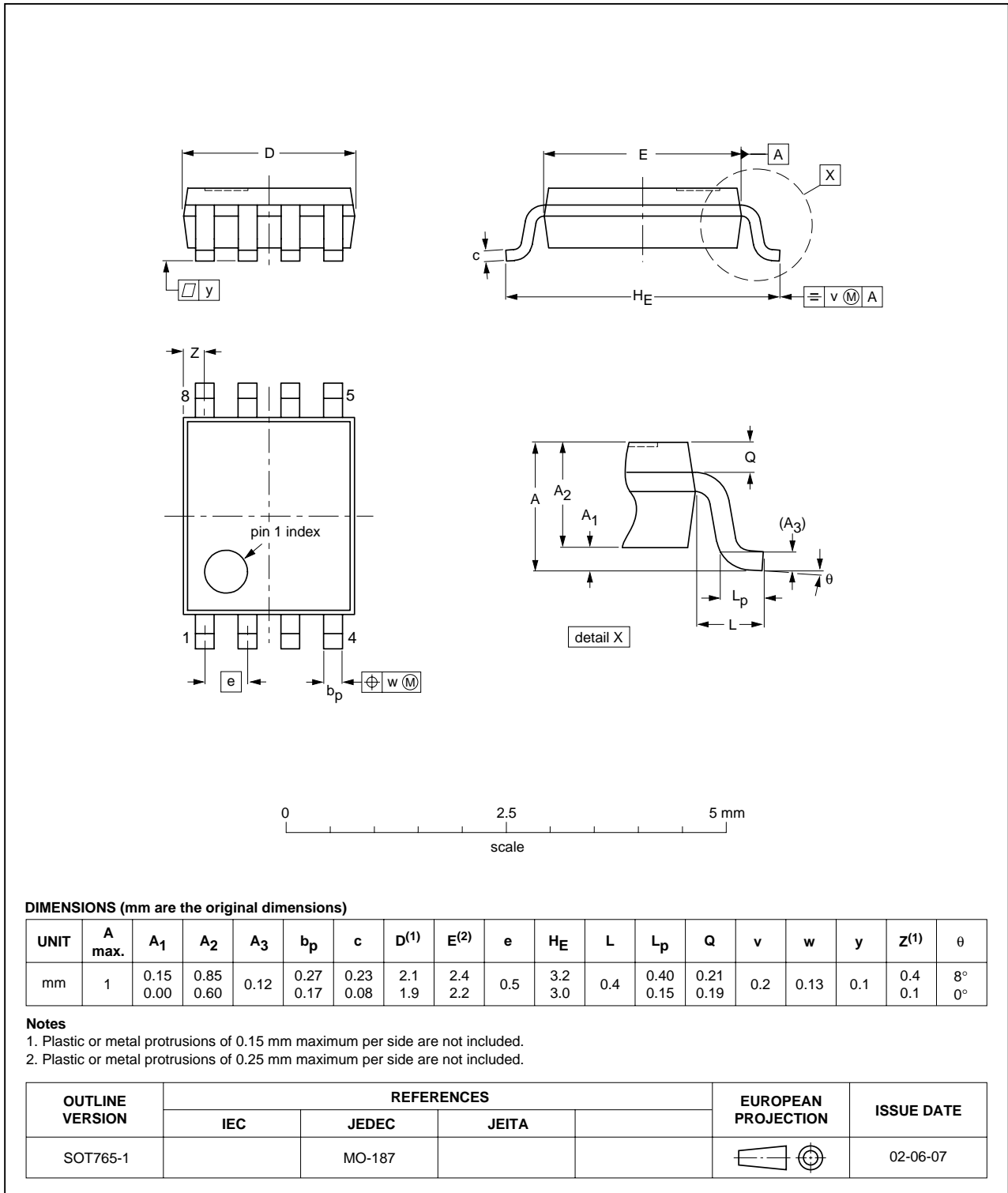


Fig 9. Package outline SOT765-1 (VSSOP8)

XSON8: plastic extremely thin small outline package; no leads; 8 terminals; body 1 x 1.95 x 0.5 mm

SOT833-1

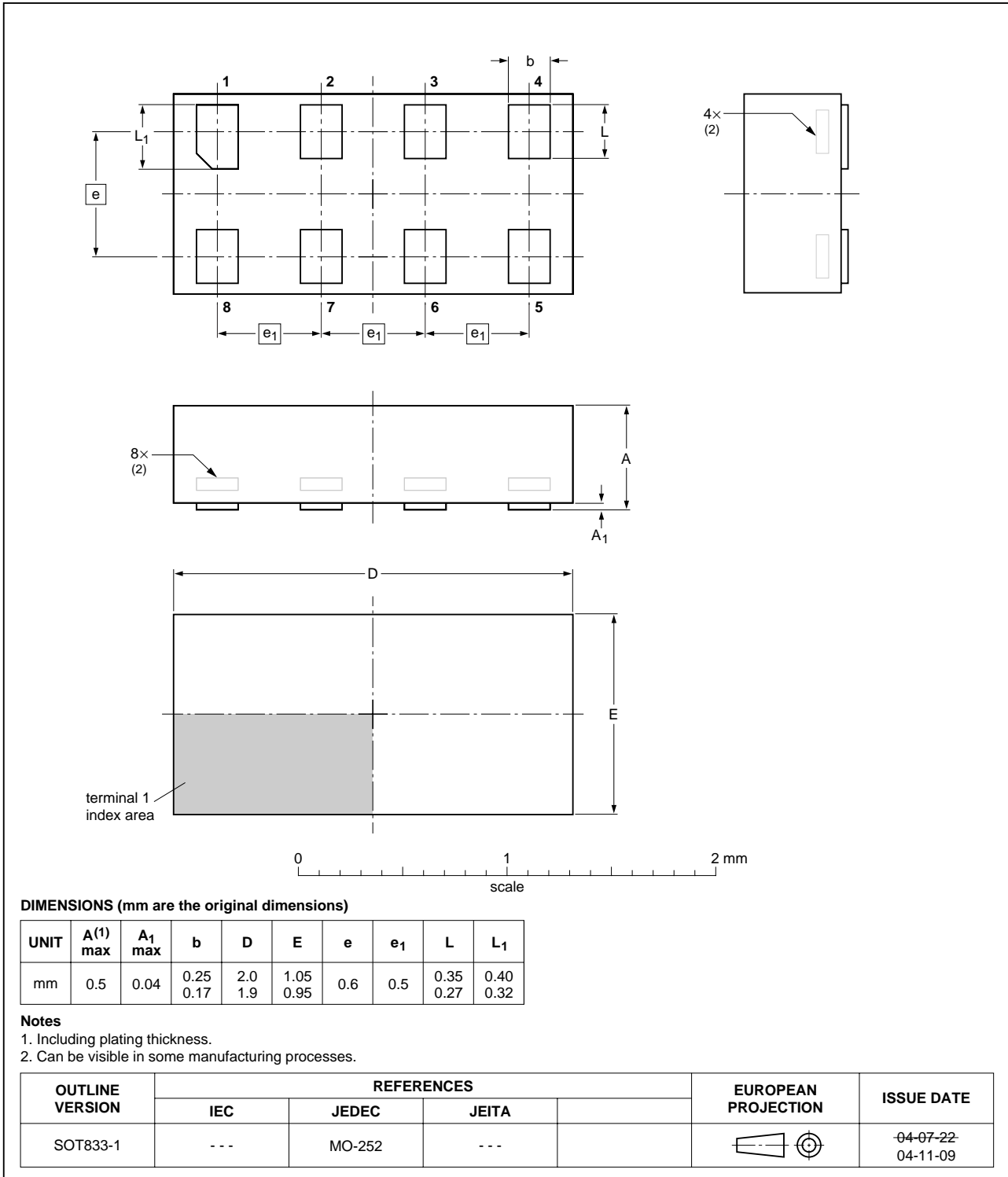


Fig 10. Package outline SOT833-1 (XSON8)

XQFN8: plastic extremely thin quad flat package; no leads; 8 terminals; body 1.6 x 1.6 x 0.5 mm

SOT902-1

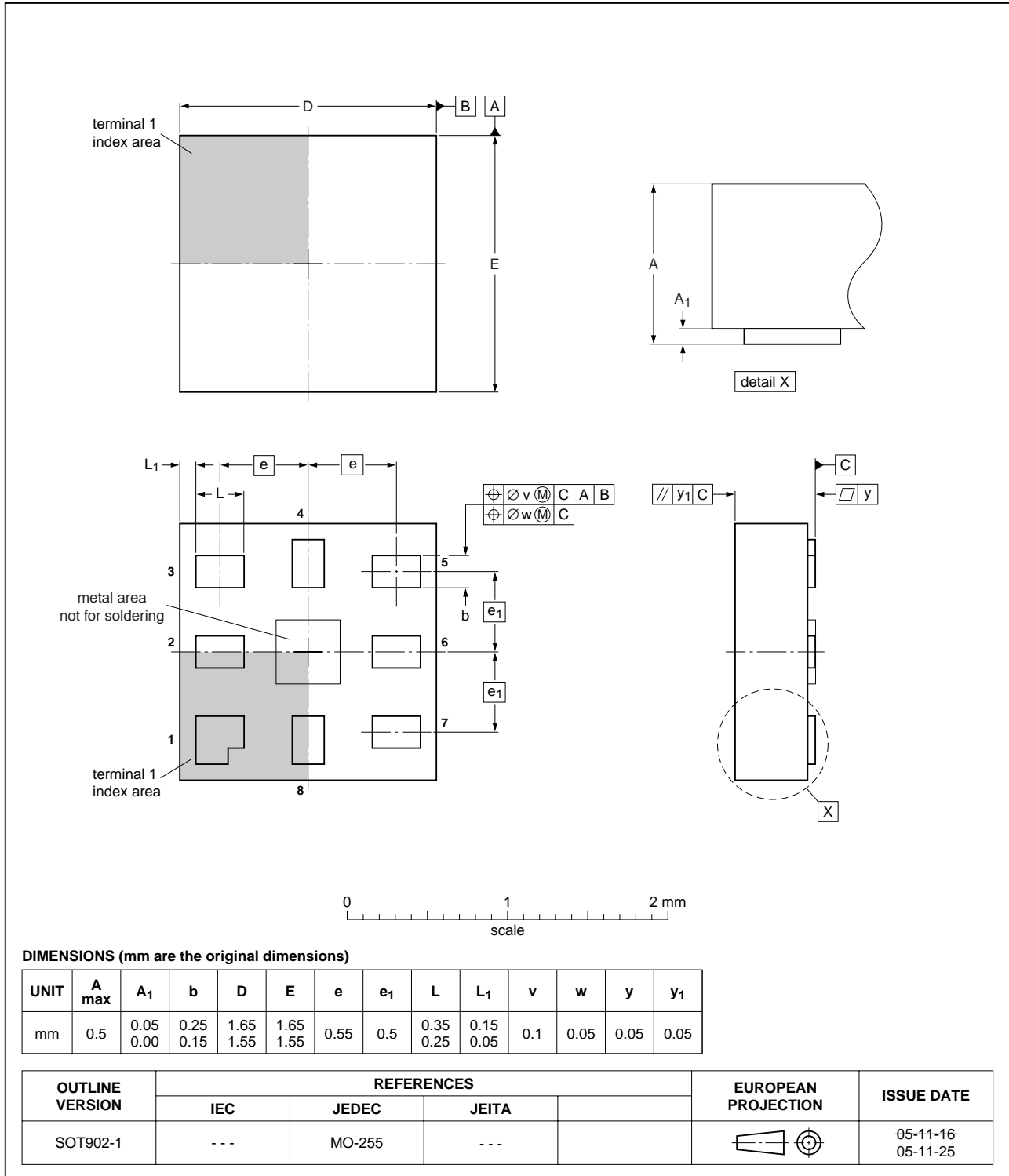


Fig 11. Package outline SOT902-1 (XQFN8)

14. Abbreviations

Table 11. Abbreviations

Acronym	Description
CDM	Charged Device Model
CMOS	Complementary Metal Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
MM	Machine Model
TTL	Transistor-Transistor Logic

15. Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74AUP2G38_1	20061016	Product data sheet	-	-

16. Legal information

16.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <http://www.nxp.com>.

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