

74HCT126

QUADRUPLE 3-STATE BUFFERS OE HIGH

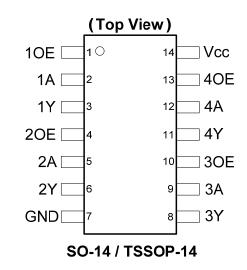
Description

The 74HCT126 provides provides four independent buffer gates with 3-state outputs. Each buffer has a separate enable pin that if driven with a low logic level places the corresponding output in the high impedance state. The device is designed for operation with a power supply range of 4.5V to 5.5V.

Features

- Wide Supply Voltage Range from 4.5V to 5.5V
- Pin Compatible with Low Power Schottky (LSTTL)
- Inputs Are TTL Voltage Level Compatible
- Sinks or sources 4mA at $V_{CC} = 4.5V$
- CMOS low power consumption
- Schmitt Trigger Action at All Inputs
- ESD Protection Exceeds JESD 22
 - 200-V Machine Model (A115-A)
 - 2000-V Human Body Model (A114-A)
 - Exceeds 1000-V Charged Device Model (C101C)
- Range of Package Options SO-14 and TSSOP-14
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Pin Assignments



Applications

- General Purpose Logic
- Wide array of products such as:
 - PCs, networking, notebooks, netbooks
 - Computer peripherals, hard drives, CD/DVD ROM
 - TV, DVD, DVR, set top box

Notes:

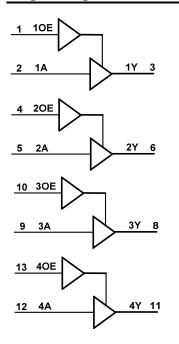
- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 - 2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 - 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



Pin Descriptions

Pin Number	Pin Name	Description
1	10E	Data Enable Input (active high)
2	1A	Data Input
3	1Y	Data Output
4	20E	Data Enable Input (active high)
5	2A	Data Input
6	2Y	Data Output
7	GND	Ground
8	3Y	Data Output
9	ЗA	Data Input
10	30E	Data Enable Input (active high)
11	4Y	Data Output
12	4A	Data Input
13	40E	Data Enable Input (active high)
14	Vcc	Supply Voltage

Logic Diagram



Function Table

Inp	Output	
OE	Α	Y
Н	Н	Н
Н	L	L
L	Х	Z



Absolute Maximum Ratings (Note 4) (@T_A = +25°C, unless otherwise specified.)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	KV
ESD CDM	Charged Device Model ESD Protection	1	KV
ESD MM	Machine Model ESD Protection	200	V
Vcc	Supply Voltage Range	-0.5 to +7.0	V
VI	Input Voltage Range (Note 5)	-0.5 to +7.0	V
I _{IK}	Input Clamp Current $V_{I} < -0.5V$ or Vi > $V_{CC} + 0.5V$	±20	mA
loк	Output Clamp Current $V_0 < -0.5V$ or $V_0 > V_{CC} + 0.5V$	±20	mA
I _O	Continuous Output Current -0.5V < Vo V _{CC} +0.5V	+/-25	mA
Icc	Continuous Current Through V _{CC}	50	mA
I _{GND}	Continuous Current Through GND	-50	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T _{STG}	Storage Temperature	-65 to +150	°C
P _{TOT}	Total Power Dissipation	500	mW

Notes: 4. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

5. Input Voltage cannot exceed V_{CC} to the extent the Maximum clamp current is exceeded.

Recommended Operating Conditions (Note 6) (@T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	Supply Voltage		4.5	5.5	V
VI	Input Voltage		0	V _{CC}	V
Vo	Output Voltage		0	V _{CC}	V
Δt/ΔV	Input Transition Rise or Fall Rate	$V_{CC} = 4.5V$ to 5.5V		500	ns/V
T _A	Operating Free-Air Temperature		-40	+125	°C

Note: 6. Unused inputs should be held at V_{CC} or Ground.

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Symphol	Deremeter	Test Conditions	N/	T _A = -40°	C to +85°C	T _A = -40°C	to +125°C	l lmit
Symbol	Parameter	rest conditions	V _{cc}	Min	Max	Min	Max	Unit
VIH	High-level Input Voltage		4.5V to 5.5V	2.0		2.0		V
VIL	Low-level Input Voltage		4.5V to 5.5V		0.8		0.8	V
N/	High-level Output	I _{OH} = -20μA	4.5V	4.4		4.4		V
V _{OH}	Voltage	I _{OH} = -4mA	4.5V	3.84		3.70		v
Max	Low-level Output	I _{OL} = 20μA	4.5V		0.1		0.1	V
Vol	Voltage	I _{OL} = 4.0mA	4.5V		0.33		0.4	v
loz	Z State Leakage Current	$V_{\rm O}$ = 0 to 5.5V	5.5V		± 5.0		± 10	μA
lı –	Input Current	$V_1 = GND$ to 6.0V	6.0V		± 1		± 1	μA
I _{CC}	Supply Current	$V_I = GND \text{ or } V_{CC}, I_O = 0$	6.0V		20		40	μA
ΔI _{CC}	Additional Supply Current	One Input at V_{CC} -2.1V Other Pins at V_{CC} or GND	4.5V to 5.5V		675		735	μA



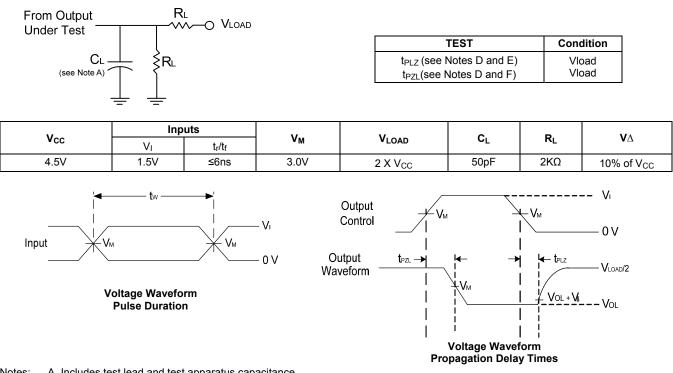
Switching Characteristics

Symbol	vmbol Parameter Test Conditions		itiono V.		Γ _A = +25°0	:	-40°C to +85°C	-40°C to +125°C	Unit											
Symbol	Falametei	Test conditions	Vcc	Min	Тур	Max	Max	Max	Unit											
t	Propagation				15	25	31	38	ns											
t _{PD}	Delay A_N to Y_N	Figure 1			_	15	25	51	50	115										
1	Enable Time				15	28	35	42	20											
t _{EN}	OE_N to Y_N		-	-	-	-	-	-	-	-	-	-	-	- 4.5V	4.5V		15	20	55	42
	DisableTime	C _L = 50pF			15	25	31	38	20											
t _{DIS}	OE_N to Y_N			_		_	15	25	51	30	ns									
tt	Transition Time				5	12	15	18	ns											

Operating Characteristics (@T_A = +25°C, unless otherwise specified.)

Parameter		Test Conditions	V _{CC} = 5.5V Typ	Unit
C _{pd}	Power dissipation capacitance per gate	f = 1MHz	24	pF
Cl	Input Capacitance	$V_I = V_{CC} - or GND$	3.5	pF

Parameter Measurement Information



A. Includes test lead and test apparatus capacitance. Notes:

B. All pulses are supplied at pulse repetition rate ≤ 1 MHz.

C. The inputs are measured one at a time with one transition per measurement.

D. For the open drain device tPLZ and tPZL are the same as tPD.

E. t_{PZL} is measured at V_M.

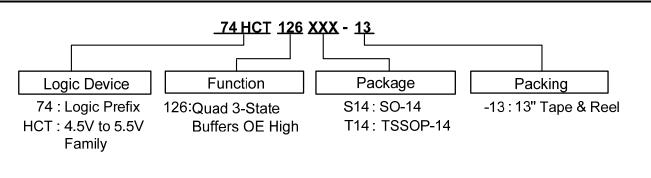
D. $t_{PLZ}\,$ is measured at V_OL +V_{\Delta.}

F. A Thevenin equivalent load may be used in place of V_{CC} X 2 and resistor divider.

Figure 1 Load Circuit and Voltage Waveforms



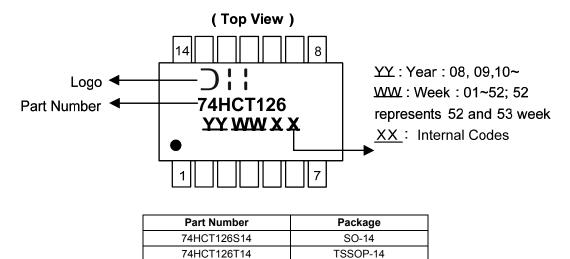
Ordering Information



	Device	Package Code	Packaging	7" Tape a	and Reel
	Device	Package Coue	Fackaging	Quantity	Part Number Suffix
Lead-free Green	74HCT126S14-13	S14	SO-14	2500/Tape & Reel	-13
Lead-free Green	74HCT126T14-13	T14	TSSOP-14	2500/Tape & Reel	-13

Marking Information

(1) SO-14, TSSOP-14

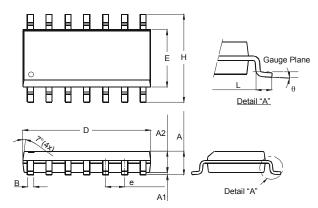




Package Outline Dimensions (All dimensions in mm.)

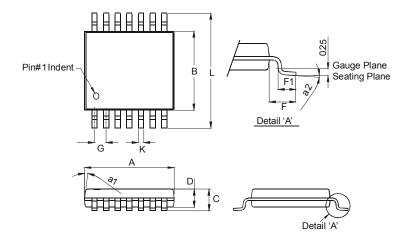
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

Package Type: SO-14



	SO-14	
Dim	Min	Max
Α	1.47	1.73
A1	0.10	0.25
A2	1.45	Тур
В	0.33	0.51
D	8.53	8.74
Е	3.80	3.99
е	1.27	Тур
Н	5.80	6.20
L	0.38	1.27
θ	0°	8°
All Di	mensions	s in mm

Package Type: TSSOP-14



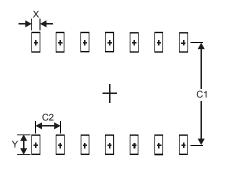
	TSSOP-14				
Dim	Min	Max			
a1	7° (4X)			
a2	0°	8°			
Α	4.9	5.10			
в	4.30	4.50			
C	_	1.2			
D	0.8	1.05			
F	1.00	Тур			
F1	0.45	0.75			
G	0.65	Тур			
κ	0.19	0.30			
Ĺ	6.40	Тур			
All Dir	nensions	s in mm			



Suggested Pad Layout

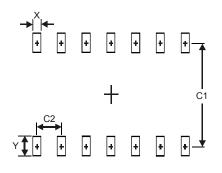
Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for latest version.

Package Type: SO-14



Dimensions	Value (in mm)
Х	0.60
Y	1.50
C1	5.4
C2	1.27

Package Type: TSSOP-14



Dimensions	Value (in mm)
Х	0.45
Y	1.45
C1	5.9
C2	0.65



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