

SANYO

No 2168B

L78MR00 Series

5 to 12V 0.5A 5-Pin
Voltage Regulators with Reset Function

The L78MR00 series, 500mA general-purpose voltage regulator ICs provide reset output signal for micro computers.

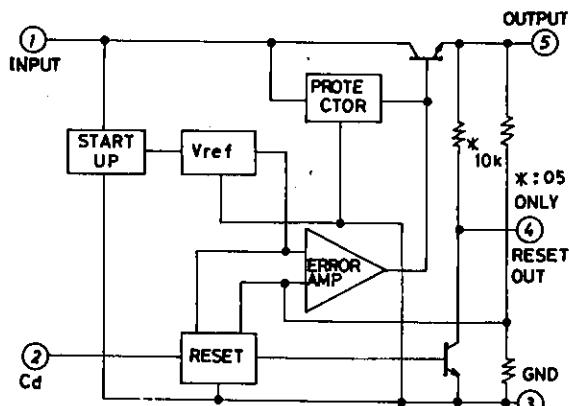
Features

- Reset function (power supply voltage monitor : Generates a reset signal at a power-on and temporal power-down).
- Output voltage L78MR05 : 5V (Reset output On-chip pull-up resistor)
 L78MR06 : 6V (Reset output Open collector)
 L78MR08 : 8V (Reset output Open collector)
 L78MR09 : 9V (Reset output Open collector)
 L78MR12 : 12V (Reset output Open collector)
- Output current 500mA
- On-chip ASO protector.
- On-chip thermal protector.
- On-chip over current limiter.
- The use of package TO220-5H facilitates easy mounting and thermal design.
- Delay time (t_d) may be set by an external capacitor.

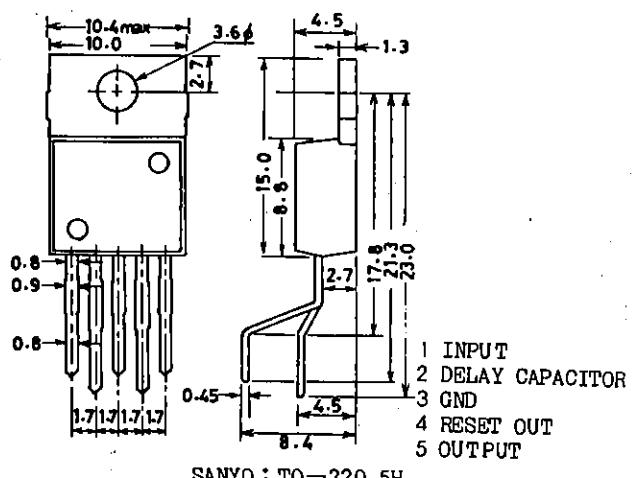
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[Common to L78MR00 Series]**Maximum Ratings at $T_a = 25^\circ\text{C}$**

			unit
Maximum Input Voltage	V_{IN} max		35 V
Reset Pin Supply Voltage	V_{reset}		35 V
Allowable Power Dissipation	P_d max	No fin	1.75 W
		$T_C = 25^\circ\text{C}$	20 W
Operating Temperature	T_{opr}		-30 to +80 °C
Storage Temperature	T_{stg}		-55 to +150 °C

Equivalent Circuit Block DiagramUnit (resistance: Ω)**Package Dimensions
(unit: mm)**

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L78MR00 Series

[L78MR05]

Recommended Operating Conditions at Ta = 25°C

	V _{IN}		7.5 to 20	unit
	I _O		5 to 500	mA
Operating Characteristics at Ta = 25°C, V_{IN} = 10V, I_O = 0.35A, C_O = 10μF				
Output Voltage	V _{O1}	T _j = 25°C	4.8	V
	V _{O2}	7V ≤ V _{IN} ≤ 20V, 5mA ≤ I _O ≤ 0.35A	4.75	V
Line Regulation	ΔV _O	LN1 T _j = 25°C, 7V ≤ V _{IN} ≤ 25V, I _O = 0.2A LN2 T _j = 25°C, 8V ≤ V _{IN} ≤ 25V, I _O = 0.2A	1.0 0.5	mV mV
Load Regulation	ΔV _O	LD1 T _j = 25°C, 5mA ≤ I _O ≤ 0.5A LD2 T _j = 25°C, 5mA ≤ I _O ≤ 0.2A	3.0 1.5	mV mV
Current Dissipation	I _{CC}	T _j = 25°C	3.4	mA
Current Dissipation Variation (Line)	ΔI _{CC}	LN 8V ≤ V _{IN} ≤ 25V, I _O ≤ 0.2A	0.8	mA
Current Dissipation Variation (Load)	ΔI _{CC}	LD 5mA ≤ I _O ≤ 0.35A	0.5	mA
Output Noise Voltage	V _{NO}	I _O = 5mA, 10Hz ≤ f ≤ 100kHz	60	μV
Ripple Rejection	R _{r1}	T _j = 25°C, f = 120Hz, 8V ≤ V _{IN} ≤ 18V, I _O = 0.1A	62	dB
	R _{r2}	T _j = 25°C, f = 120Hz, 8V ≤ V _{IN} ≤ 18V, I _O = 0.3A	62	dB
Dropout Voltage	V _{drop}		2.0	V
Peak Output Current	I _{OP}	T _j = 25°C	1.1	A
Short Circuit Current	I _{osc}	T _j = 25°C, V _{IN} = 35V	0.02	A
Temperature Coefficient of Output Voltage	ΔV _O /ΔT	I _O = 5mA, T _j = 25 to 125°C	-0.3	mV/°C
'L' Reset Output Voltage	V _{ORL}	V _O ≤ 4.5V, I _O = 5mA	0.2	V
Reset Threshold Voltage	V _{RT}	I _O = 5mA	V _O - 0.3V _O - 0.2	V
Reset Hysteresis Voltage	V _{RTH}	I _O = 5mA	100	mV
Reset Output Delay Time	t _d	C _d = 0.1μF, I _O = 5mA	10	ms

[L78MR06]

Recommended Operating Conditions at Ta = 25°C

	V _{IN}		8.5 to 21	unit
	I _O		5 to 500	mA
	I _{OR}	V _O ≤ 5.64V	20	mA max
Operating Characteristics at Ta = 25°C, V_{IN} = 11V, I_O = 0.35A, C_O = 10μF				
Output Voltage	V _{O1}	T _j = 25°C	5.75	V
	V _{O2}	8V ≤ V _{IN} ≤ 21V, 5mA ≤ I _O ≤ 0.35A	5.7	V
Line Regulation	ΔV _O	LN1 T _j = 25°C, 8V ≤ V _{IN} ≤ 25V, I _O = 0.2A LN2 T _j = 25°C, 9V ≤ V _{IN} ≤ 25V, I _O = 0.2A	1.2 0.6	mV mV
Load Regulation	ΔV _O	LD1 T _j = 25°C, 5mA ≤ I _O ≤ 0.5A LD2 T _j = 25°C, 5mA ≤ I _O ≤ 0.2A	4.0 2.0	mV mV
Current Dissipation	I _{CC}	T _j = 25°C	3.4	mA
Current Dissipation Variation (Line)	ΔI _{CC}	LN 9V ≤ V _{IN} ≤ 25V, I _O ≤ 0.2A	0.8	mA
Current Dissipation Variation (Load)	ΔI _{CC}	LD 5mA ≤ I _O ≤ 0.35A	0.5	mA
Output Noise Voltage	V _{NO}	I _O = 5mA, 10Hz ≤ f ≤ 100kHz	70	μV
Ripple Rejection	R _{r1}	T _j = 25°C, f = 120Hz, 9V ≤ V _{IN} ≤ 19V, I _O = 0.1A	59	dB
	R _{r2}	T _j = 25°C, f = 120Hz, 9V ≤ V _{IN} ≤ 19V, I _O = 0.3A	59	dB

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L78MR00 Series

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			min	typ	max	unit
Dropout Voltage	V _{drop}			2.0	2.5	V
Peak Output Current	I _{OP}	T _j =25°C		1.1		A
Short Circuit Current	I _{OSC}	T _j =25°C, V _{IN} =35V	0.02			A
Temperature Coefficient of Output Voltage	ΔV _O /ΔT	I _O =5mA, T _j =25 to 125°C	-0.4			mV/°C
'L' Reset Output Voltage	V _{ORL}	V _O ≤ 5.64V, I _{OR} =20mA, I _O =5mA		0.8		V
Reset Output Leakage Current	I _{RL}	V _R =35V		50	μA	
Reset Threshold Voltage	V _{RT}	I _O =5mA	V _O -0.36	V _O -0.24		V
Reset Hysteresis Voltage	V _{RTH}	I _O =5mA		120		mV
Reset Output Delay Time	t _d	C _d =0.1μF, I _O =5mA		10		ms

[L78MR08]

Recommended Operating Conditions at Ta=25°C

				unit
Input Voltage	V _{IN}		10.5 to 23	V
Output Current	I _O		5 to 500	mA
Reset Output Current	I _{OR}	V _O ≤ 7.2V	20	mA max

Operating Characteristics at Ta=25°C, V_{IN}=14V, I_O=0.35A, C_d=10μF

			min	typ	max	unit
Output Voltage	V _{O1}	T _j =25°C	7.7	8.0	8.3	V
	V _{O2}	10.5V≤ V _{IN} ≤ 23V, 5mA≤ I _O ≤ 0.35A	7.6		8.4	V
Line Regulation	ΔV _O	LN1 T _j =25°C, 10.5V≤ V _{IN} ≤ 25V, I _O =0.2A		1.6	100	mV
		LN2 T _j =25°C, 11V≤ V _{IN} ≤ 25V, I _O =0.2A		0.8	50	mV
Load Regulation	ΔV _O	LD1 T _j =25°C, 5mA≤ I _O ≤ 0.5A		5.0	160	mV
		LD2 T _j =25°C, 5mA≤ I _O ≤ 0.2A		2.0	80	mV
Current Dissipation	I _{CC}	T _j =25°C		3.5	6.0	mA
Current Dissipation Variation (Line)	ΔI _{CC}	LN 10.5V≤ V _{IN} ≤ 25V, I _O ≤ 0.2A			0.8	mA
Current Dissipation Variation (Load)	ΔI _{CC}	LD 5mA≤ I _O ≤ 0.35A		0.5		mA
Output Noise Voltage	V _{NO}	I _O =5mA, 10Hz≤ f≤ 100kHz		100		μV
Ripple Rejection	R _{r1}	T _j =25°C, f=120Hz, 11.5V≤ V _{IN} ≤ 21.5V, I _O =0.1A	56	75		dB
	R _{r2}	T _j =25°C, f=120Hz, 11.5V≤ V _{IN} ≤ 21.5V, I _O =0.3A	56	71		dB
Dropout Voltage	V _{drop}			2.0	2.5	V
Peak Output Current	I _{OP}	T _j =25°C		1.1		A
Short Circuit Current	I _{OSC}	T _j =25°C, V _{IN} =35V	0.02			A
Temperature Coefficient of Output Voltage	ΔV _O /ΔT	I _O =5mA, T _j =25 to 125°C	-0.7			mV/°C
'L' Reset Output Voltage	V _{ORL}	V _O ≤ 7.2V, I _{OR} =20mA, I _O =5mA		0.8		V
Reset Output Leakage Current	I _{RL}	V _R =35V		50	μA	
Reset Threshold Voltage	V _{RT}	I _O =5mA	V _O -0.48	V _O -0.32		V
Reset Hysteresis Voltage	V _{RTH}	I _O =5mA		160		mV
Reset Output Delay Time	t _d	C _d =0.1μF, I _O =5mA		10		ms

L78MR00 Series

[L78MR09]

Recommended Operating Conditions at $T_a = 25^\circ C$

					unit
Input Voltage	V_{IN}			12 to 24	V
Output Current	I_o			5 to 500	mA
Reset Output Current	I_{oR}	$V_o \leq 8.1V$		20	mA max
Operating Characteristics at $T_a = 25^\circ C$, $V_{IN} = 15V$, $I_o = 0.35A$, $C_o = 10\mu F$					
Output Voltage	V_{o1}	$T_j = 25^\circ C$	min	8.6	typ 9.0
	V_{o2}	$11.5V \leq V_{IN} \leq 24V$, $5mA \leq I_o \leq 0.35A$		8.5	max 9.4 9.5 V
Line Regulation	ΔV_o	LN1 $T_j = 25^\circ C$, $11.5V \leq V_{IN} \leq 25V$, $I_o = 0.2A$		1.6	100 mV
		LN2 $T_j = 25^\circ C$, $12V \leq V_{IN} \leq 25V$, $I_o = 0.2A$		0.8	50 mV
Load Regulation	ΔV_o	LD1 $T_j = 25^\circ C$, $5mA \leq I_o \leq 0.5A$		5.0	180 mV
		LD2 $T_j = 25^\circ C$, $5mA \leq I_o \leq 0.2A$		3.0	90 mV
Current Dissipation	I_{CC}	$T_j = 25^\circ C$		3.5	6.0 mA
Current Dissipation Variation (Line)	ΔI_{CC}	LN $11.5V \leq V_{IN} \leq 25V$, $I_o \leq 0.2A$		0.8	mA
Current Dissipation Variation (Load)	ΔI_{CC}	LD $5mA \leq I_o \leq 0.35A$		0.5	mA
Output Noise Voltage Ripple Rejection	V_{NO}	$I_o = 5mA$, $10Hz \leq f \leq 100kHz$		110	μV
	R_{r1}	$T_j = 25^\circ C$, $f = 120Hz$, $12V \leq V_{IN} \leq 22V$, $I_o = 0.1A$	56	73	dB
	R_{r2}	$T_j = 25^\circ C$, $f = 120Hz$, $12V \leq V_{IN} \leq 22V$, $I_o = 0.3A$	56	70	dB
Dropout Voltage	V_{drop}			2.0	2.5 V
Peak Output Current	I_{OP}	$T_j = 25^\circ C$		1.1	A
Short Circuit Current	I_{osc}	$T_j = 25^\circ C$, $V_{IN} = 35V$		0.02	A
Temperature Coefficient of Output Voltage	$\Delta V_o/\Delta T$	$I_o = 5mA$, $T_j = 25$ to $125^\circ C$		-0.9	$mV/^\circ C$
'L' Reset Output Voltage	V_{oRL}	$V_o \leq 8.1V$, $I_{oR} = 20mA$, $I_o = 5mA$		0.8	V
Reset Output Leakage Current	I_{RL}	$V_R = 35V$		50	μA
Reset Threshold Voltage	V_{RT}	$I_o = 5mA$		$V_o - 0.54$	V
Reset Hysteresis Voltage	V_{RTH}	$I_o = 5mA$		180	mV
Reset Output Delay Time	t_d	$C_d = 0.1\mu F$, $I_o = 5mA$		10	ms

[L78MR12]

Recommended Operating Conditions at $T_a = 25^\circ C$

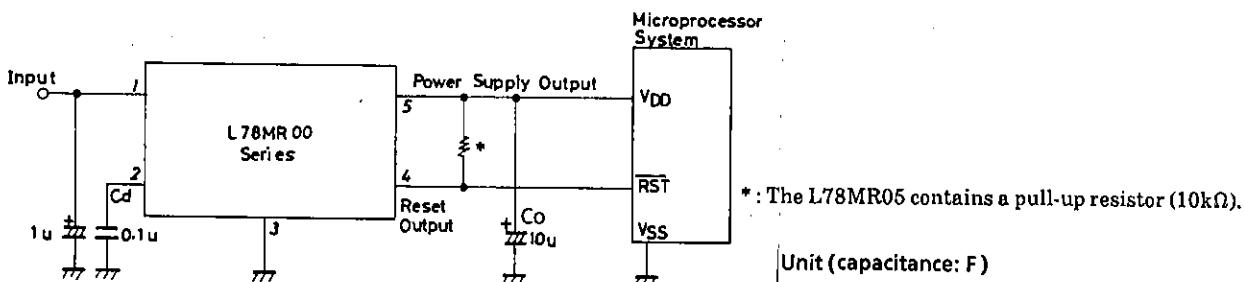
					unit
Input Voltage	V_{IN}			15 to 27	V
Output Current	I_o			5 to 500	mA
Reset Output Current	I_{oR}	$V_o \leq 10.8V$		20	mA max
Operating Characteristics at $T_a = 25^\circ C$, $V_{IN} = 19V$, $I_o = 0.35A$, $C_o = 10\mu F$					
Output Voltage	V_{o1}	$T_j = 25^\circ C$	min	11.5	typ 12.0
	V_{o2}	$14.5V \leq V_{IN} \leq 27V$, $5mA \leq I_o \leq 0.35A$		11.4	max 12.5 12.6 V
Line Regulation	ΔV_o	LN1 $T_j = 25^\circ C$, $14.5V \leq V_{IN} \leq 30V$, $I_o = 0.2A$		2.4	100 mV
		LN2 $T_j = 25^\circ C$, $16V \leq V_{IN} \leq 30V$, $I_o = 0.2A$		1.2	50 mV
Load Regulation	ΔV_o	LD1 $T_j = 25^\circ C$, $5mA \leq I_o \leq 0.5A$		7.0	240 mV
		LD2 $T_j = 25^\circ C$, $5mA \leq I_o \leq 0.2A$		4.0	120 mV
Current Dissipation	I_{CC}	$T_j = 25^\circ C$		3.7	6.0 mA

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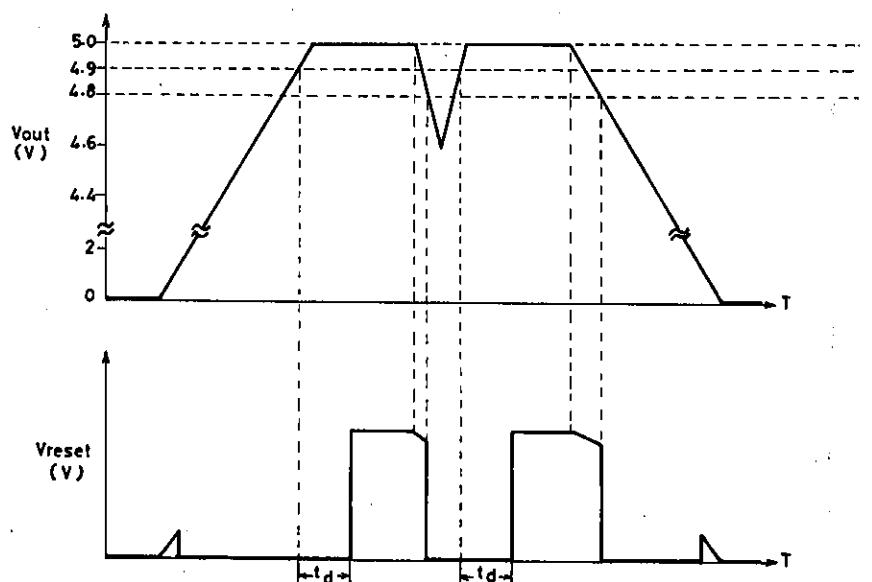
	ΔI_{CC}	LN	$14.5V \leq V_{IN} \leq 30V, I_o \leq 0.2A$	min	typ	max	unit
Current Dissipation Variation (Line)						0.8	mA
Current Dissipation Variation (Load)	ΔI_{CC}	LD	$5mA \leq I_o \leq 0.35A$			0.5	mA
Output Noise Voltage Ripple Rejection	V _{NO} R _{r1}		$I_o = 5mA, 10Hz \leq f \leq 100kHz$ $T_j = 25^\circ C, f = 120Hz,$ $15V \leq V_{IN} \leq 25V, I_o = 0.1A$		140 55	68	μV dB
	R _{r2}		$T_j = 25^\circ C, f = 120Hz,$ $15V \leq V_{IN} \leq 25V, I_o = 0.3A$		55	66	dB
Dropout Voltage	V _d _{rop}				2.0	2.5	V
Peak Output Current	I _{OP}		$T_j = 25^\circ C$		1.1		A
Short Circuit Current	I _{OSC}		$T_j = 25^\circ C, V_{IN} = 35V$		0.02		A
Temperature Coefficient of Output Voltage	$\Delta V_o / \Delta T$		$I_o = 5mA, T_j = 25 \text{ to } 125^\circ C$		-1.6		mV/°C
'L' Reset Output Voltage	V _{ORL}		$V_o \leq 10.8V, I_{oR} = 20mA, I_o = 5mA$		0.8		V
Reset Output Leakage Current	I _{RL}		$V_R = 35V$		50		μA
Reset Threshold Voltage	V _{RT}		$I_o = 5mA$			Vo - 0.72	V
Reset Hysteresis Voltage	V _{RTH}		$I_o = 5mA$			240	mV
Reset Output Delay Time	t _d		$C_d = 0.1\mu F, I_o = 5mA$			10	ms

Sample Application Circuit

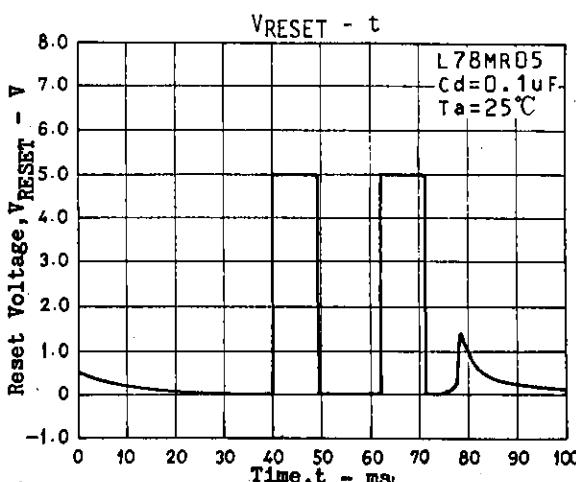
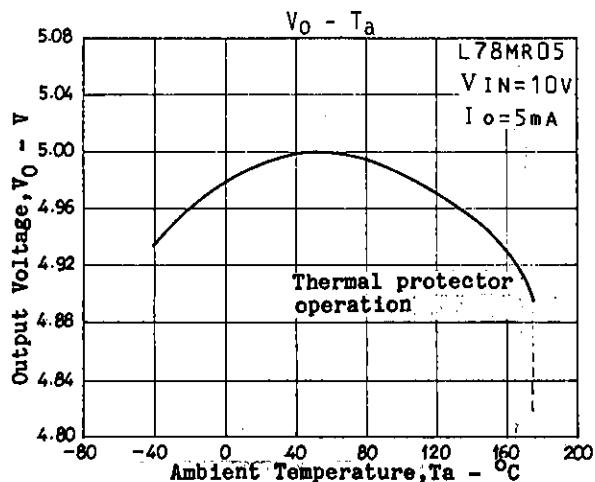
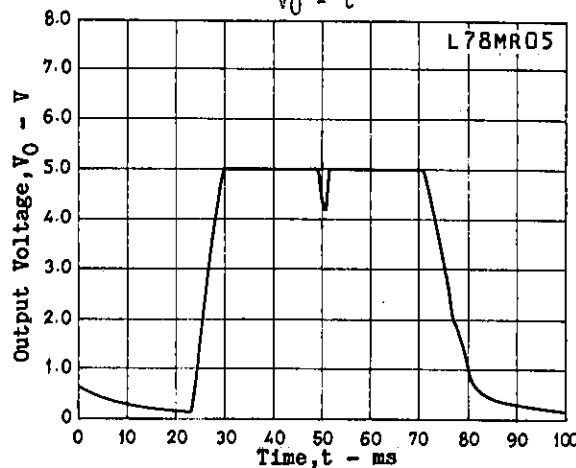
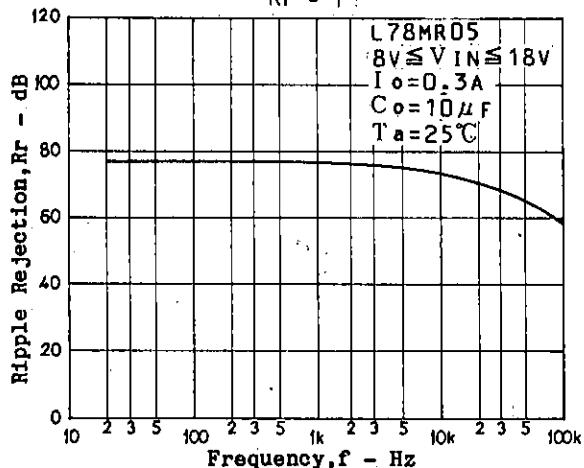
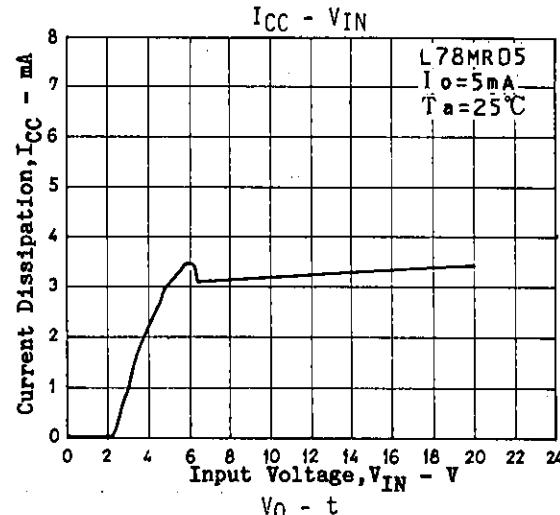
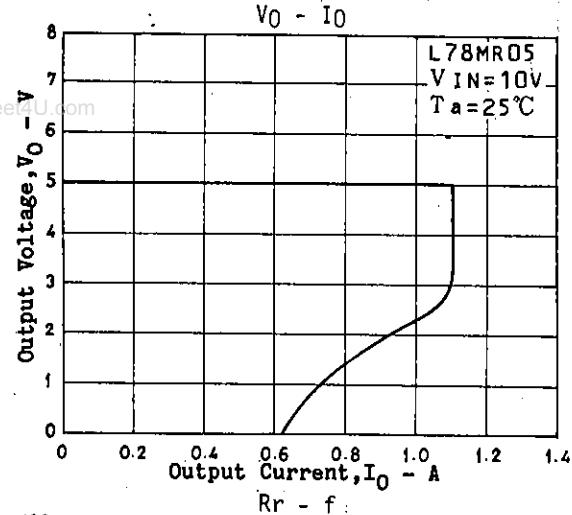
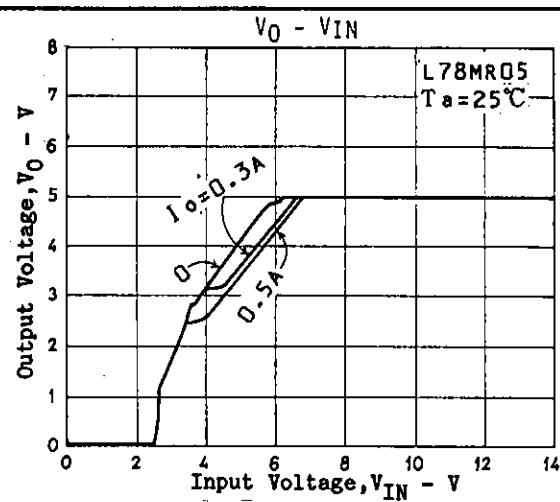
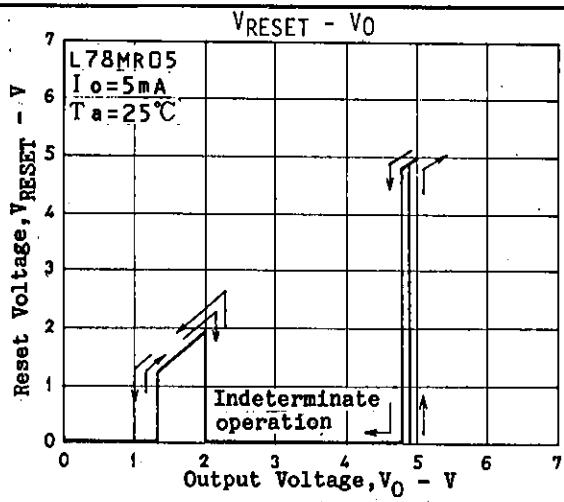


Note on use) If a load current (in particular, pulse-like load current) that is greater than a rated value is used, a reset signal may be generated due to the overload. Please keep it in mind.

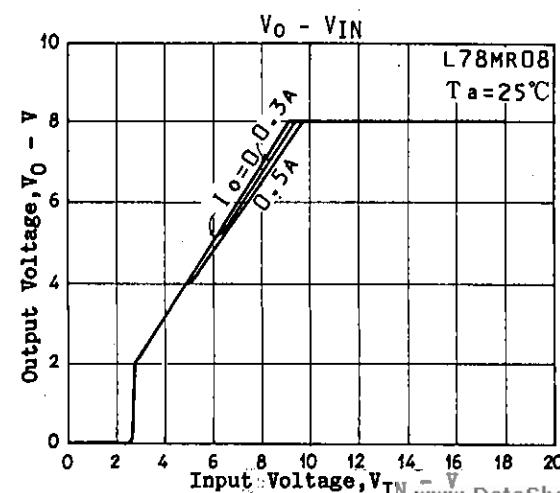
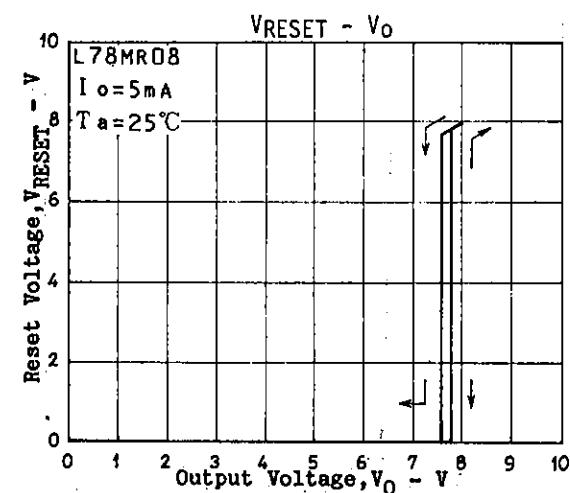
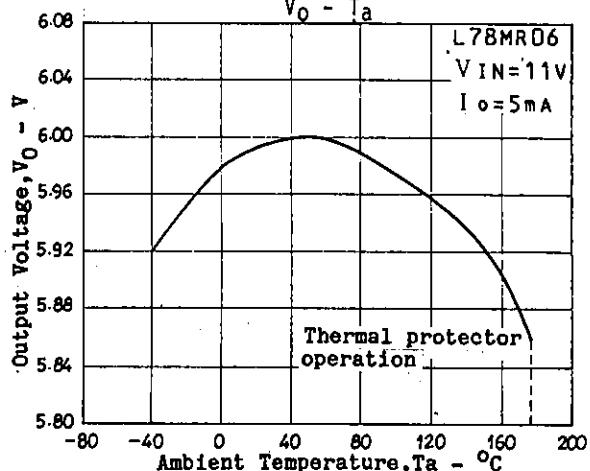
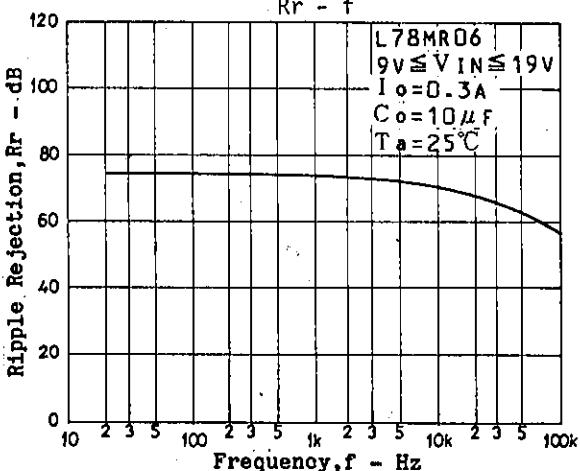
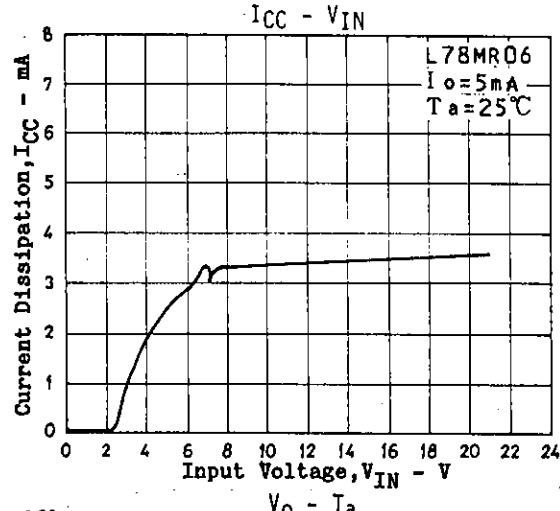
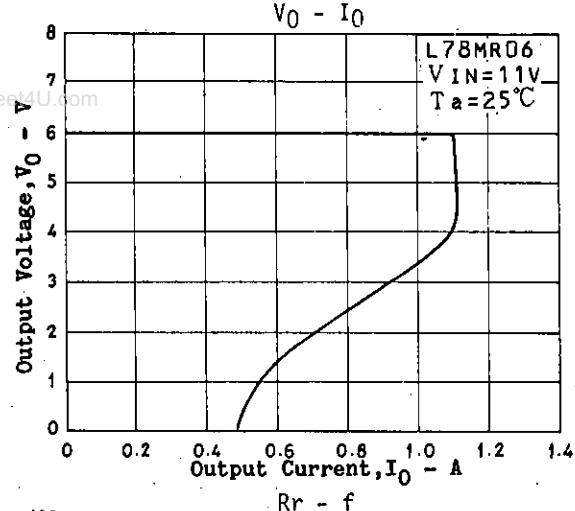
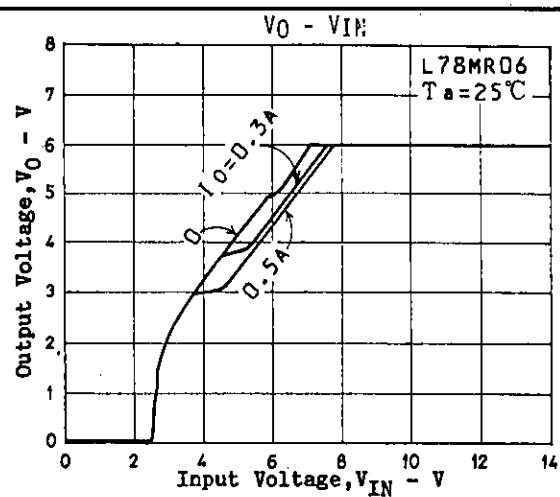
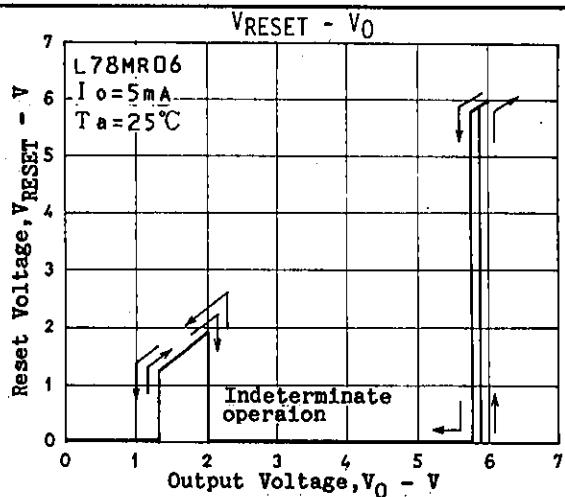
L78MR05 Reset Operation



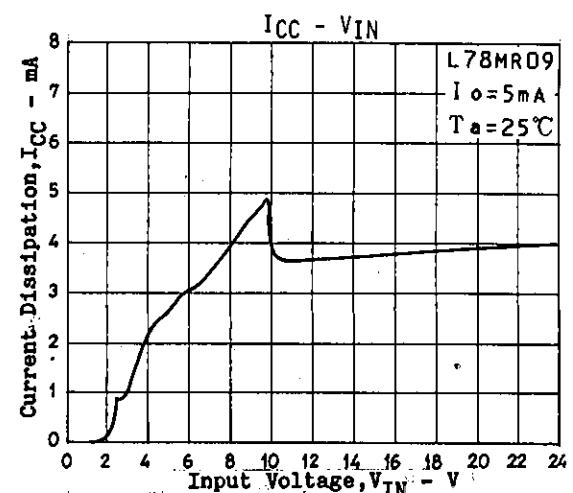
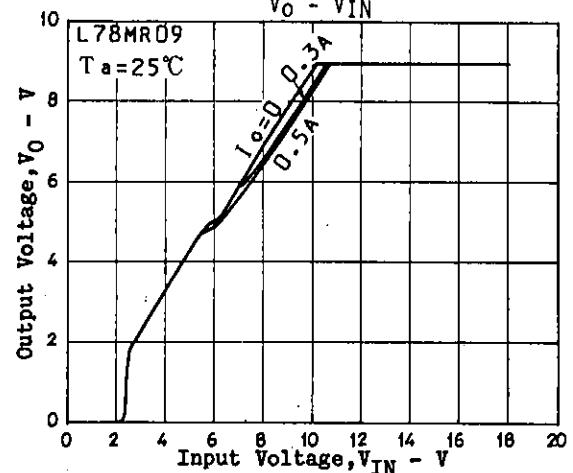
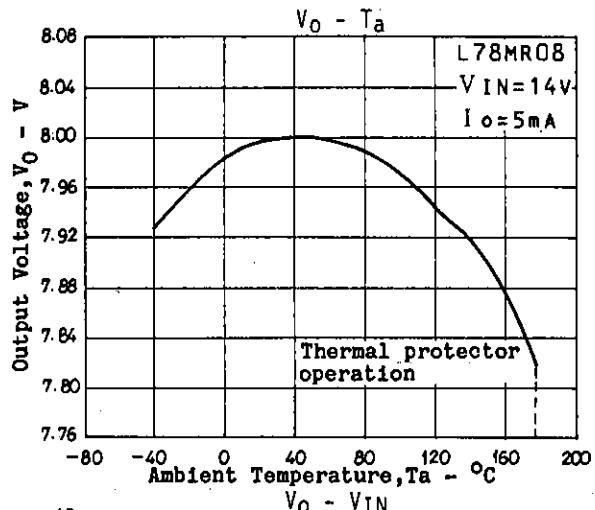
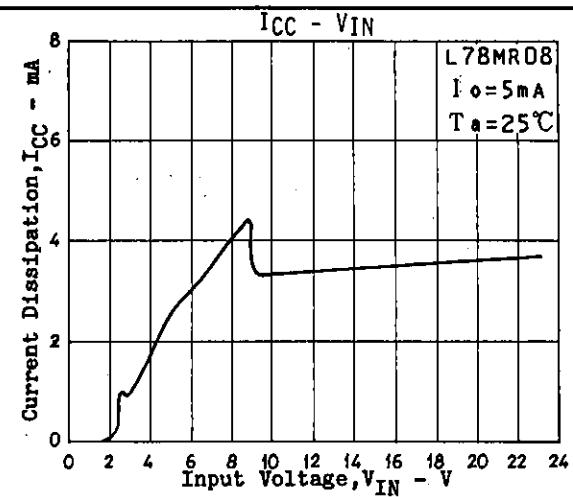
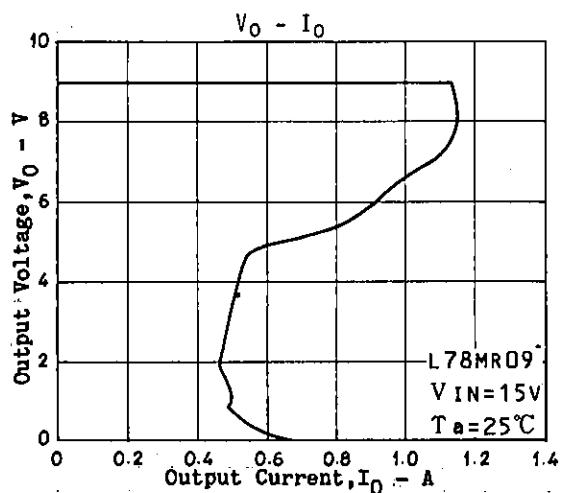
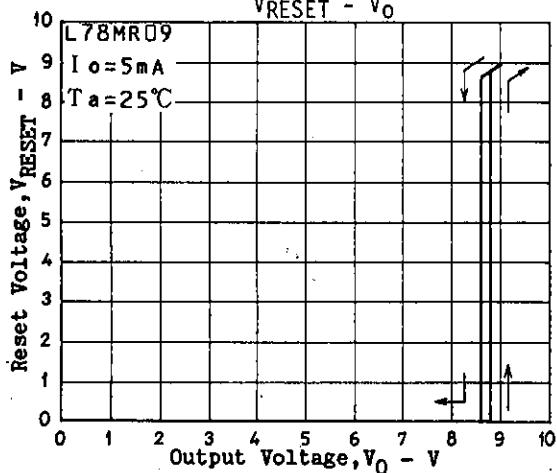
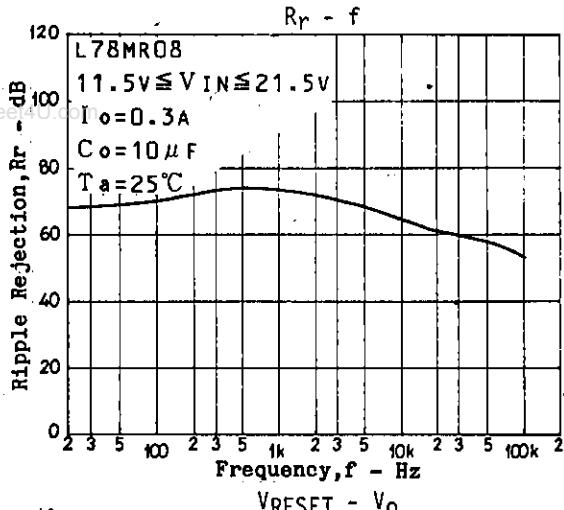
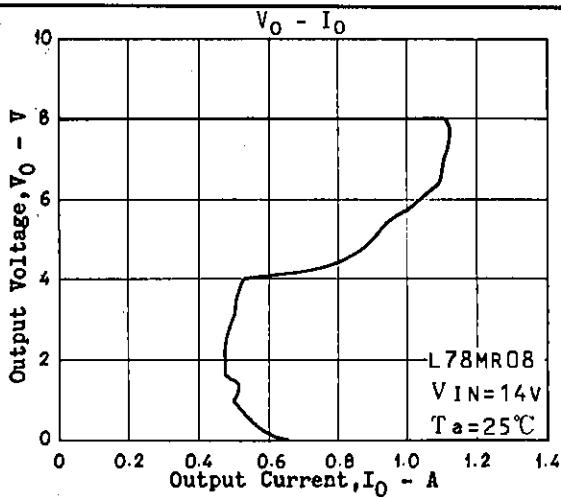
L78MR00 Series



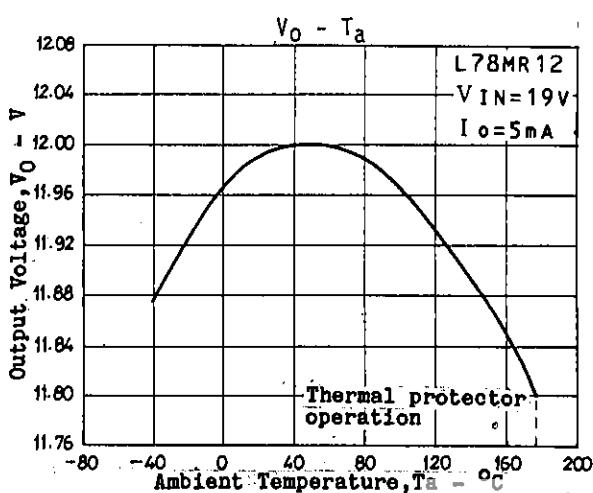
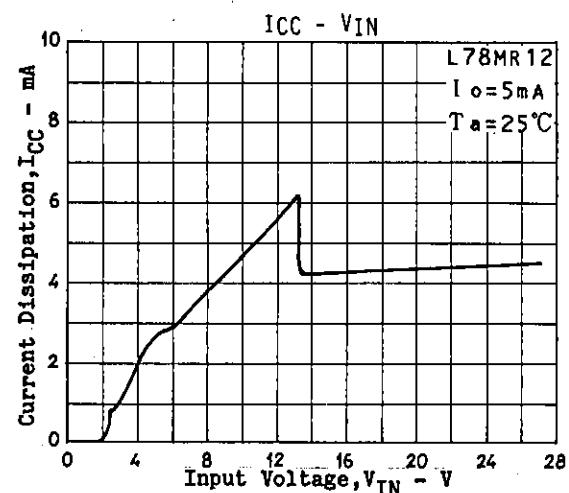
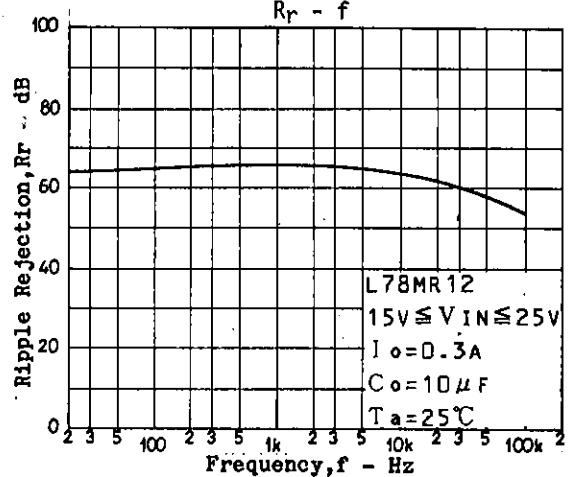
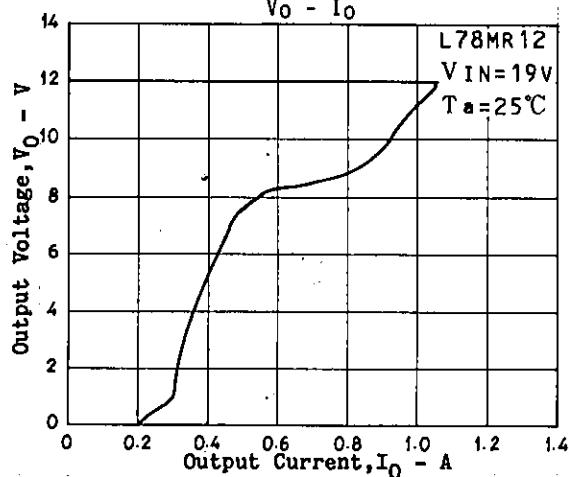
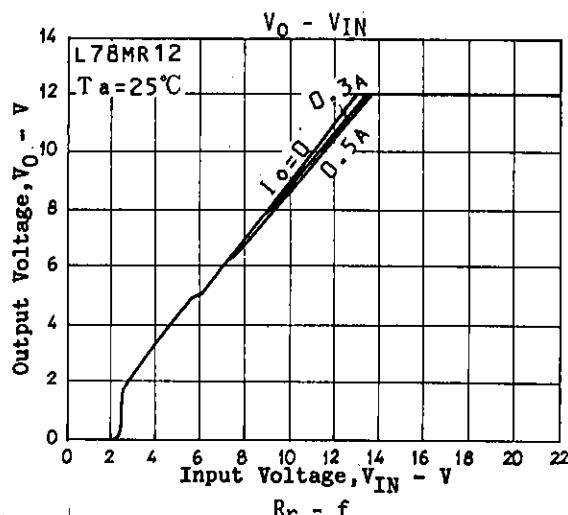
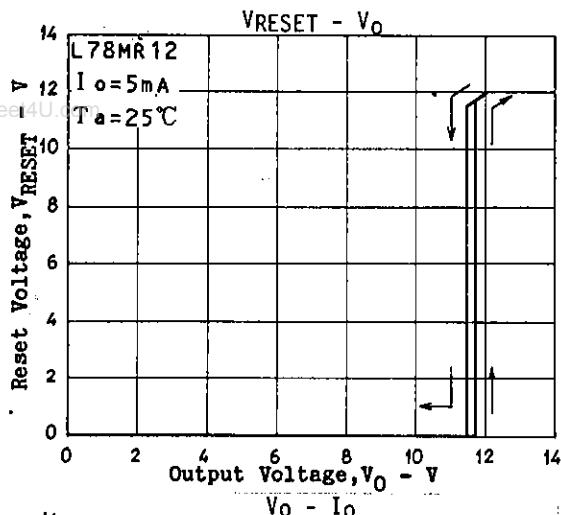
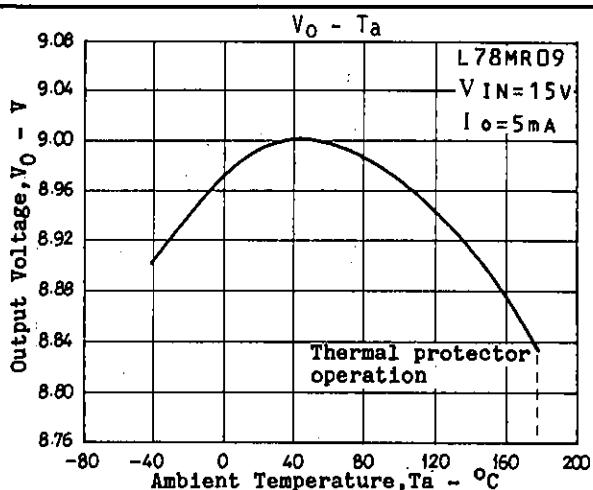
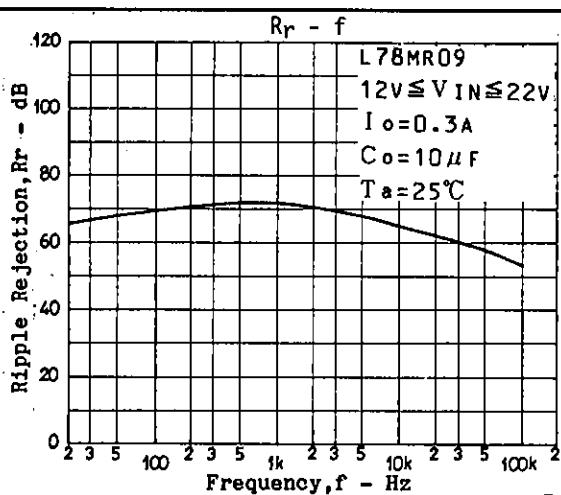
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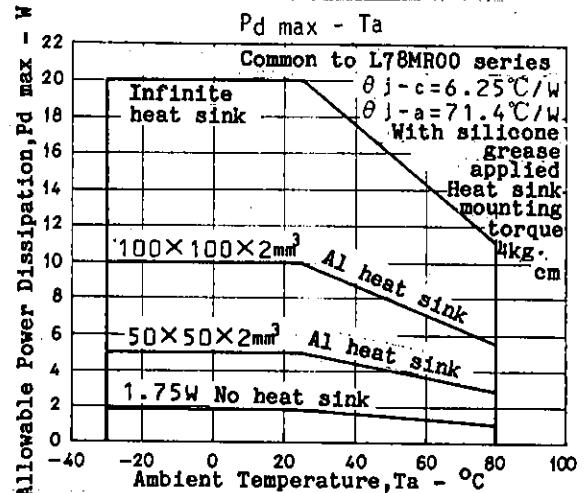
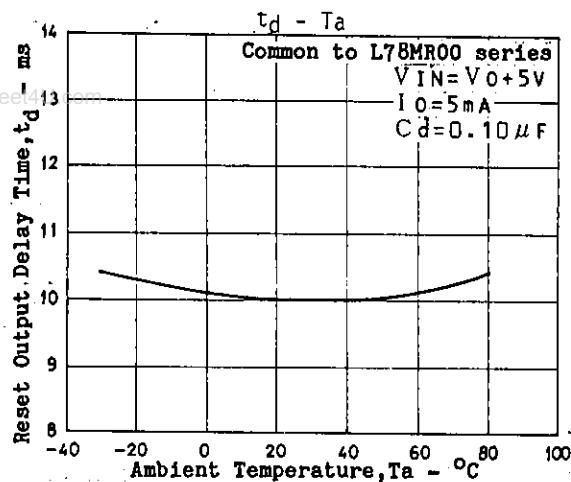
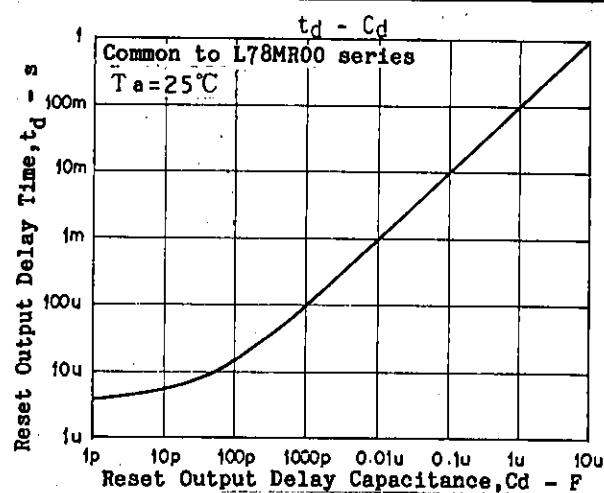
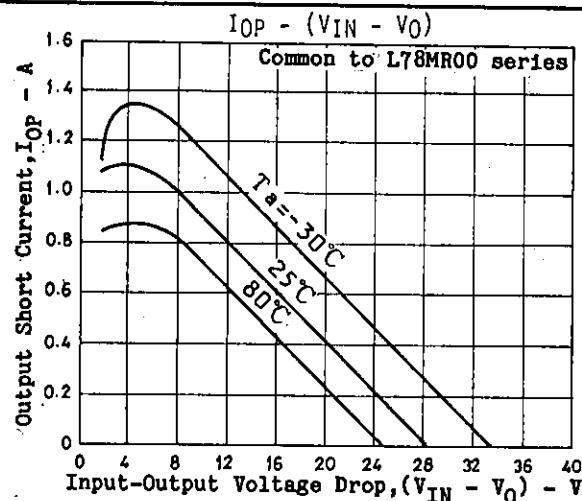
L78MR00 Series



L78MR00 Series



L78MR00 Series



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