

78NXX

LINEAR INTEGRATED CIRCUIT

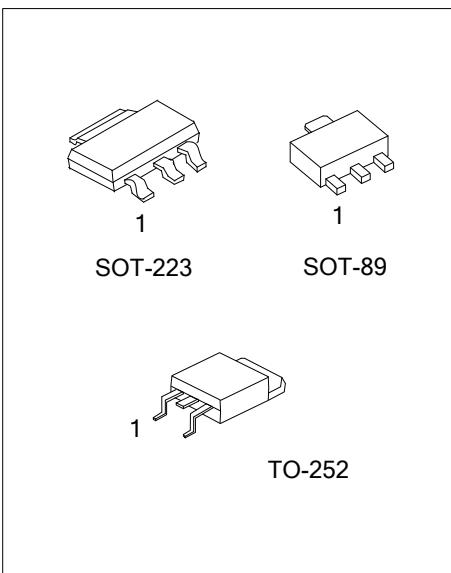
3-Terminal 300mA Positive Voltage Regulator

■ DESCRIPTION

The UTC **78NXX** family is monolithic fixed voltage regulator integrated circuit. They are suitable for applications requiring supply current up to 300mA.

■ FEATURES

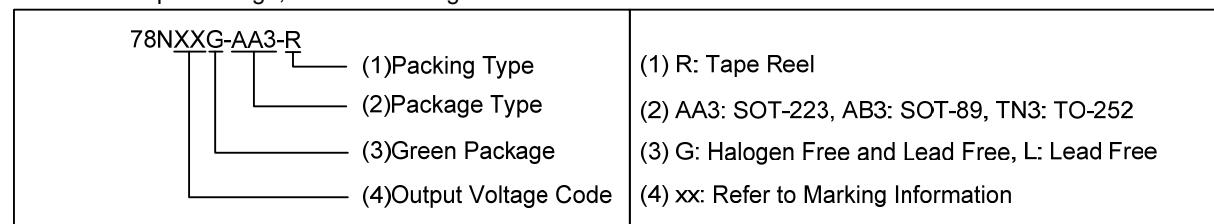
- * Output Current up to 300mA
 - * Fixed Output Voltage of 5V, 6V, 8V, 9V, 10V, 12V, 15V, 18V, 24V
 - * Thermal Overload Shutdown Protection
 - * Short Circuit Current Limiting



■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
78NXXL-AA3-R	78NXXG-AA3-R	SOT-223	O	G	I	Tape Reel
78NXXL-AB3-R	78NXXG-AB3-R	SOT-89	O	G	I	Tape Reel
78NXXL-TN3-R	78NXXG-TN3-R	TO-252	I	G	O	Tape Reel

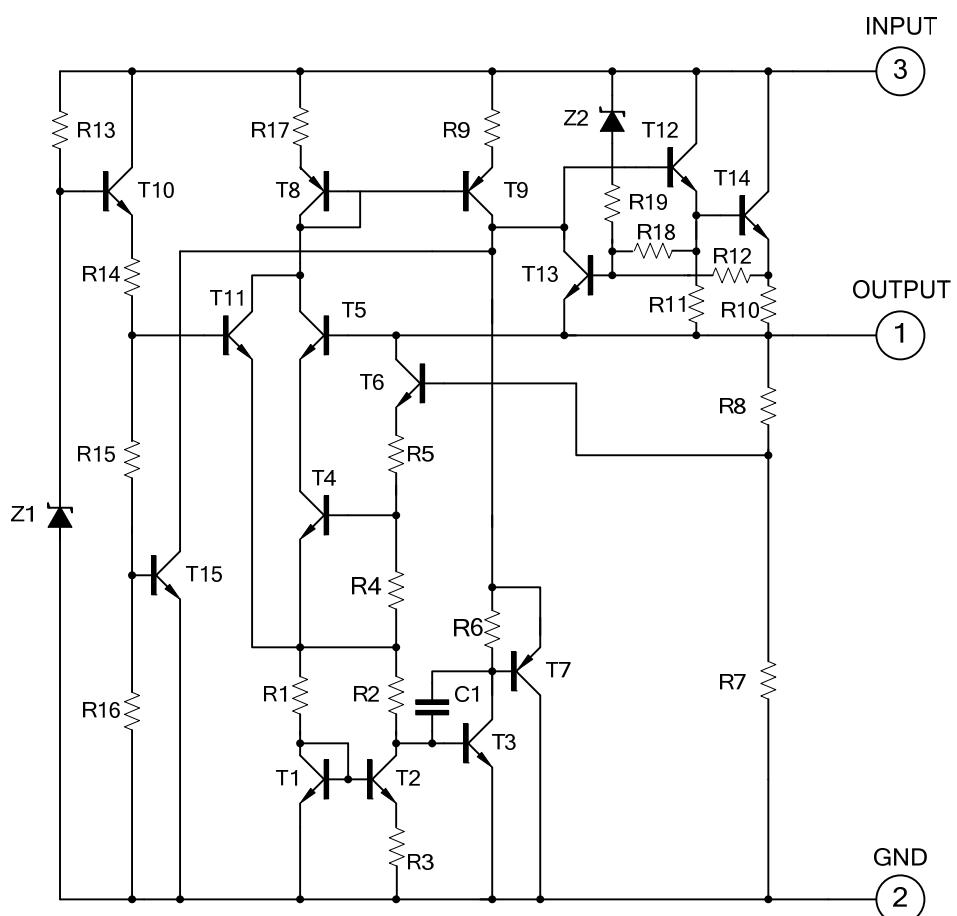
Note: XX: Output Voltage, refer to Marking Information



■ MARKING INFORMATION

PACKAGE	VOLTAGE CODE	MARKING
SOT-89		<p>Date Code Voltage Code L: Lead Free G: Halogen Free</p>
SOT-223	05: 5.0V 06: 6.0V 08: 8.0V 09: 9.0V 10: 10V 12: 12V 15: 15V 18: 18V 24: 24V	<p>Voltage Code 78NXX□ L: Lead Free G: Halogen Free Date Code</p>
TO-252		<p>Voltage Code Lot Code UTC 78NXX□ L: Lead Free G: Halogen Free Date Code</p>

■ BLOCK CIRCUIT



■ ABSOLUTE MAXIMUM RATING (Operating temperature range applies, unless otherwise specified.)

PARAMETER	SYMBOL	RATINGS	UNIT
Input Voltage	V_{IN}	30	V
		35	V
Output Current	I_{OUT}	300	mA
Power Dissipation	P_D	350	mW
		750	
		1000	
Operating Temperature (Note 2)	T_{OPR}	-40 ~ +85	°C
Storage Temperature	T_{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. It is guarantee by design, not 100% be tested.

■ ELECTRICAL CHARACTERISTICS

(0°C < T_J < 125°C, $C_1=0.33\mu F$, $C_0=0.1\mu F$, unless otherwise specified) (Note 1)

For 78N05 ($V_{IN}=10V$, $I_{OUT}=40mA$)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	V_{OUT}	$T_J=25^\circ C$	4.80	5.0	5.20	V
		$7V \leq V_{IN} \leq V_{MAX}$ $I_{OUT}=1mA \sim 300mA$ (Note 2)	4.75		5.25	V
Load Regulation	ΔV_{OUT}	$T_J=25^\circ C$, $I_{OUT}=1mA \sim 300mA$		15	60	mV
		$T_J=25^\circ C$, $I_{OUT}=1mA \sim 40mA$		8	30	mV
Line Regulation	ΔV_{OUT}	$7V \leq V_{IN} \leq 20V$, $T_J=25^\circ C$		8	150	mV
		$8V \leq V_{IN} \leq 20V$, $T_J=25^\circ C$		6	100	mV
Quiescent Current	I_Q			2.0	5.5	mA
Quiescent Current Change	ΔI_Q	$8V \leq V_{IN} \leq 20V$			1.5	mA
		$1mA \leq V_{IN} \leq 40mA$			0.1	mA
Output Noise Voltage	e_N	$10Hz \leq f \leq 100kHz$		40		uV
Temperature Coefficient of V_{OUT}	$\Delta V_O/\Delta T$	$I_{OUT}=5mA$		-0.65		mV/°C
Ripple Rejection	RR	$8V \leq V_{IN} \leq 20V$, $f=120Hz$, $T_J=25^\circ C$		60		dB
Dropout Voltage	V_D	$T_J=25^\circ C$		1.7		V

For 78N06 ($V_{IN}=12V$, $I_{OUT}=40mA$)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	V_{OUT}	$T_J=25^\circ C$	5.76	6.0	6.24	V
		$8.5V \leq V_{IN} \leq V_{MAX}$, $I_{OUT}=1mA \sim 300mA$ (Note 2)	5.7		6.3	V
Load Regulation	ΔV_{OUT}	$T_J=25^\circ C$, $I_{OUT}=1mA \sim 300mA$		16	80	mV
		$T_J=25^\circ C$, $I_{OUT}=1mA \sim 40mA$		9	40	mV
Line Regulation	ΔV_{OUT}	$8.5V \leq V_{IN} \leq 20V$, $T_J=25^\circ C$		10	175	mV
		$9V \leq V_{IN} \leq 20V$, $T_J=25^\circ C$		8	125	mV
Quiescent Current	I_Q			2.0	5.5	mA
Quiescent Current Change	ΔI_Q	$9V \leq V_{IN} \leq 20V$			1.5	mA
		$1mA \leq V_{IN} \leq 40mA$			0.1	mA
Output Noise Voltage	e_N	$10Hz \leq f \leq 100kHz$		49		uV
Temperature Coefficient of V_{OUT}	$\Delta V_O/\Delta T$	$I_{OUT}=5mA$		-0.75		mV/°C
Ripple Rejection	RR	$10V \leq V_{IN} \leq 20V$, $f=120Hz$, $T_J=25^\circ C$		56		dB
Dropout Voltage	V_D	$T_J=25^\circ C$		1.7		V

■ ELECTRICAL CHARACTERISTICS (Cont.)

For 78N08 ($V_{IN}=14V$, $I_{OUT}=40mA$)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	V_{OUT}	$T_J=25^\circ C$	7.68	8.0	8.32	V
		$10.5V \leq V_{IN} \leq V_{MAX}$, $I_{OUT}=1mA \sim 300mA$ (Note 2)	7.60		8.40	V
Load Regulation	ΔV_{OUT}	$T_J=25^\circ C$, $I_{OUT}=1mA \sim 300mA$		18	80	mV
		$T_J=25^\circ C$, $I_{OUT}=1mA \sim 40mA$		10	40	mV
Line Regulation	ΔV_{OUT}	$10.5V \leq V_{IN} \leq 23V$, $T_J=25^\circ C$		10	175	mV
		$11V \leq V_{IN} \leq 23V$, $T_J=25^\circ C$		8	125	mV
Quiescent Current	I_Q			2.0	5.5	mA
Quiescent Current Change	ΔI_Q	$11V \leq V_{IN} \leq 23V$			1.5	mA
		$1mA \leq V_{IN} \leq 40mA$			0.1	mA
Output Noise Voltage	e_N	$10Hz \leq f \leq 100kHz$		49		uV
Temperature Coefficient of V_{OUT}	$\Delta V_O/\Delta T$	$I_{OUT}=5mA$		-0.75		mV/°C
Ripple Rejection	RR	$11V \leq V_{IN} \leq 23V$, $f=120Hz$, $T_J=25^\circ C$		52		dB
Dropout Voltage	V_D	$T_J=25^\circ C$		1.7		V

For 78N09 ($V_{IN}=15V$, $I_{OUT}=40mA$)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	V_{OUT}	$T_J=25^\circ C$	8.64	9.0	9.36	V
		$10.5V \leq V_{IN} \leq V_{MAX}$, $I_{OUT}=1mA \sim 300mA$ (Note 2)	8.55		9.45	V
Load Regulation	ΔV_{OUT}	$T_J=25^\circ C$, $I_{OUT}=1mA \sim 300mA$		20	90	mV
		$T_J=25^\circ C$, $I_{OUT}=1mA \sim 40mA$		10		mV
Line Regulation	ΔV_{OUT}	$11.5V \leq V_{IN} \leq 24V$, $T_J=25^\circ C$		15	200	mV
		$13V \leq V_{IN} \leq 24V$, $T_J=25^\circ C$		10	150	mV
Quiescent Current	I_Q			2.0	6.0	mA
Quiescent Current Change	ΔI_Q	$13V \leq V_{IN} \leq 24V$			1.5	mA
		$1mA \leq V_{IN} \leq 40mA$			0.1	mA
Output Noise Voltage	e_N	$10Hz \leq f \leq 100kHz$		70		uV
Temperature Coefficient of V_{OUT}	$\Delta V_O/\Delta T$	$I_{OUT}=5mA$		-0.75		mV/°C
Ripple Rejection	RR	$12V \leq V_{IN} \leq 24V$, $f=120Hz$, $T_J=25^\circ C$		46		dB
Dropout Voltage	V_D	$T_J=25^\circ C$		1.7		V

For 78N10 ($V_{IN}=16V$, $I_{OUT}=40mA$)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	V_{OUT}	$T_J=25^\circ C$	9.6	10	10.4	V
		$12.5V \leq V_{IN} \leq V_{MAX}$, $I_{OUT}=1mA \sim 300mA$ (Note 2)	9.5		10.5	V
Load Regulation	ΔV_{OUT}	$T_J=25^\circ C$, $I_{OUT}=1mA \sim 300mA$		20	94	mV
		$T_J=25^\circ C$, $I_{OUT}=1mA \sim 40mA$		10	47	mV
Line Regulation	ΔV_{OUT}	$12.5V \leq V_{IN} \leq 25V$, $T_J=25^\circ C$		25	200	mV
		$14V \leq V_{IN} \leq 25V$, $T_J=25^\circ C$		20	170	mV
Quiescent Current	I_Q			2.0	6.0	mA
Quiescent Current Change	ΔI_Q	$12.5V \leq V_{IN} \leq 25V$			1.5	mA
		$1mA \leq V_{IN} \leq 40mA$			0.1	mA
Output Noise Voltage	e_N	$10Hz \leq f \leq 100kHz$		74		uV
Temperature Coefficient of V_{OUT}	$\Delta V_O/\Delta T$	$I_{OUT}=5mA$		-0.8		mV/°C
Ripple Rejection	RR	$15V \leq V_{IN} \leq 25V$, $f=120Hz$, $T_J=25^\circ C$		45		dB
Dropout Voltage	V_D	$T_J=25^\circ C$		1.7		V

■ ELECTRICAL CHARACTERISTICS (Cont.)

For 78N12 ($V_{IN}=19V$, $I_{OUT}=40mA$)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	V_{OUT}	$T_J=25^\circ C$	11.52	12	12.48	V
		$14.5V \leq V_{IN} \leq V_{MAX}$, $I_{OUT}=1mA \sim 300mA$ (Note 2)	11.40		12.60	V
Load Regulation	ΔV_{OUT}	$T_J=25^\circ C$, $I_{OUT}=1mA \sim 300mA$		25	100	mV
		$T_J=25^\circ C$, $I_{OUT}=1mA \sim 40mA$		12	50	mV
Line Regulation	ΔV_{OUT}	$14.5V \leq V_{IN} \leq 27V$, $T_J=25^\circ C$		25	300	mV
		$16V \leq V_{IN} \leq 27V$, $T_J=25^\circ C$		20	250	mV
Quiescent Current	I_Q			2.0	6.5	mA
Quiescent Current Change	ΔI_Q	$16V \leq V_{IN} \leq 27V$			1.5	mA
		$1mA \leq V_{IN} \leq 40mA$			0.1	mA
Output Noise Voltage	e_N	$10Hz \leq f \leq 100kHz$		80		uV
Temperature Coefficient of V_{OUT}	$\Delta V_O/\Delta T$	$I_{OUT}=5mA$		-1.0		mV/°C
Ripple Rejection	RR	$15V \leq V_{IN} \leq 25V$, $f=120Hz$, $T_J=25^\circ C$		45		dB
Dropout Voltage	V_D	$T_J=25^\circ C$		1.7		V

For 78N15 ($V_{IN}=23V$, $I_{OUT}=40mA$)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	V_{OUT}	$T_J=25^\circ C$	14.40	15	15.60	V
		$17.5V \leq V_{IN} \leq V_{MAX}$, $I_{OUT}=1mA \sim 300mA$ (Note 2)	14.25		15.75	V
Load Regulation	ΔV_{OUT}	$T_J=25^\circ C$, $I_{OUT}=1mA \sim 300mA$		25	150	mV
		$T_J=25^\circ C$, $I_{OUT}=1mA \sim 40mA$		15	75	mV
Line Regulation	ΔV_{OUT}	$17.5V \leq V_{IN} \leq 30V$, $T_J=25^\circ C$		25	150	mV
		$20V \leq V_{IN} \leq 30V$, $T_J=25^\circ C$		15	75	mV
Quiescent Current	I_Q			2.2	6.5	mA
Quiescent Current Change	ΔI_Q	$20V \leq V_{IN} \leq 30V$			1.5	mA
		$1mA \leq V_{IN} \leq 40mA$			0.1	mA
Output Noise Voltage	e_N	$10Hz \leq f \leq 100kHz$		90		uV
Temperature Coefficient of V_{OUT}	$\Delta V_O/\Delta T$	$I_{OUT}=5mA$		-1.3		mV/°C
Ripple Rejection	RR	$18.5V \leq V_{IN} \leq 28.5V$, $f=120Hz$, $T_J=25^\circ C$		45		dB
Dropout Voltage	V_D	$T_J=25^\circ C$		1.7		V

For 78N18 ($V_{IN}=27V$, $I_{OUT}=40mA$)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	V_{OUT}	$T_J=25^\circ C$	17.28	18	18.72	V
		$21V \leq V_{IN} \leq V_{MAX}$, $I_{OUT}=1mA \sim 300mA$ (Note 2)	17.10		18.90	V
Load Regulation	ΔV_{OUT}	$T_J=25^\circ C$, $I_{OUT}=1mA \sim 300mA$		30	180	mV
		$T_J=25^\circ C$, $I_{OUT}=1mA \sim 40mA$		20	90	mV
Line Regulation	ΔV_{OUT}	$21V \leq V_{IN} \leq 33V$, $T_J=25^\circ C$		45	300	mV
		$22V \leq V_{IN} \leq 33V$, $T_J=25^\circ C$		35	250	mV
Quiescent Current	I_Q			2.2	6.5	mA
Quiescent Current Change	ΔI_Q	$21V \leq V_{IN} \leq 33V$			1.5	mA
		$1mA \leq V_{IN} \leq 40mA$			0.1	mA
Output Noise Voltage	e_N	$10Hz \leq f \leq 100kHz$		150		uV
Temperature Coefficient of V_{OUT}	$\Delta V_O/\Delta T$	$I_{OUT}=5mA$		1.8		mV/°C
Ripple Rejection	RR	$23V \leq V_{IN} \leq 28.5V$, $f=120Hz$, $T_J=25^\circ C$		45		dB
Dropout Voltage	V_D	$T_J=25^\circ C$		1.7		V

■ ELECTRICAL CHARACTERISTICS (Cont.)

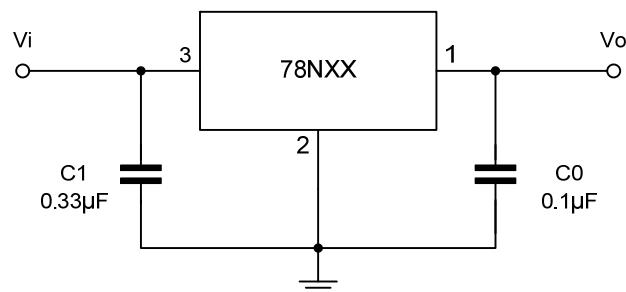
For 78N24 ($V_{IN}=33V$, $I_{OUT}=40mA$)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	V_{OUT}	$T_J=25^\circ C$	23.04	24.00	24.96	V
		$27V \leq V_{IN} \leq V_{MAX}$, $I_{OUT}=1mA \sim 300mA$ (Note 2)	22.80		25.20	V
Load Regulation	ΔV_{OUT}	$T_J=25^\circ C$, $I_{OUT}=1mA \sim 300mA$		30	200	mV
		$T_J=25^\circ C$, $I_{OUT}=1mA \sim 40mA$		20	100	mV
Line Regulation	ΔV_{OUT}	$27V \leq V_{IN} \leq 35V$, $T_J=25^\circ C$		160	300	mV
		$28V \leq V_{IN} \leq 35V$, $T_J=25^\circ C$		150	250	mV
Quiescent Current	I_Q			3.0	7.5	mA
Quiescent Current Change	ΔI_Q	$27V \leq V_{IN} \leq 35V$			1.5	mA
		$1mA \leq V_{IN} \leq 40mA$			0.1	mA
Output Noise Voltage	e_N	$10Hz \leq f \leq 100kHz$		200		uV
Temperature Coefficient of V_{OUT}	$\Delta V_O/\Delta T$	$I_{OUT}=5mA$		2.0		mV/°C
Ripple Rejection	RR	$27V \leq V_{IN} \leq 35V$, $f=120Hz$, $T_J=25^\circ C$		45		dB
Dropout Voltage	V_D	$T_J=25^\circ C$		1.7		V

Notes: 1. The Maximum steady state usable output current are dependent on input voltage, heat sinking, lead length of the package and copper pattern of PCB. The data above represent pulse test conditions with junction temperatures specified at the initiation of test.

2. Power dissipation<0.5W

■ TYPICAL APPLICATION CIRCUIT



Notes:

1. To specify an output voltage, substitute voltage value for "XX".
2. Bypass capacitors are recommended for optimum stability and transient response and should be located as close as possible to the regulators.

■ TYPICAL CHARACTERISTICS

Fig.1 78N08 Output Voltage vs.
Ambient Temperature

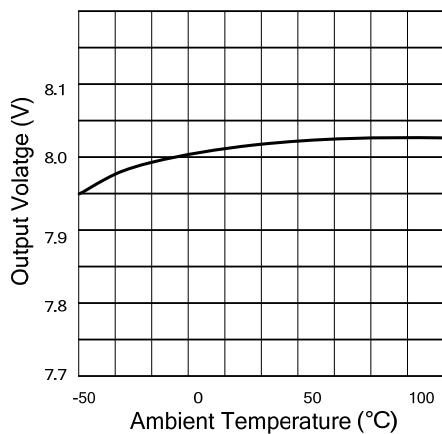


Fig.2 Output Characteristics
($I_{OUT}=0mA, T_J=25^{\circ}C$)

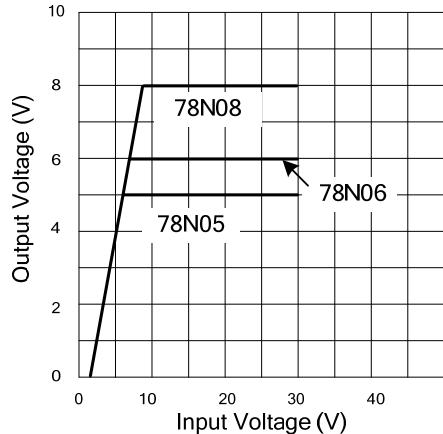


Fig.3 78N05 Dropout Characteristics
($T_J=25^{\circ}C$)

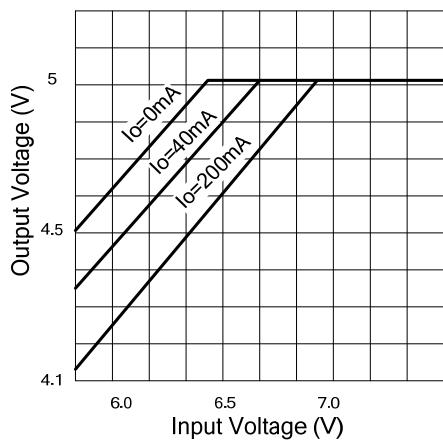


Fig.4 Short Circuit Output Current
($T_J=25^{\circ}C$)

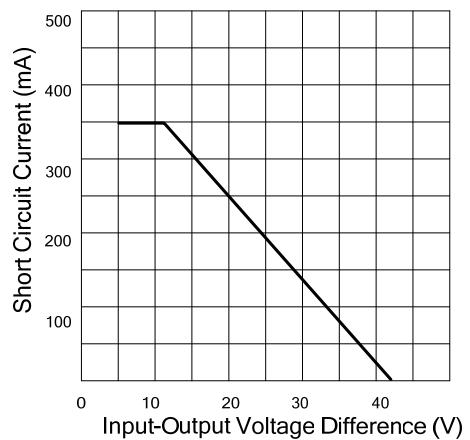


Fig.5 78N05 Quiescent Current vs. Input
Voltage ($I_{OUT}=0mA, T_J=25^{\circ}C$)

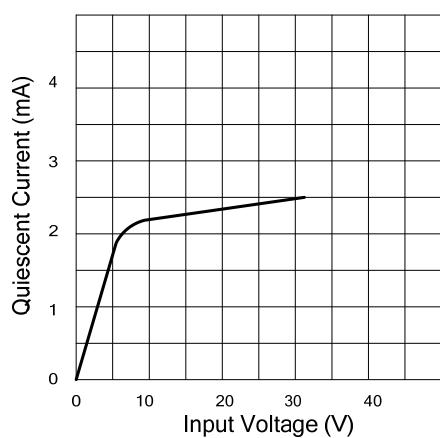
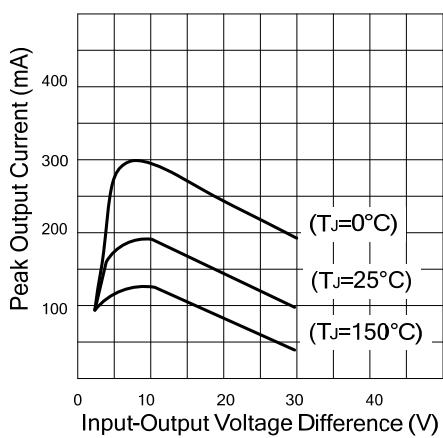
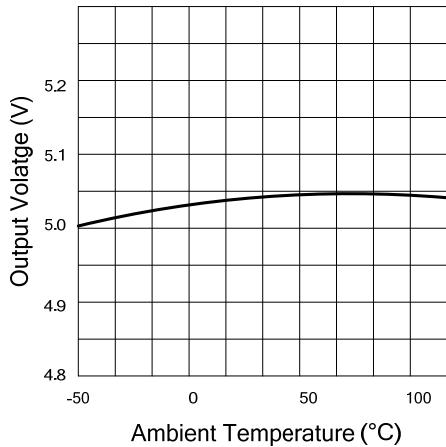


Fig.6 Peak Output Current
vs Dropout Voltage Difference



- TYPICAL CHARACTERISTICS(Cont.)

Fig.7 78N05 Output Voltage
vs.Ambient Temperature



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