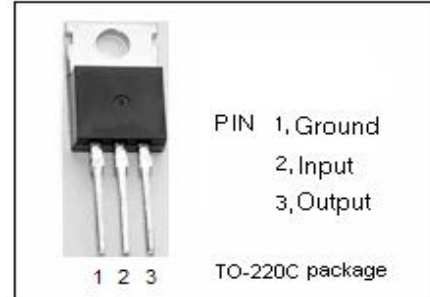
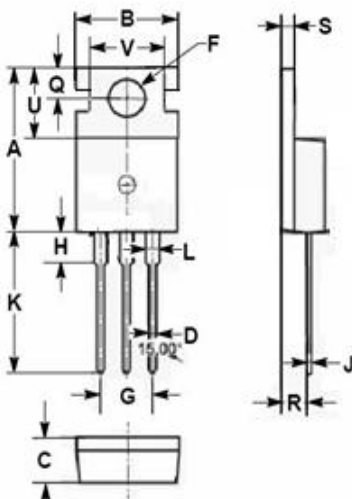


isc Three Terminal Positive Voltage Regulator
7924
FEATURES

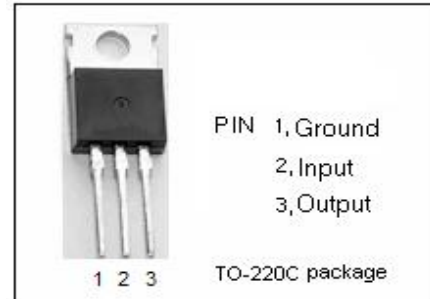
- Output current in excess of 1.0A
- Output voltage of 24V
- Internal short current circuit limiting
- Output transition Safe-Area compensation
- Minimum Lot-to-Lot variations for robust device performance and reliable operation


• ELECTRICAL CHARACTERISTICS
 $T_j = 25^\circ\text{C}$ ($V_i = -33\text{V}$, $I_O = 0.5\text{A}$, $C_i = 2.2 \mu\text{F}$, $C_O = 1.0 \mu\text{F}$ unless otherwise noted)

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V_O	Output Voltage	$-27\text{V} \geq V_{in} \geq -38\text{V}$ $5.0\text{mA} \leq I_O \leq 1.0\text{A}$	23	25	V
ΔU_V	Line Regulation	$I_O = 0.1\text{A}$, $-27\text{V} \geq V_{in} \geq -38\text{V}$ $I_O = 0.1\text{A}$, $-30\text{V} \geq V_{in} \geq -36\text{V}$ $I_O = 0.5\text{A}$, $-27\text{V} \geq V_{in} \geq -38\text{V}$ $I_O = 0.5\text{A}$, $-30\text{V} \geq V_{in} \geq -36\text{V}$		240 120 470 228	mV
ΔU_i	Load Regulation	$5.0\text{mA} \leq I_O \leq 1.5\text{A}$; $V_{in} = 33\text{V}$ $250\text{mA} \leq I_O \leq 750\text{mA}$; $V_{in} = 33\text{V}$		470 228	mV
I_B	Quiescent Current	$V_{in} = -33\text{V}$; $I_O = -0.5\text{A}$		8.0	mA
Δ_{b1}	Quiescent Current Change	$5.0\text{mA} \leq I_O \leq 1.0\text{A}$; $V_{in} = 33\text{V}$		0.5	mA
ΔI_B	Quiescent Current Change	$-27\text{V} \leq V_{in} \leq -33\text{V}$; $5.0\text{mA} \leq I_O \leq 1.5\text{A}$		1.0 0.5	mA

Sizing Specification


DIM	mm	
	MIN	MAX
A	15.50	15.90
B	9.80	10.20
C	4.20	4.50
D	0.70	0.90
F	3.40	3.70
G	4.98	5.18
H	2.68	2.90
J	0.44	0.60
K	12.80	13.40
L	1.20	1.45
Q	2.70	2.90
R	2.30	2.70
S	1.29	1.35
U	6.45	6.65
V	8.66	8.86

isc Three Terminal Positive Voltage Regulator**7924****NOTICE:**

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