Negative-Voltage Regulators

- 3-Terminal Regulators
- Output Current Up to 100 mA
- No External Components Required
- Internal Thermal-Overload Protection
- Internal Short-Circuit Current Limiting
- Provided Pb-Free packages from the end of 2004

C O OUTPUT INPUT COMMON TO-92 79L08ACZ SOT-89 79L08CPK COMMON INPUT OUTPUT

description

This series of fixed negative-voltage integrated-circuit voltage regulators is designed for a wide range of applications. These include on-card regulation for elimination of noise and distribution problems associated with single-point regulation. In addition,

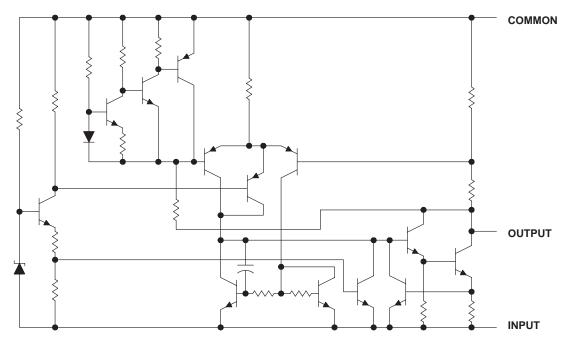
they can be used to control series pass elements to make high-current voltage-regulator circuits. One of these regulators can deliver up to 100 mA of output current. The internal current-limiting and thermal-shutdown features make them essentially immune to overload. When used as a replacement for a zener-diode and resistor combination, these devices can provide effective improvement in output impedance of two orders of magnitude, with lower bias current.

electrical characteristics at specified virtual junction temperature, $V_I = -14V$, $I_I = 40mA$ (unless otherwise noted)

DADAMETED	TEST CONDITIONS	т‡	79L06			UNIT	
PARAMETER			MIN	TYP	MAX		
		25°C	-7.7	-8	-8.3		
Output voltage	I ₀ =1 to 40mA, V = -10.5V to-23V I ₀ =1mA to 70mA	Full range	-7.6		-8.4	V	
Input voltage regulation	$V_{\parallel} = -10.5 \text{V to } -23 \text{V}$	25°C		42	200	mV	
	V _I = -11V to -23V			36	150		
Ripple rejection	V _I = -11V to -23V f = 120 Hz	25°C	37	46		dB	
Output	I _O = 1 mA to 40mA	0-		15	50	mV	
voltage regulation	I _O = 1 mA to 100mA	25°C		30	100		
Output noise voltage	f = 10 Hz to 100 kHz	25°C		54		μV	
Dropout voltage		25°C		1.7		V	
Bias current		25°C		3	6		
		125°C			5.5	mA	
Bias current change	V _I = -11V to -23V	Fullrange			1.5		
	I _O = 1 mA to 40 mA	- Full range			0.1	mA	

[‡] Pulse-testing techniques maintain T_J as close to T_A as possible. Thermal effects must be taken into account separately. All characteristics are measured with a 0.33-μF capacitor across the input and a 0.1-μF capacitor across the output. Full range for the 79L08 is T_J = 0°C to 70°C

equivalent schematic



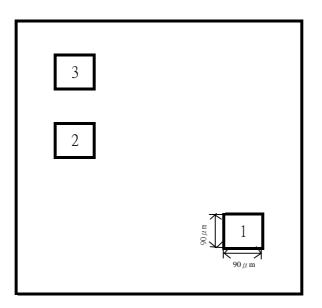
absolute maximum ratings over operating free-air temperature range (unless otherwise noted)[†]

Input voltage: 79L08		30V
Operating free-air, case, or vir	ual junction temperature	150 °C
Lead temperature 1.6 mm (1/16	,	

recommended operating conditions

79L08	MIN	MAX	UNIT
Input voltage, V _I	-10.5	-23	V
Output current, IO		100	mA
Operating virtual junction temperature, TJ	0	70	°C

Pad Location WS79L00



chip size 1.15 x 1.35mm

Pad Location Coordinates

Pad N	Pad Name	X(μ m)	Υ(μ m)
1	Ground	1150	115
2	Input	115	690
3	Output	115	950