

### General Description

This planar stripe MOSFET has better characteristics, such as fast switching time, low on resistance, low gate charge and excellent avalanche characteristics. It is mainly suitable for active power factor correction and switching mode power supplies.

### FEATURES

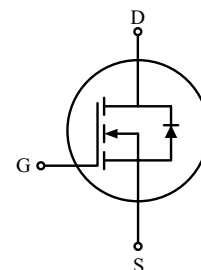
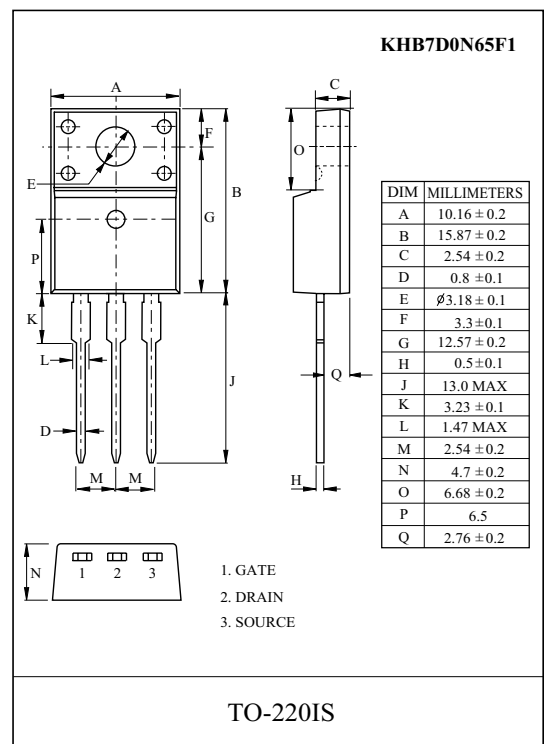
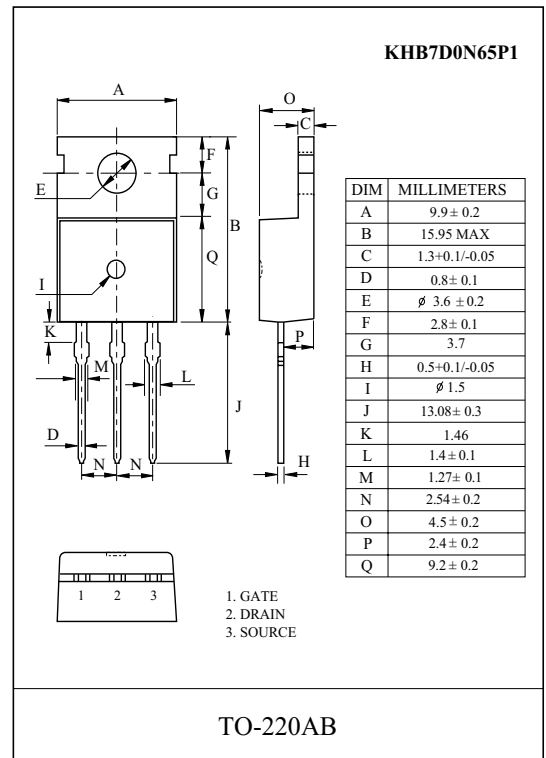
- $V_{DSS}=650V$ ,  $I_D=7A$
- Drain-Source ON Resistance :  
 $R_{DS(ON)}=1.4\ \Omega$  @  $V_{GS}=10V$
- $Q_g(\text{typ.})=32nC$

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### MAXIMUM RATING (Tc=25°C)

CHARACTERISTIC	SYMBOL	RATING		UNIT	
		KHB7D0N65P1	KHB7D0N65F1		
Drain-Source Voltage	$V_{DSS}$	650		V	
Gate-Source Voltage	$V_{GSS}$	$\pm 30$		V	
Drain Current	@T <sub>c</sub> =25°C	$I_D$	7	7*	A
	@T <sub>c</sub> =100°C		4.2	4.2*	
	Pulsed (Note 1)	$I_{DP}$	28	28*	
Single Pulsed Avalanche Energy (Note 2)	$E_{AS}$	212		mJ	
Repetitive Avalanche Energy (Note 1)	$E_{AR}$	1.6		mJ	
Peak Diode Recovery dv/dt (Note 3)	dv/dt	4.5		V/ns	
Drain Power Dissipation	T <sub>c</sub> =25°C	$P_D$	160	52	W
	Derate above 25°C		1.28	0.42	
Maximum Junction Temperature	$T_j$	150		°C	
Storage Temperature Range	$T_{stg}$	-55 ~ 150		°C	
<b>Thermal Characteristics</b>					
Thermal Resistance, Junction-to-Case	$R_{thJC}$	0.78	2.4	°C/W	
Thermal Resistance, Case-to-Sink	$R_{thCS}$	0.5	-	°C/W	
Thermal Resistance, Junction-to-Ambient	$R_{thJA}$	62.5	62.5	°C/W	

\* : Drain current limited by maximum junction temperature.



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## ELECTRICAL CHARACTERISTICS (Tc=25 °C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
<b>Static</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$I_D=250\mu A, V_{GS}=0V$	650	-	-	V
Breakdown Voltage Temperature Coefficient	$\Delta BV_{DSS}/\Delta T_j$	$I_D=250\mu A$ , Referenced to 25 °C	-	0.8	-	V/°C
Drain Cut-off Current	$I_{DSS}$	$V_{DS}=650V, V_{GS}=0V$ ,	-	-	$\pm 10$	$\mu A$
Gate Threshold Voltage	$V_{th}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2	-	4	V
Gate Leakage Current	$I_{GSS}$	$V_{GS}=\pm 30V, V_{DS}=0V$	-	-	$\pm 100$	nA
Drain-Source ON Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=3.75A$	-	1.2	1.4	$\Omega$
<b>Dynamic</b>						
Total Gate Charge	$Q_g$	$V_{DS}=520V, I_D=7.0A$ $V_{GS}=10V$ (Note4,5)	-	32	40	nC
Gate-Source Charge	$Q_{gs}$		-	5.4	-	
Gate-Drain Charge	$Q_{gd}$		-	12.6	-	
Turn-on Delay time	$t_{d(on)}$	$V_{DD}=325V$ $R_L=46\Omega$ $R_G=25\Omega$ (Note4,5)	-	20	45	ns
Turn-on Rise time	$t_r$		-	40	90	
Turn-off Delay time	$t_{d(off)}$		-	125	260	
Turn-off Fall time	$t_f$		-	80	170	
Input Capacitance	$C_{iss}$	$V_{DS}=25V, V_{GS}=0V, f=1.0MHz$	-	1310	1700	pF
Output Capacitance	$C_{oss}$		-	113	147	
Reverse Transfer Capacitance	$C_{rss}$		-	11.4	14.8	
<b>Source-Drain Diode Ratings</b>						
Continuous Source Current	$I_S$	$V_{GS}<V_{th}$	-	-	7	A
Pulsed Source Current	$I_{SP}$		-	-	28	
Diode Forward Voltage	$V_{SD}$	$I_S=7.0A, V_{GS}=0V$	-	-	1.5	V
Reverse Recovery Time	$t_{rr}$	$I_S=7.0A, V_{GS}=0V$ , $dI_S/dt=100A/\mu s$	-	410	-	ns
Reverse Recovery Charge	$Q_{rr}$		-	4	-	$\mu C$

Note 1) Repetivity rating : Pulse width limited by junction temperature.

Note 2)  $L=8mH, I_S=7.0A, V_{DD}=50V, R_G=25\Omega$ , Starting  $T_j=25\text{ }^\circ\text{C}$ .

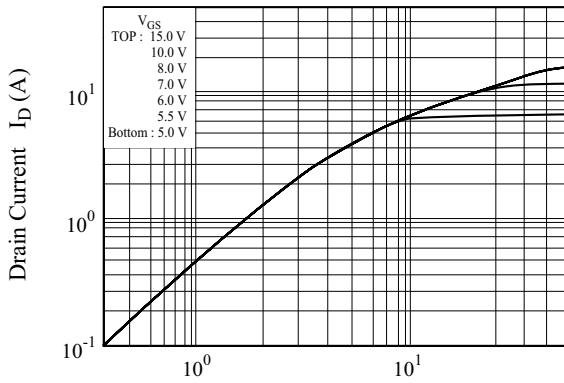
Note 3)  $I_S \leq 7.0A, dI/dt \leq 200A/\mu s, V_{DD} \leq BV_{DSS}$ , Starting  $T_j=25\text{ }^\circ\text{C}$ .

Note 4) Pulse Test : Pulse width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .

Note 5) Essentially independent of operating temperature.

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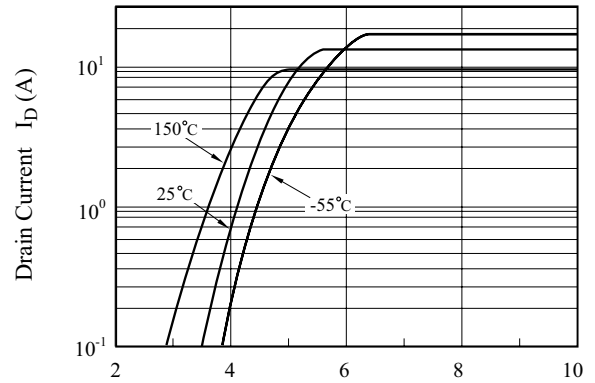
$I_D - V_{DS}$



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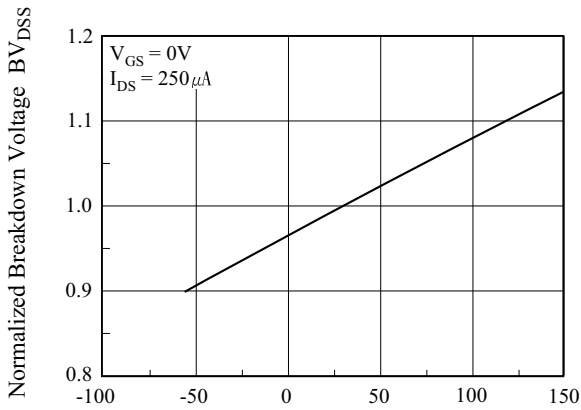
Drain - Source Voltage  $V_{DS}$  (V)

$I_D - V_{GS}$



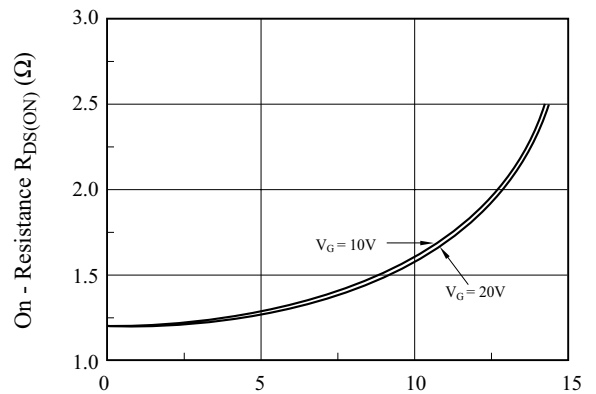
Gate - Source Voltage  $V_{GS}$  (V)

$BV_{DSS} - T_j$



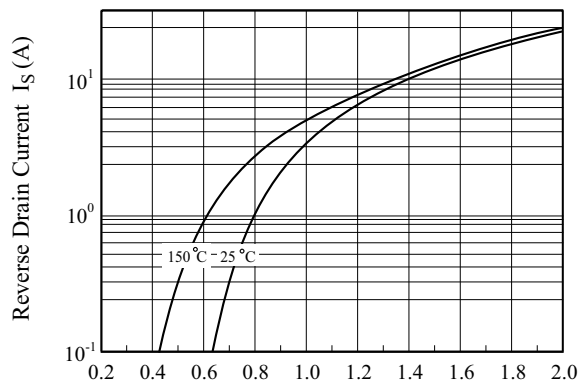
Junction Temperature  $T_j$  (°C)

$R_{DS(ON)} - I_D$



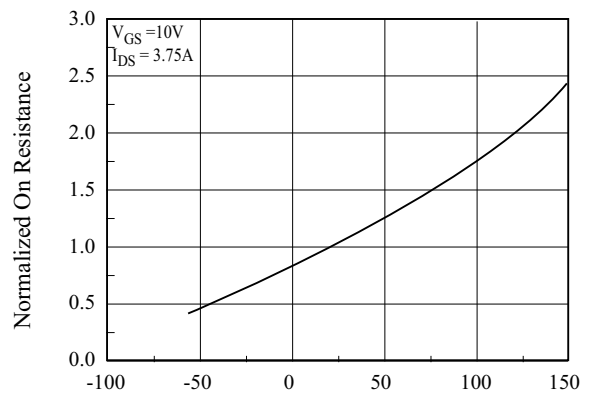
Drain Current  $I_D$  (A)

$I_S - V_{SD}$



Source - Drain Voltage  $V_{SD}$  (V)

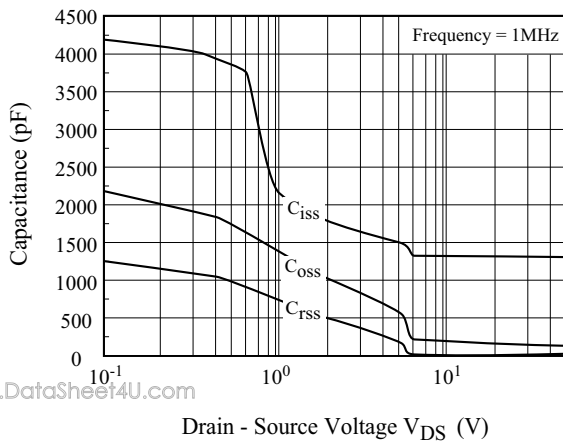
$R_{DS(ON)} - T_j$



Junction Temperature  $T_j$  (°C)

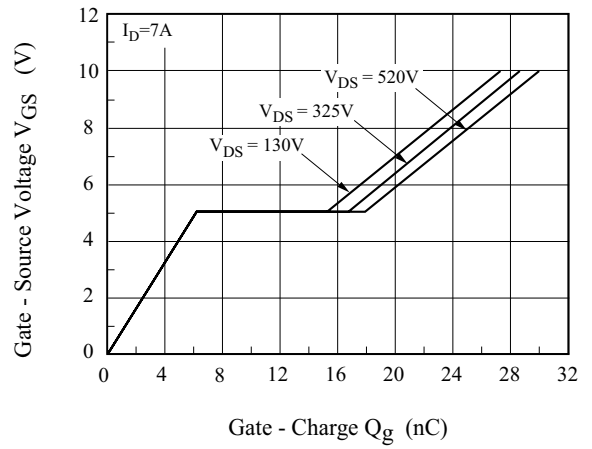
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C - V<sub>DS</sub>

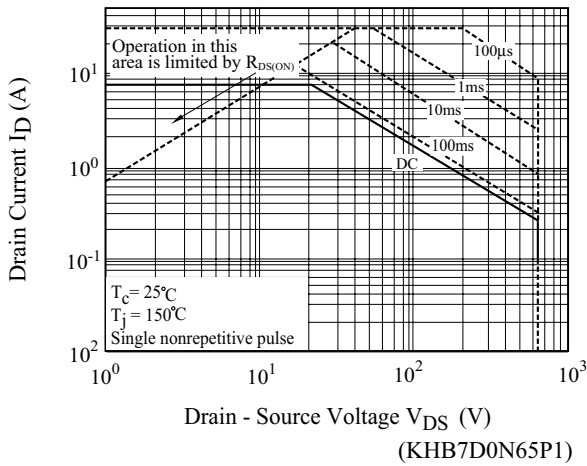


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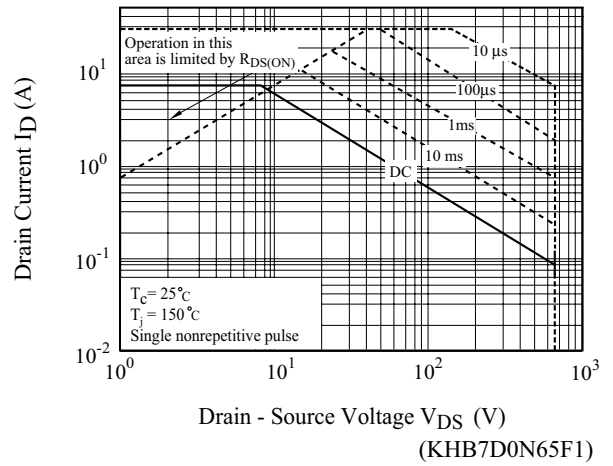
Q<sub>g</sub>- V<sub>GS</sub>



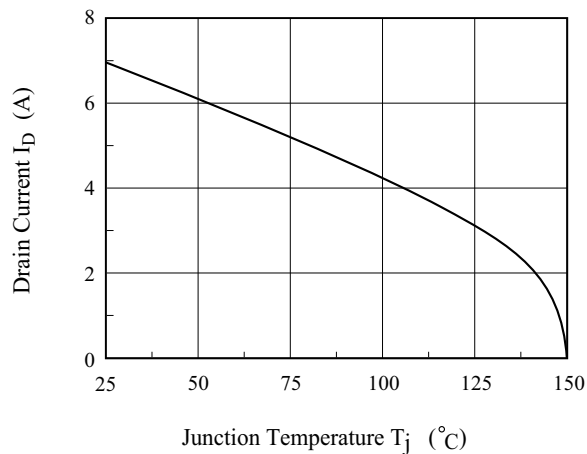
Safe Operation Area



Safe Operation Area

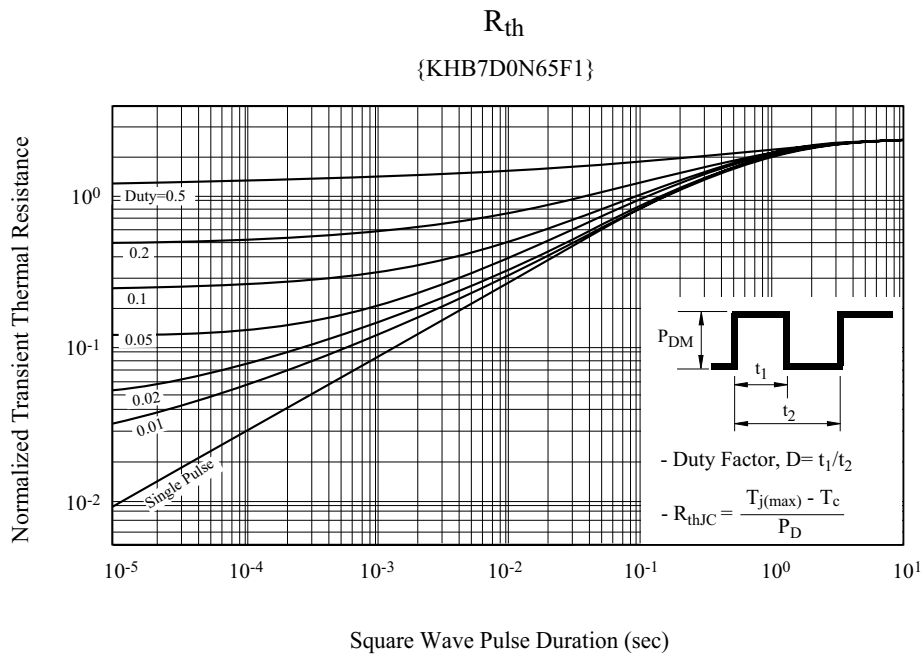
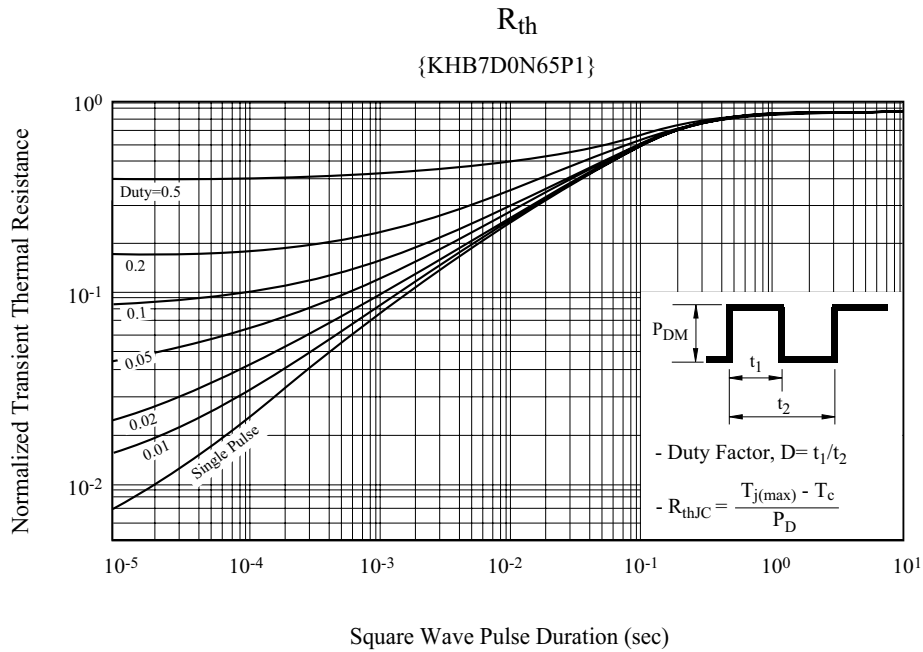


I<sub>D</sub> - T<sub>j</sub>



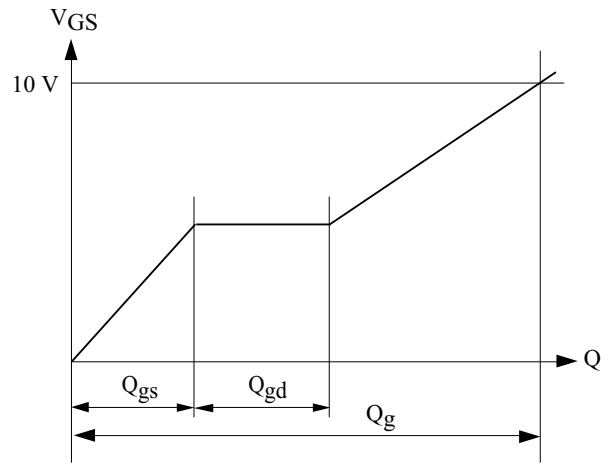
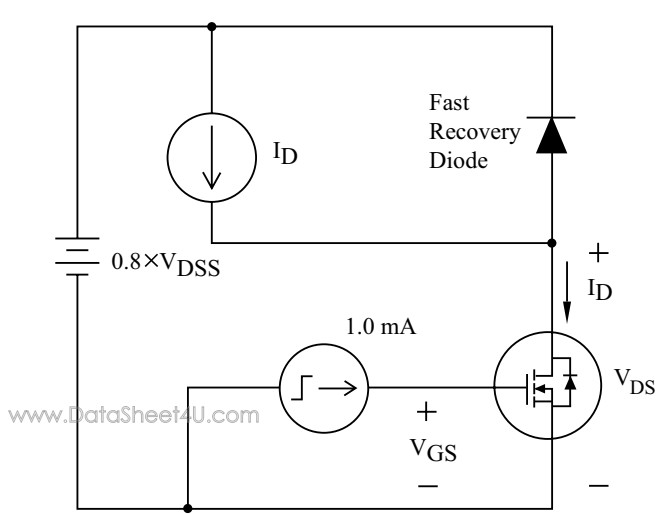
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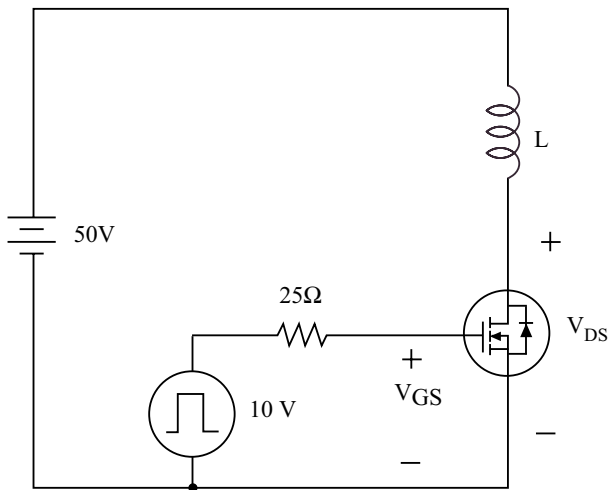


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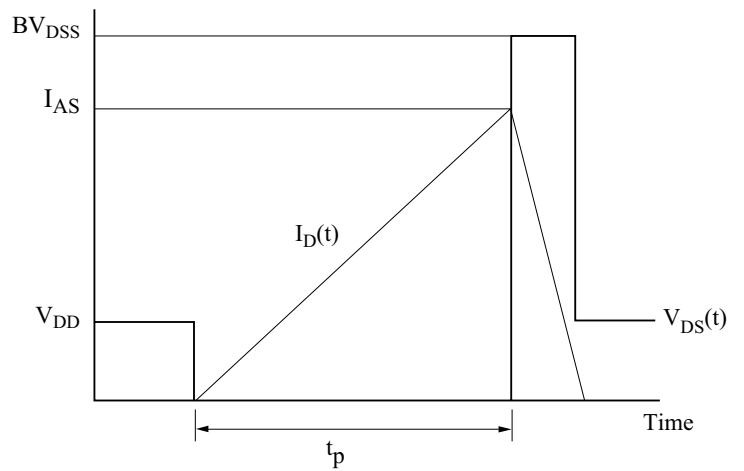
## - Gate Charge



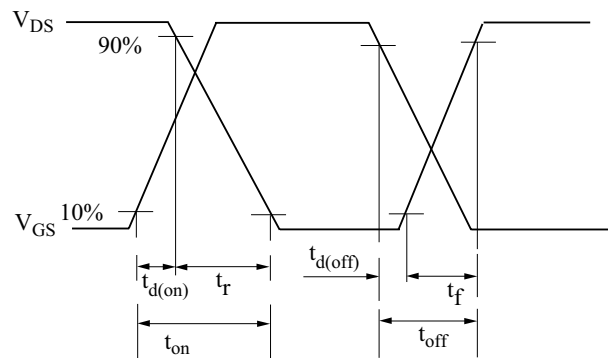
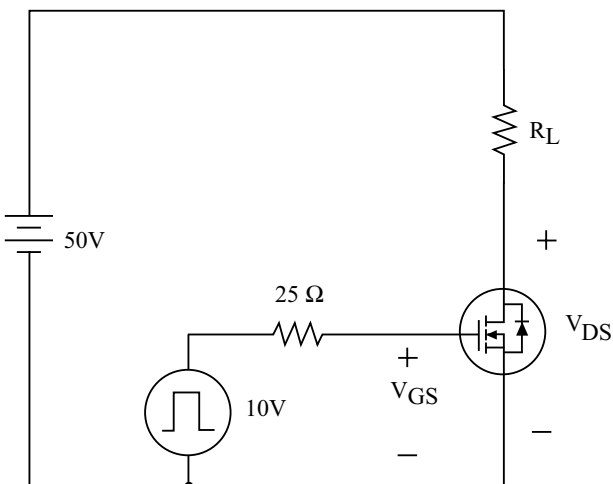
## - Single Pulsed Avalanche Energy



$$E_{AS} = \frac{1}{2} L I_{AS}^2 \frac{BV_{DSS}}{BV_{DSS} - V_{DD}}$$



## - Resistive Load Switching



# KHB7D0N65P1/F1

- Source - Drain Diode Reverse Recovery and  $dv/dt$

