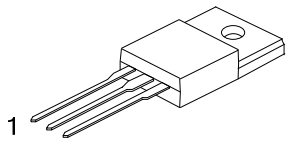
7N60K-MT

Preliminary | ***Power MOSFET***

6.2A, 600V N-CHANNEL POWER MOSFET



TO-220F1

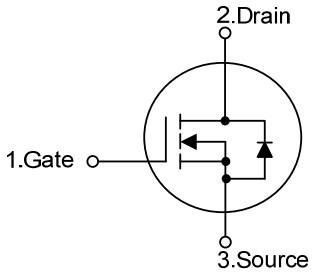
■ DESCRIPTION

The UTC **7N60K-MT** is a high voltage power MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in switching power supplies and adaptors.

■ FEATURES

* $R_{DS(ON)}$ < 1.4Ω @V_{GS} = 10V, I_D = 3.1A
* Fast switching capability
* Avalanche energy tested
* Improved dv/dt capability, high ruggedness

■ SYMBOL



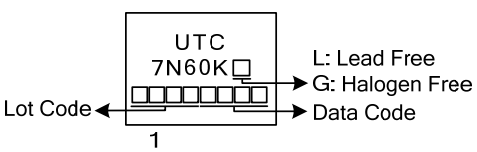
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
7N60KL-TF1-T	7N60KG-TF1-T	TO-220F1	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

7N60KL-TF1-T (1)Packing Type (2)Package Type (3)Green Package	(1) T: Tube (2) TF1: TO-220F1 (3) L: Lead Free, G: Halogen Free and Lead Free

■ MARKING



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■ ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	600	V
Gate-Source Voltage		V_{GSS}	±30	V
Avalanche Current (Note 2)		I_{AR}	6.2	A
Continuous Drain Current		I_D	6.2	A
Pulsed Drain Current (Note 2)		I_{DM}	24.8	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	330	mJ
	Repetitive (Note 2)	E_{AR}	13	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	ns
Power Dissipation		P_D	48	W
Junction Temperature		T_J	+150	°C
Operating Temperature		T_{OPR}	-55 ~ +150	°C
Storage Temperature		T_{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Repetitive Rating : Pulse width limited by T_J
3. L = 18.33mH, I_{AS} = 6A, V_{DD} = 90V, R_G = 25 Ω, Starting T_J = 25°C
4. I_{SD} ≤ 6.2A, di/dt ≤200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C

■ THERMAL DATA

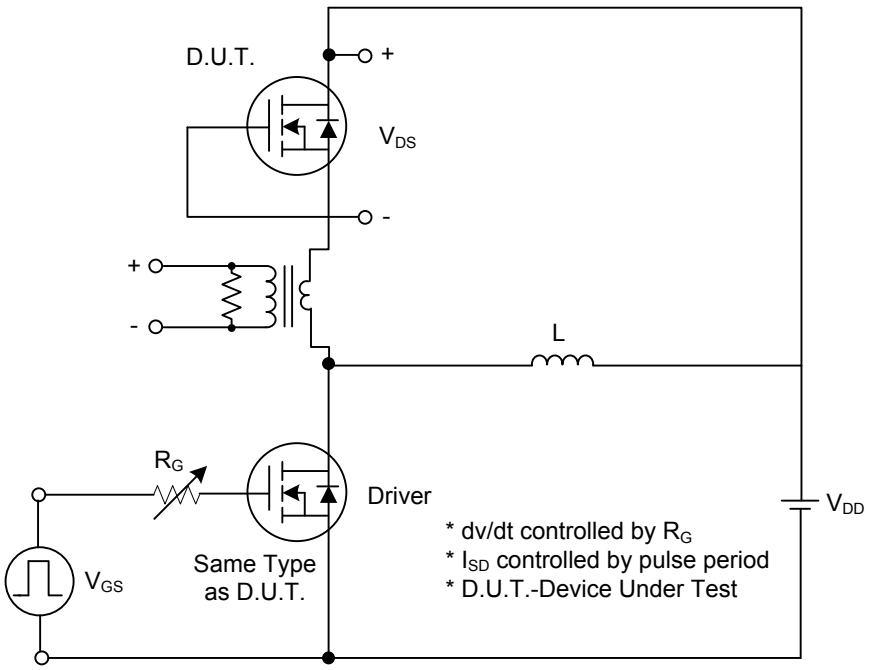
PARAMETER	SYMBOL	RATING	UNIT
Junction to Ambient	θ_{JA}	62.5	°C/W
Junction to Case	θ_{JC}	2.6	°C/W

■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

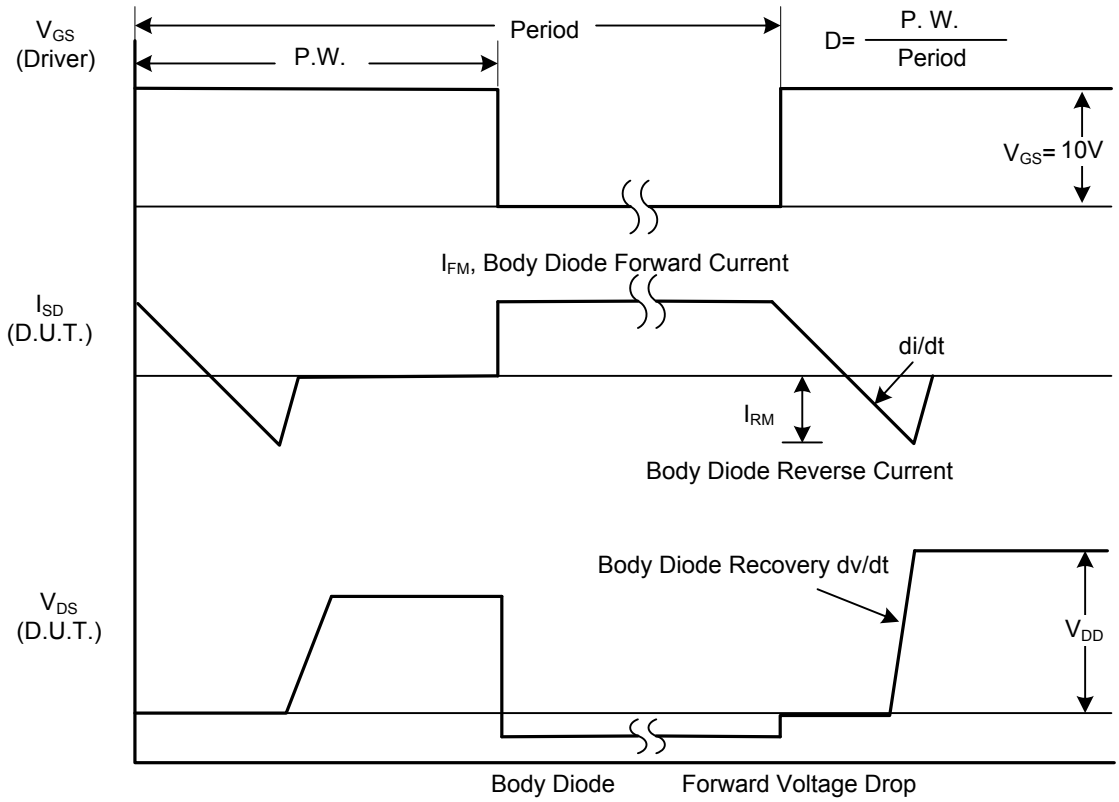
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV_{DSS}	V_{GS}=0V, I_D=250μA	600			V
Drain-Source Leakage Current		I_{DSS}	V_{DS}=600V, V_{GS}=0V			10	μA
			V_{DS}=480V, V_{GS}=0V, T_J=125°C			10	μA
Gate- Source Leakage Current	Forward	I_{GSS}	V_G=30V, V_{DS}=0V			100	nA
	Reverse		V_{GS}=-30V, V_{DS}=0V			-100	nA
Breakdown Voltage Temperature Coefficient		$\triangle BV_{DSS}/\triangle T_J$	I_D=250μA, Referenced to 25°C		0.53		V/°C
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	V_{DS}=V_{GS}, I_D=250μA	3.0		5.0	V
Static Drain-Source On-State Resistance		$R_{DS(ON)}$	V_{GS}=10V, I_D=3.1A			1.4	Ω
DYNAMIC CHARACTERISTICS							
Input Capacitance		C_{ISS}	V_{DS}=25V, V_{GS}=0V, f=1.0 MHz		540		pF
Output Capacitance		C_{OSS}			97		pF
Reverse Transfer Capacitance		C_{RSS}			11		pF
SWITCHING CHARACTERISTICS							
Turn-On Delay Time		$t_{D(ON)}$	V_{DD}=30V, I_D =0.5A, R_G =25Ω (Note 1, 2)		60		ns
Turn-On Rise Time		t_R			66		ns
Turn-Off Delay Time		$t_{D(OFF)}$			120		ns
Turn-Off Fall Time		t_F			64		ns
Total Gate Charge		Q_G	V_{DS}=50V, I_D=1.3A, V_{GS}=10V (Note 1, 2)		23	28	nC
Gate-Source Charge		Q_{GS}			6.7		nC
Gate-Drain Charge		Q_{GD}			5.7		nC
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS							
Drain-Source Diode Forward Voltage		V_{SD}	V_{GS}=0V, I_S=6.2 A			1.4	V
Maximum Continuous Drain-Source Diode Forward Current		I_S				6.2	A
Maximum Pulsed Drain-Source Diode Forward Current		I_{SM}				24.8	A

Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%.
2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

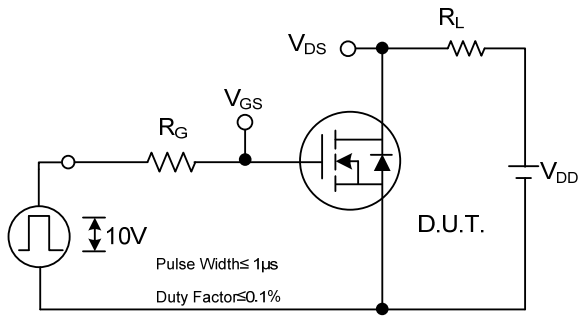


Peak Diode Recovery dv/dt Test Circuit

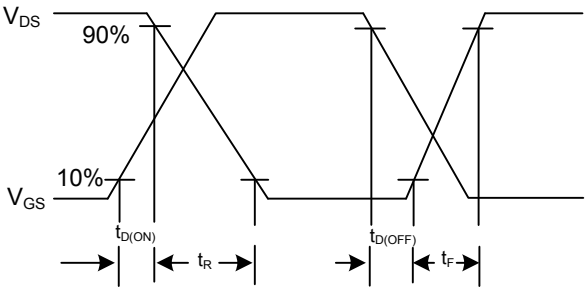


Peak Diode Recovery dv/dt Waveforms

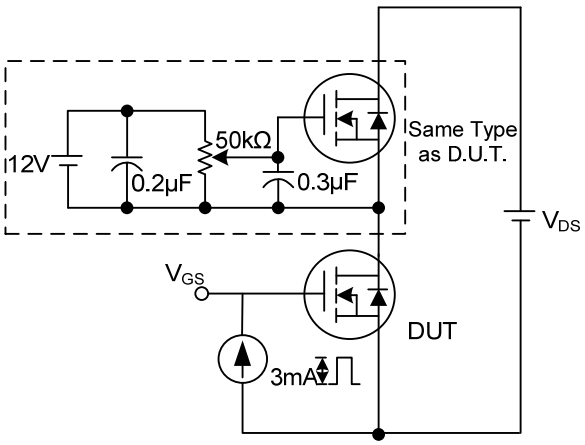
■ TEST CIRCUITS AND WAVEFORMS (Cont.)



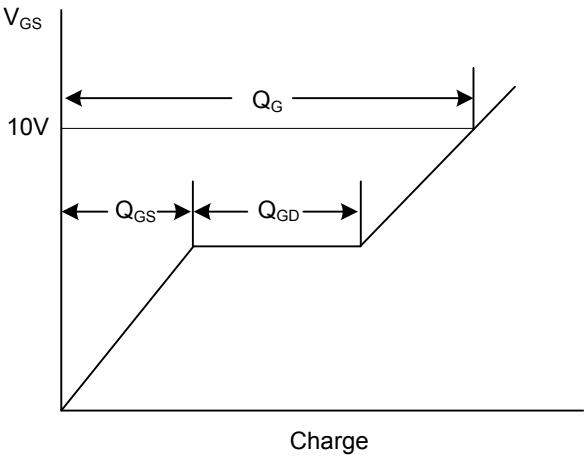
Switching Test Circuit



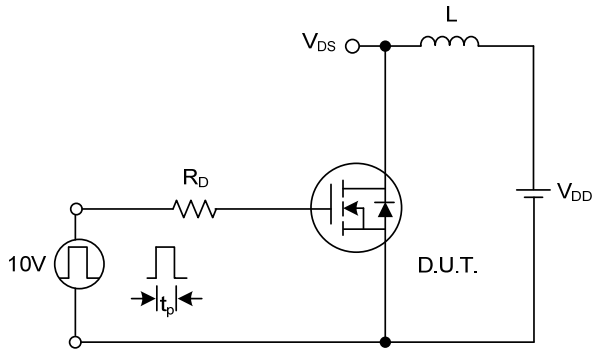
Switching Waveforms



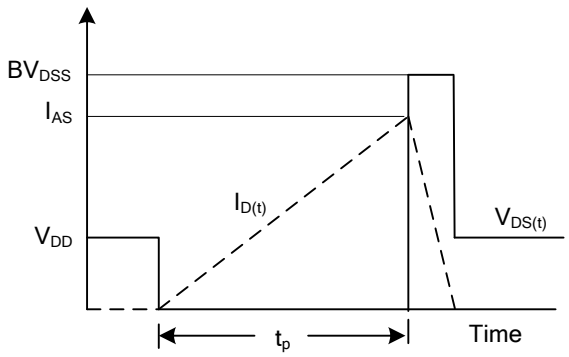
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

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