UNISONIC TECHNOLOGIES CO., LTD

7N65-CB Power MOSFET

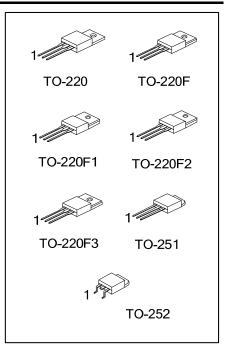
7.0A, 650V N-CHANNEL POWER MOSFET

DESCRIPTION

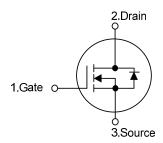
The UTC 7N65-CB is a high voltage power MOSFET designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche characteristics. This power MOSFET is usually used in high speed switching applications of switching power supplies and adaptors.

FEATURES

- * $R_{DS(ON)}$ < 1.1 Ω @ V_{GS} = 10 V, I_D = 3.5 A
- * Fast switching capability
- * Avalanche energy tested
- * Improved dv/dt capability, high ruggedness



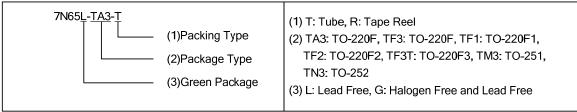
SYMBOL



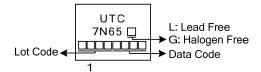
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing	
			FIII Assignment				
Lead Free	Halogen Free	1 dokage	1	2	3	. comig	
7N65L-TA3-T	7N65G-TA3-T	TO-220	G	D	S	Tube	
7N65L-TF3-T	7N65G-TF3-T	TO-220F	G	D	S	Tube	
7N65L-TF1-T	7N65G-TF1-T	TO-220F1	G	D	S	Tube	
7N65L-TF2-T	7N65G-TF2-T	TO-220F2	G	D	S	Tube	
7N65L-TF3T-T	7N65G-TF3T-T	TO-220F3	G	D	S	Tube	
7N65L-TM3-T	7N65G-TM3-T	TO-251	G	D	S	Tube	
7N65L-TN3-R	7N65G-TN3-R	TO-252	G	D	S	Tape Reel	

Pin Assignment: G: Gate D: Drain S: Source Note:



■ MARKING



■ **ABSOLUTE MAXIMUM RATINGS** (T_C = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V _{DSS}	650	V
Gate-Source Voltage		V_{GSS}	±30	V
Avalanche Current (Note 2)		I _{AR}	7	Α
Continuous Drain Current		I _D	7	Α
Pulsed Drain Current (Note 2)		I_{DM}	24	Α
Avalanche Energy	Single Pulsed (Note 3)	E _{AS}	245	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	3.0	V/ns
Power Dissipation	TO-220	P _D	125	W
	TO-220F/TO-220F1 TO-220F3		40	W
	TO-220F2		42	W
	TO-251/TO-252		55	W
Junction Temperature		T_J	+150	°C
Operating Temperature		T_OPR	-55 ~ + 150	°C
Storage Temperature		T_{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating : Pulse width limited by T_{J}
- 3. L = 10mH, I_{AS} = 7A, V_{DD} = 90V, R_{G} = 25 Ω , Starting T_{J} = 25°C
- 4. $I_{SD} \le 7A$, di/dt $\le 200A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25^{\circ}C$

■ THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT	
Junction to Ambient	TO-220/TO-220F TO-220F1/TO-220F2 TO-220F3	$\theta_{ m JA}$	62.5	°C/W	
	TO-251/TO-252		110		
Junction to Case	TO-220	θјс	1.0		
	TO-220F/TO-220F1 TO-220F3		3.2	°C/W	
	TO-220F2		2.97		
	TO-251/TO-252		2.27		

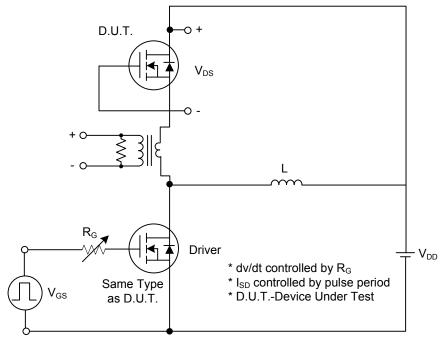
■ ELECTRICAL CHARACTERISTICS (T」=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	$V_{GS} = 0V, I_D = 250\mu A$	650			V
Drain-Source Leakage Current	I _{DSS}	$V_{DS} = 650V, V_{GS} = 0V$			10	μΑ
Gate- Source Leakage Current Forward	CSS	$V_{GS} = 30V, V_{DS} = 0V$			100	nA
Reverse		$V_{GS} = -30V, V_{DS} = 0V$			-100	nA
Breakdown Voltage Temperature Coefficient	$\triangle BV_{DSS}/\triangle T_{J}$	I _D =250μA, Referenced to 25°C		0.53		V/°C
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.0	V
Static Drain-Source On-State Resistance	R _{DS(ON)}	$V_{GS} = 10V, I_D = 3.5A$			1.1	Ω
DYNAMIC CHARACTERISTICS						
Input Capacitance	C _{ISS}	V _{DS} =25V, V _{GS} =0V,		359		pF
Output Capacitance	Coss	f=1.0 MHz		114		pF
Reverse Transfer Capacitance	C _{RSS}	1-1.0 WH 12		7		pF
SWITCHING CHARACTERISTICS						
Total Gate Charge	Q_G	V _{DS} =50V, V _{GS} =10V, I _D =1.3A,		118		nC
Gate-Source Charge	Q_GS	$I_D=100\mu A$ (Note 1, 2)		8.0		nC
Gate-Drain Charge	Q_GD	10-100μA (Note 1, 2)		8.4		nC
Turn-On Delay Time	t _{D(ON)}			48		ns
Turn-On Rise Time	t _R	V_{DD} =30V, V_{GS} =10V, I_{D} =0.5A,		36		ns
Turn-Off Delay Time	t _{D(OFF)}	$R_G = 25\Omega$ (Note 1, 2)		270		ns
Turn-Off Fall Time	t _F			50		ns
DRAIN-SOURCE DIODE CHARACTERISTI	CS AND MAXII	MUM RATINGS				
Maximum Continuous Drain-Source Diode	Is				7	Α
Forward Current					1	А
Maximum Pulsed Drain-Source Diode	I _{SM}				28	Α
Forward Current					20	^
Drain-Source Diode Forward Voltage	V_{SD}	$V_{GS} = 0 \text{ V}, I_{S} = 7 \text{ A}$			1.4	V
Body Diode Reverse Recovery Time	t _{rr}	I _S =7A, di/dt=100A/µs		400		ns
Body Diode Reverse Recovery Charge	Q_{RR}	15-17, αι/αι-1007/μ5		2.4		μC

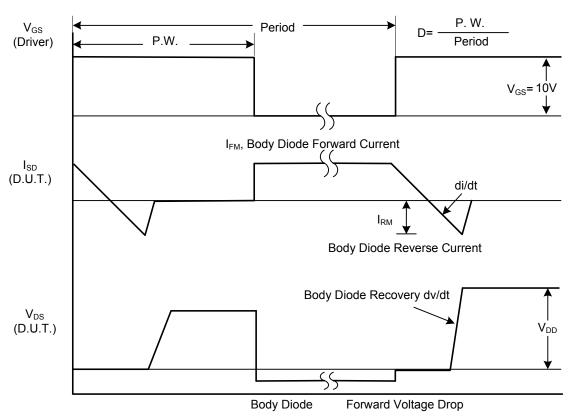
Notes: 1. Pulse Test: Pulse width ≤ 300µs, Duty cycle ≤ 2%

^{2.} Essentially independent of operating temperature

■ TEST CIRCUITS AND WAVEFORMS



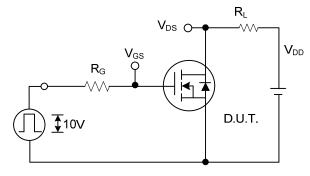
Peak Diode Recovery dv/dt Test Circuit



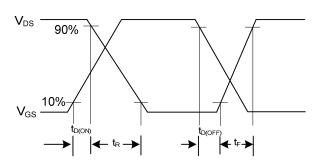
Peak Diode Recovery dv/dt Waveforms

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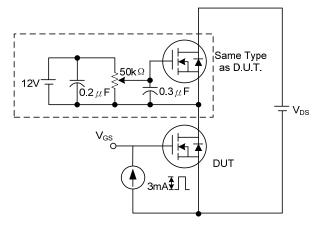
■ TEST CIRCUITS AND WAVEFORMS (Cont.)



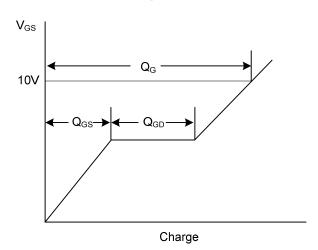
Switching Test Circuit



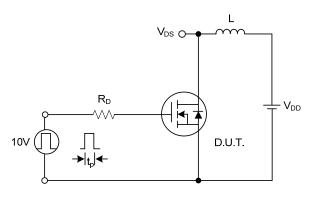
Switching Waveforms



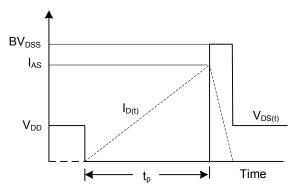
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

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