CMOS Digital Integrated Circuits Silicon Monolithic

# 7UL1T02FU

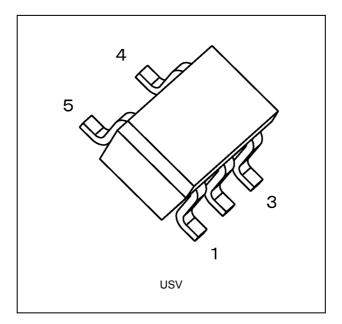
### 1. Functional Description

• 2-Input NOR Gate with Level Shifting

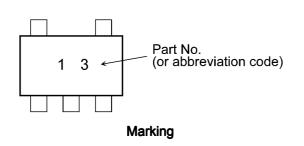
### 2. Features

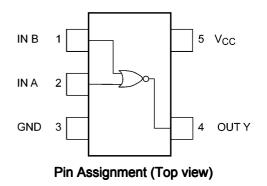
- (1) Wide operating voltage range:  $V_{CC}$  = 2.3 to 3.6 V
- (2) Output level up to supply V\_{CC} CMOS level: 1.65 V to 3.6 V (V\_{CC} = 3.6 V)
- (3) Output level down to supply V\_{CC} CMOS level: 3.6 V to 2.3 V (V\_{CC} = 2.3 V)
- (4) 3.6 V tolerant inputs
- (5) 3.6 V power down protection output

### 3. Packaging



4. Marking and Pin Assignment





Start of commercial production 2018-10 2018-06-18 Rev.1.0

### 5. IEC Logic Symbol



#### 6. Truth Table

А	В	Y
L	L	Н
L	Н	L
Н	L	L
н	Н	L

#### 7. Absolute Maximum Ratings (Note) (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V <sub>CC</sub>		-0.5 to 4.6	V
Input voltage	V <sub>IN</sub>		-0.5 to 4.6	V
DC output voltage	V <sub>OUT</sub>	(Note 1)	-0.5 to 4.6	V
		(Note 2)	-0.5 to V <sub>CC</sub> + 0.5	]
Input diode current	I <sub>IK</sub>		-20	mA
Output diode current	Ι <sub>ΟΚ</sub>	(Note 3)	-20	mA
DC output current	I <sub>OUT</sub>		±25	mA
V <sub>CC</sub> /ground current	I <sub>CC</sub>		±50	mA
Power dissipation	PD		200	mW
Storage temperature	T <sub>stg</sub>		-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1:  $V_{CC}$  = 0 V

Note 2: High (H) or Low (L) state. I<sub>OUT</sub> absolute maximum rating must be observed.

Note 3: V<sub>OUT</sub> < GND

### 8. Operating Ranges (Note)

Characteristics	Symbol	Note	Test Condition	Rating	Unit
Supply voltage	V <sub>CC</sub>		—	2.3 to 3.6	V
Input voltage	V <sub>IN</sub>			0 to 3.6	V
Output voltage	V <sub>OUT</sub>	(Note 1)		0 to 3.6	V
		(Note 2)		0 to V <sub>CC</sub>	
Output current	I <sub>OH</sub> ,I <sub>OL</sub>		V <sub>CC</sub> = 3.0 to 3.6 V	±8.0	mA
			V <sub>CC</sub> = 2.3 to 2.7 V	±4.0	
Operating temperature	T <sub>opr</sub>			-40 to 85	°C
Input rise and fall time	dt/dv		V <sub>IN</sub> = 1.65 to 3.6 V	0 to 10	ns/V

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either  $V_{CC}$  or GND.

Note 1:  $V_{CC} = 0 V$ 

Note 2: High (H) or Low (L) state.

#### 9. Electrical Characteristics

### 9.1. DC Characteristics (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Test Condition		V <sub>CC</sub> (V)	Min	Тур.	Max	Unit
High-level input voltage	V <sub>IH</sub>	—		2.3 to 2.7	1.1		_	V
				3.0 to 3.6	1.2		_	
Low-level input voltage	VIL	—		2.3 to 2.7	—	—	0.35	V
				3.0 to 3.6	—	—	0.5	
High-level output voltage	V <sub>OH</sub>	$V_{IN} = V_{IL}$	I <sub>OH</sub> = -0.02 mA	2.3 to 3.6	V <sub>CC</sub> -0.1	—	_	V
			I <sub>OH</sub> = -4.0 mA	2.3 to 2.7	2.0	—		
			I <sub>OH</sub> = -8.0 mA	3.0 to 3.6	2.48	_	_	]
Low-level output voltage	V <sub>OL</sub>	$V_{IN} = V_{IH} \text{ or } V_{IL}$	I <sub>OL</sub> = 0.02 mA	2.3 to 3.6	_	_	0.1	V
			I <sub>OL</sub> = 4.0 mA	2.3 to 2.7	—	_	0.4	
			I <sub>OL</sub> = 8.0 mA	3.0 to 3.6	_		0.4	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 0 to 3.6 V		0 to 3.6	_	_	±0.1	μA
Power-OFF leakage current	I <sub>OFF</sub>	V <sub>IN</sub> = 0 to 3.6 V, V <sub>OUT</sub> = 0 to 3.6 V		0	—	—	1.0	μA
Quiescent supply current	I <sub>CC</sub>	$V_{IN} = V_{CC}$ or GND		3.6	_		1.0	μA
Quiescent supply current	I <sub>CCT</sub>	V <sub>IN</sub> = 1.5 V		3.6	_	_	35	μA

### 9.2. DC Characteristics (Unless otherwise specified, T<sub>a</sub> = -40 to 85 °C)

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Min	Max	Unit	
High-level input voltage	V <sub>IH</sub>	—		2.3 to 2.7	1.1	—	V
				3.0 to 3.6	1.2	—	
Low-level input voltage	VIL	—		2.3 to 2.7	—	0.35	V
				3.0 to 3.6	—	0.5	
High-level output voltage	V <sub>OH</sub>	$V_{IN} = V_{IL}$	I <sub>OH</sub> = -0.02 mA	2.3 to 3.6	V <sub>CC</sub> -0.1	—	V
			I <sub>OH</sub> = -4.0 mA	2.3 to 2.7	2.0	—	
			I <sub>OH</sub> = -8.0 mA	3.0 to 3.6	2.48	—	
Low-level output voltage	V <sub>OL</sub>	$V_{IN} = V_{IH}$ or $V_{IL}$	I <sub>OL</sub> = 0.02 mA	2.3 to 3.6	—	0.1	V
			I <sub>OL</sub> = 4.0 mA	2.3 to 2.7	_	0.4	
			I <sub>OL</sub> = 8.0 mA	3.0 to 3.6	_	0.4	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 0 to 3.6 V		0 to 3.6	—	±0.5	μA
Power-OFF leakage current	I <sub>OFF</sub>	V <sub>IN</sub> = 0 to 3.6 V, V <sub>OUT</sub> = 0 to 3.6 V	0	—	10.0	μΑ	
Quiescent supply current	I <sub>CC</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND	3.6	_	10.0	μA	
Quiescent supply current	I <sub>CCT</sub>	V <sub>IN</sub> = 1.5 V		3.6	_	40	μA

#### 9.3. AC Characteristics (Unless otherwise specified, $T_a = 25$ °C, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Note	Test Condition	V <sub>CC</sub> (V)	V <sub>IN</sub> (V)	Min	Тур.	Max	Unit									
Propagation delay time	t <sub>PLH</sub>		C <sub>L</sub> = 15 pF	2.3 to 2.7	1.65 to 1.95	_	3.5	5.1	ns									
			$R_L = 1 M\Omega$		2.3 to 2.7	_	3.9	5.5										
					3.0 to 3.6	_	4.2	5.9										
				3.0 to 3.6	1.65 to 1.95	_	2.9	3.8										
					2.3 to 2.7	_	3.0	4.1										
														3.0 to 3.6		3.2	4.4	
Propagation delay time	t <sub>PHL</sub>		t <sub>PHL</sub> C <sub>L</sub> =	C <sub>L</sub> = 15 pF	2.3 to 2.7	1.65 to 1.95	_	3.6	5.6	ns								
										$R_L = 1 M\Omega$	$R_L = 1 M\Omega$		2.3 to 2.7	_	2.9	4.7		
					3.0 to 3.6	_	2.5	4.1										
				3.0 to 3.6	1.65 to 1.95		3.6	4.7										
					2.3 to 2.7	_	2.7	3.8										
					3.0 to 3.6	_	2.2	3.3										
Input capacitance	C <sub>IN</sub>		—	3.6	—	_	3	_	pF									
Power dissipation capacitance	C <sub>PD</sub>	(Note 1)	_	2.3 to 3.6	—	—	9	—	pF									

Note 1: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation.

 $I_{CC(opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$ 

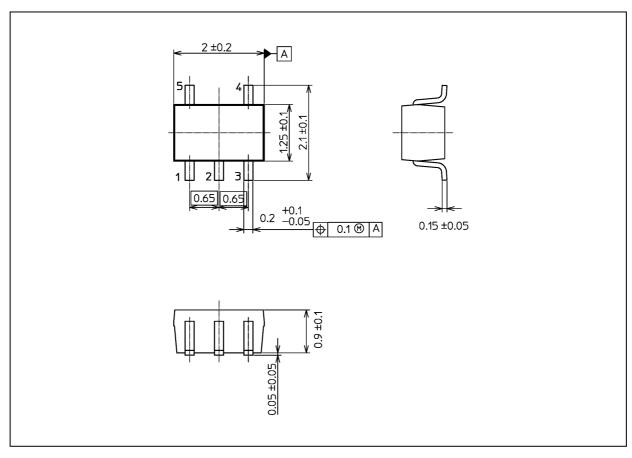
# 9.4. AC Characteristics (Unless otherwise specified, $T_a = -40$ to 85 °C, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	V <sub>IN</sub> (V)	Min	Max	Unit
Propagation delay time	t <sub>PLH</sub>	C <sub>L</sub> = 15 pF	2.3 to 2.7	1.65 to 1.95	1.0	6.0	ns
		R <sub>L</sub> = 1 ΜΩ		2.3 to 2.7	1.0	6.4	
				3.0 to 3.6	1.0	6.9	
			3.0 to 3.6	1.65 to 1.95	1.0	4.8	
				2.3 to 2.7	1.0	5.0	
				3.0 to 3.6	1.0	5.3	
Propagation delay time	t <sub>PHL</sub>	C <sub>L</sub> = 15 pF	2.3 to 2.7	1.65 to 1.95	1.0	5.9	ns
		R <sub>L</sub> = 1 ΜΩ		2.3 to 2.7	1.0	5.1	
				3.0 to 3.6	1.0	4.6	
			3.0 to 3.6	1.65 to 1.95	1.0	5.6	
						2.3 to 2.7	1.0
				3.0 to 3.6	1.0	4.1	

## 7UL1T02FU

#### **Package Dimensions**

Unit: mm



#### Weight: 6.2 mg (typ.)

	Package Name(s)	
Nickname: USV		

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