

8N10P

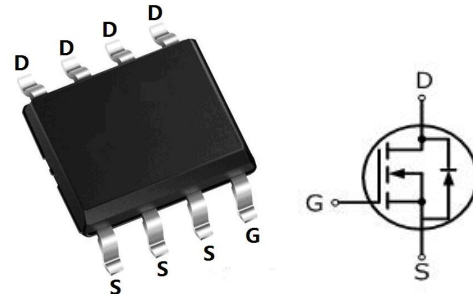
8 Amps, 100 Volts N-CHANNEL Power MOSFET

FEATURE

- 8A, 100V, $R_{DS(ON)MAX}=23m\Omega$ $V_{GS}=10V/8A$
- Low gate charge
- Low C_{iss}
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

APPLICATION

- High Frequency Piont-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- LCD/LED back light



SOP8L PIN CONFIGURATION

GENERAL DESCRIPTION

The 8N10P is the highest performance trench N-ch MOSFETs with extreme high cell density, which provide excellent R_{DS(on)} and gate charge for most of the synchronous buck converter applications.

The 8N10P meet the RoHS and Green product requirement, 100% EAS guaranteed with full function reliability approved.

Absolute Maximum Ratings ($T_C=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	8N10P	UNIT
Drain-Source Voltage	V_{DSS}	100	V
Gate-Source Voltage	V_{GSS}	± 20	
Continuous Drain Current	I_D	8	A
Pulsed Drain Current (Note 1)	I_{DM}	32	
Single Pulse Avalanche Energy (Note 2)	E_{AS}	11	mJ
Avalanche Current	I_{AS}	15	A
Reverse Diode dv/dt (Note 3)	dv/dt	5.5	V/ns
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 to +150	$^\circ\text{C}$
Channel Temperature	T_{CH}	150	$^\circ\text{C}$
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	T_L	260	$^\circ\text{C}$

Thermal Characteristics

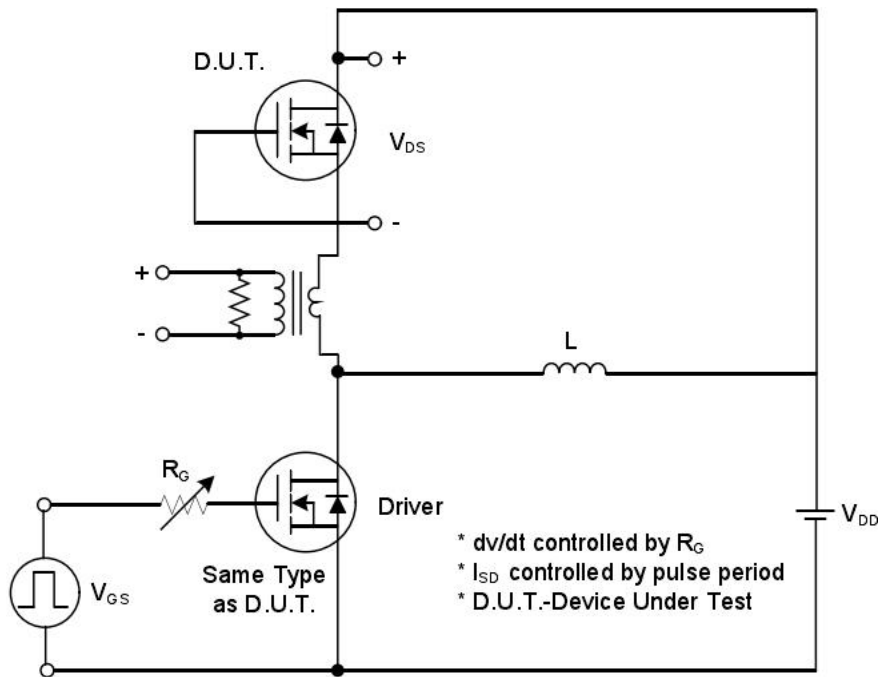
Parameter	Symbol	MAX	Units
Thermal resistance, Channel to Case	$R_{th(ch-c)}$	16	$^\circ\text{C}/\text{W}$
Thermal resistance, Channel to Ambient	$R_{th(ch-a)}$	59	$^\circ\text{C}/\text{W}$
Maximum Power Dissipation	$T_C=25^\circ\text{C}$ P_D	3.1	W

Electrical Characteristics ($T_c=25^\circ\text{C}$, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	100	—	—	V
Breakdown Temperature Coefficient	$\Delta BV_{DSS} / \Delta T_J$	Reference to 25°C , $I_D=250\mu A$	—	0.06	—	$V/^\circ\text{C}$
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=100V, V_{GS}=0V$	—	—	1	μA
	I_{DSS}	$V_{DS}=100V, V_{GS}=0V$ ($T_J = 55^\circ\text{C}$)	—	—	5	μA
Gate-Body Leakage Current, Forward	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	—	—	± 100	nA
On Characteristics						
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS}=10V, I_D=250\mu A$	1.0	—	3.0	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=8A$	—	18.5	23	m Ω
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS}=50V, V_{GS}=0V,$ $f=1.0\text{MHZ}$	—	1190	—	pF
Output Capacitance	C_{oss}		—	95	—	pF
Reverse Transfer Capacitance	C_{rss}		—	7	—	pF
Switching Characteristics						
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=50V,$ $R_L=6.25\Omega,$ $R_{GEN}=3\Omega$ $V_{GS}=10V$ (Note4,5)	—	7	—	ns
Turn-On Rise Time	t_r		—	3	—	ns
Turn-Off Delay Time	$t_{d(off)}$		—	20	—	ns
Turn-Off Fall Time	t_f		—	3	—	ns
Total Gate Charge	Q_g	$V_{DS}=50V, I_D=8A,$ $V_{GS}=10V,$ (Note4,5)	—	16	—	nC
Gate-Source Charge	Q_{gs}		—	4.5	—	nC
Gate-Drain Charge	Q_{gd}		—	2.5	—	nC
Drain-Source Body Diode Characteristics and Maximum Ratings						
Continuous Diode Forward Current	I_S	$V_G = V_D = 0V,$ Force Current	—	—	8	A
Pulsed Diode Forward Current	I_{SM}		—	—	32	A
Diode Forward Voltage	V_{SD}	$I_S=10A, V_{GS}=0V$	—	—	1.0	V

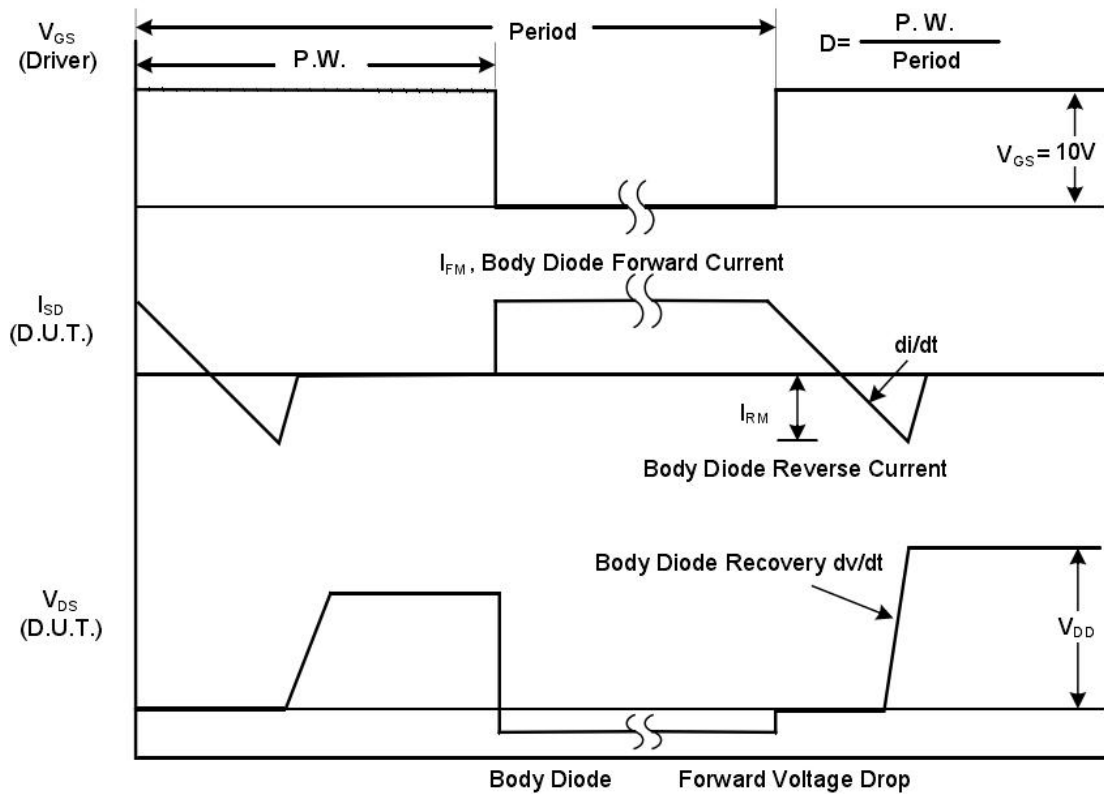
Notes

1. Repetitive Rating: pulse width limited by maximum junction temperature.
2. $V_{DD}=25V, L=0.1\text{mH}, R_g=25\Omega, I_{AS}=13A$, starting $T_J=25^\circ\text{C}$.
3. $I_{SD} \leq I_D, dI/dt=200A/\mu s, V_{DD} \leq BV_{DSS}$, starting $T_J=25^\circ\text{C}$, Pulse width $\leq 300\mu s$; duty cycle $\leq 2\%$.
4. Repetitive rating; pulse width limited by maximum junction temperature.

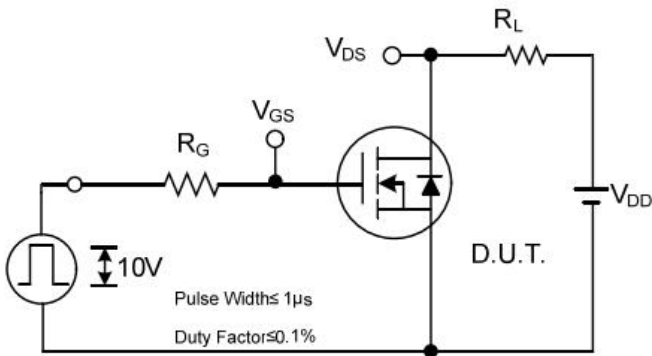
RATING AND CHARACTERISTIC CURVES



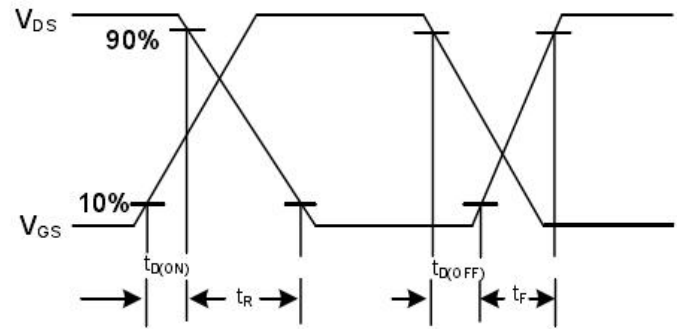
Peak Diode Recovery dv/dt Test Circuit



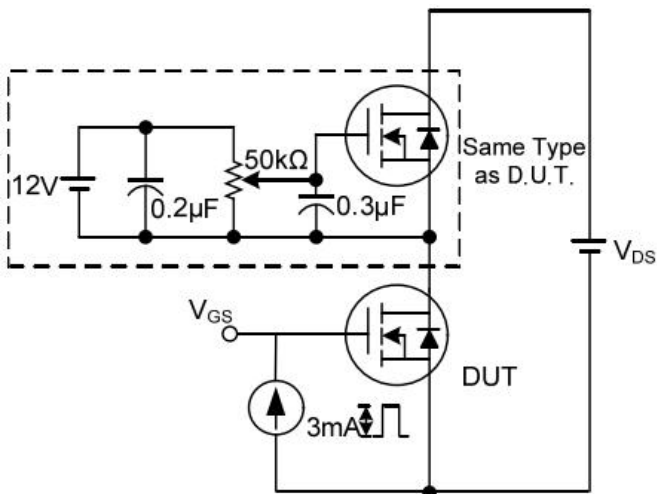
Peak Diode Recovery dv/dt Waveforms



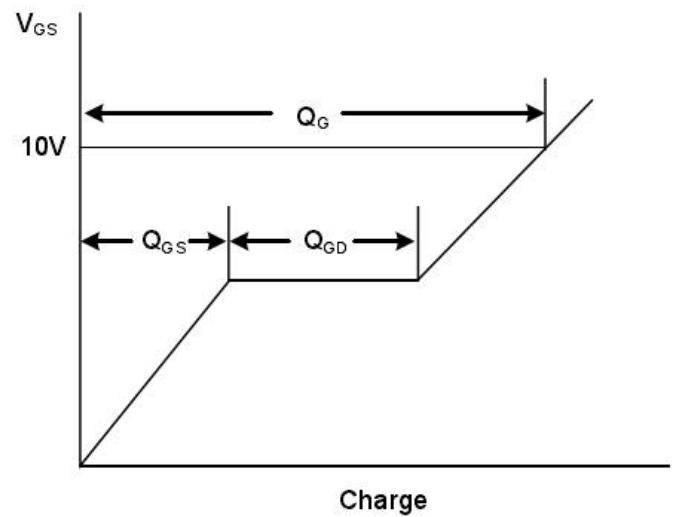
Switching Test Circuit



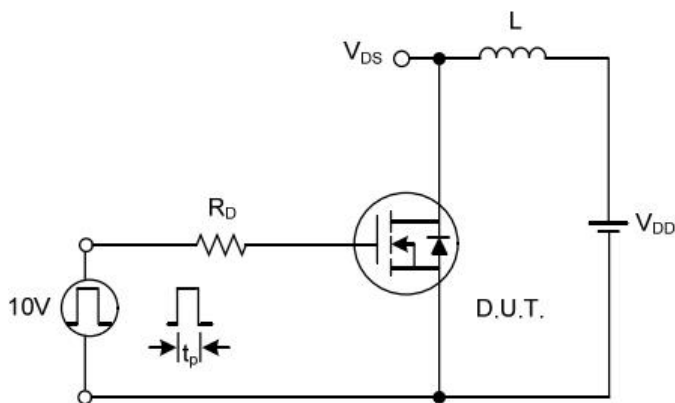
Switching Waveforms



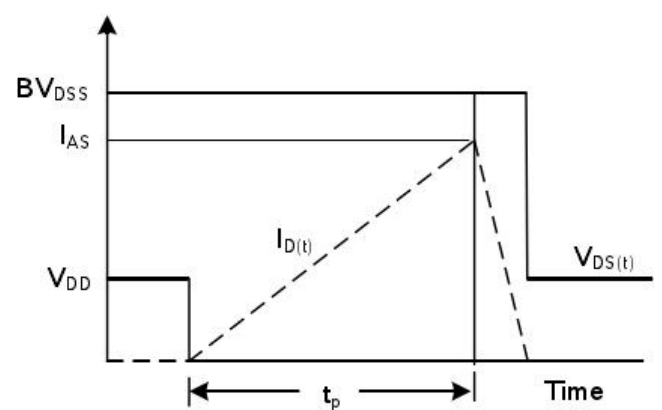
Gate Charge Test Circuit



Gate Charge Waveform

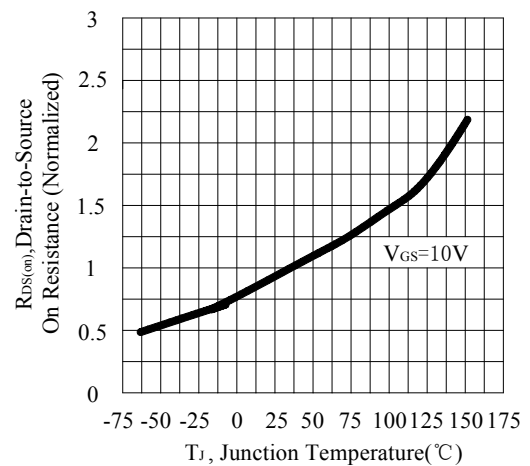
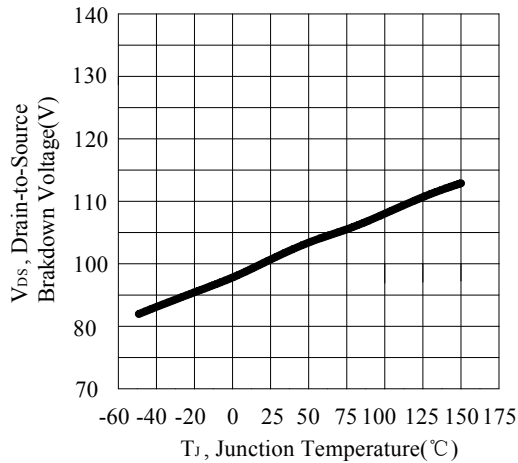
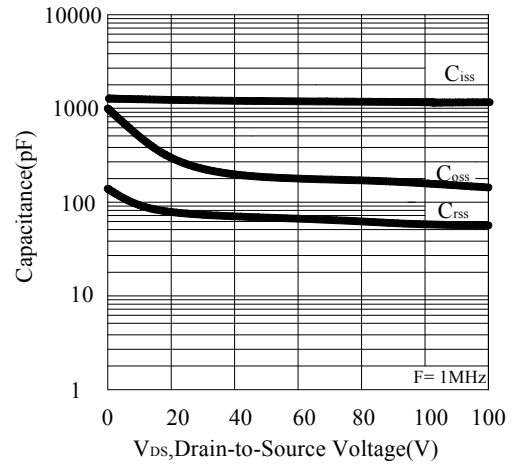
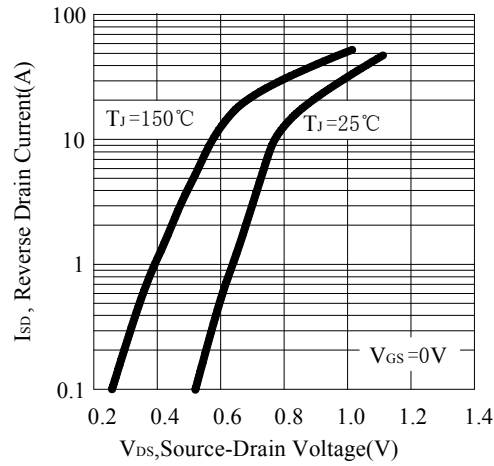
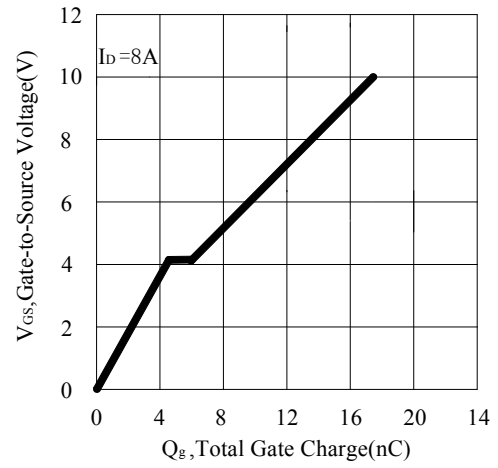
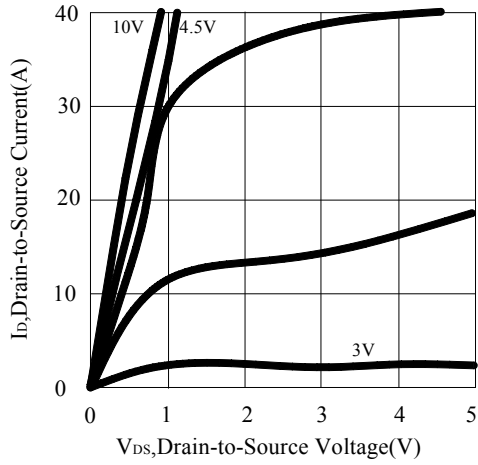


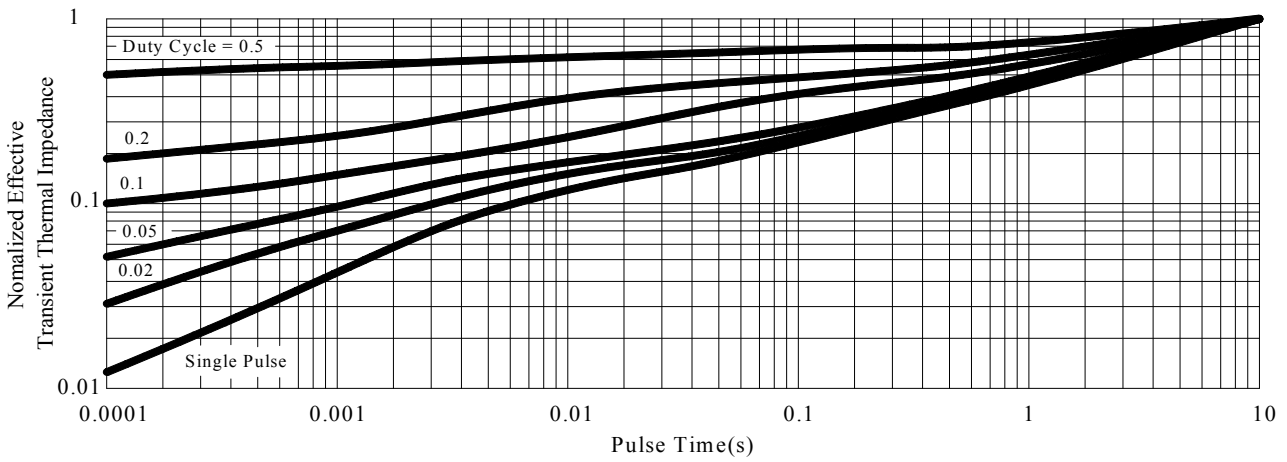
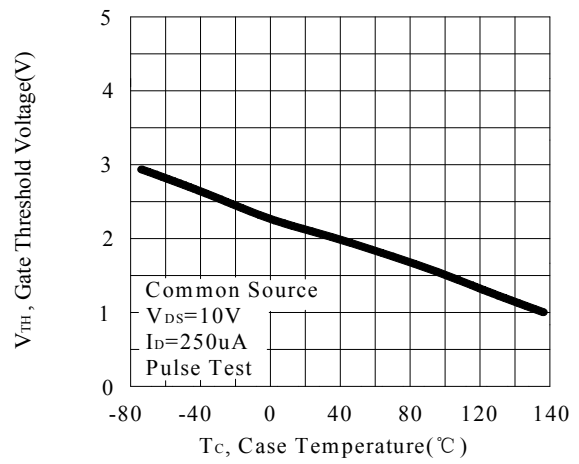
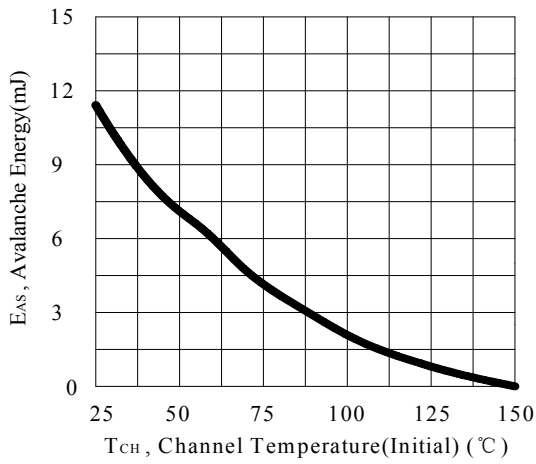
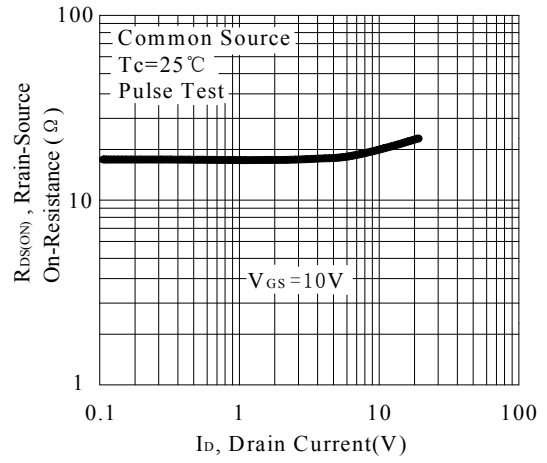
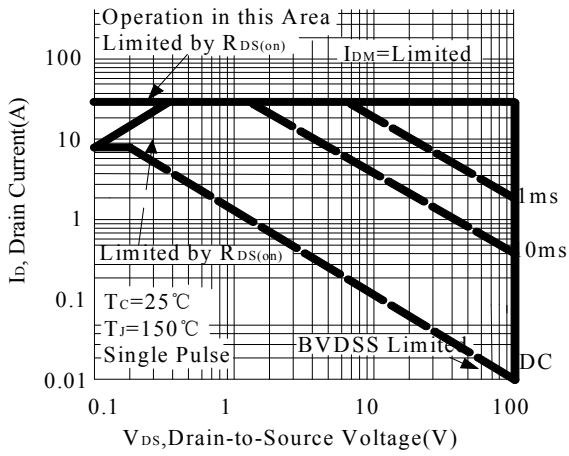
Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

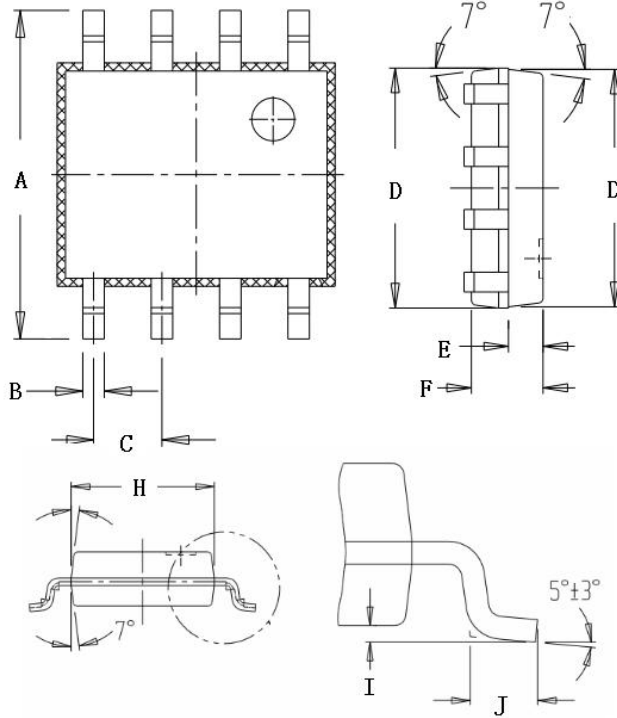
RATING AND CHARACTERISTIC CURVES





PACKAGE OUTLINE DIMENSIONS

SOP8L



SOP8L		
Dim	Min	Max
A	.236 (6.0)	.245 (6.20)
B	.014 (0.37)	.017 (0.43)
C	—	.050 (1.27)
D	.188 (4.80)	.194 (4.92)
E	.025 (0.65)	.030 (0.75)
F	.055 (1.40)	.060 (1.50)
H	.149 (3.80)	.154 (3.90)
I	.003 (0.10)	.008 (0.20)
J	.019 (0.50)	.028 (0.70)

Dimensions in inches and (millimeters)