

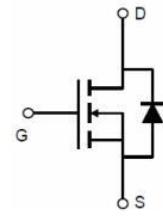


## 9435A

## -30V P-Channel Enhancement Mode MOSFET

### General Description:

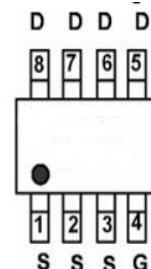
The 9435A is the single P-Channel logic enhancement mode power field effect transistors to provide excellent  $R_{DS(on)}$ , low gate charge and low gate resistance. It's up to -30V operation voltage is well suited in switching mode power supply, SMPS, notebook computer power management and other battery powered circuits.



Schematic Diagram

### Features:

$R_{DS(ON)} < 55\text{m} @ V_{GS}=10\text{V}$  (N-Ch)  
 $R_{DS(ON)} < 90\text{m} @ V_{GS}=4.5\text{V}$  (N-Ch)  
Super high density cell design for extremely low  
 $R_{DS(ON)}$  Exceptional on-resistance and maximum DC current



Marking and pin Assignment

### Applications:

Switching power supply, SMPS  
Battery Powered System  
DC/DC Converter  
DC/AC Converter  
Load Switch

### Package Marking and Ordering Information

| Product ID | Pack  | Marking       | Qty(PCS) |
|------------|-------|---------------|----------|
| 9435A      | SOP-8 | 9435A XX YYYY | 3000     |

### Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ unless otherwise noted)

| Parameter  | Symbol         | Limit      | Unit             |
|--|----------------|------------|------------------|
| Drain-Source Voltage                             | $V_{DS}$       | -30        | V                |
| Gate-Source Voltage                              | $V_{GS}$       | $\pm 20$   | V                |
| Drain Current-Continuous                         | $I_D$          | -5.1       | A                |
| Drain Current-Pulsed (Note 1)                    | $I_{DM}$       | -20        | A                |
| Maximum Power Dissipation                        | $P_D$          | 2.5        | W                |
| Operating Junction and Storage Temperature Range | $T_J, T_{STG}$ | -55 To 150 | $^\circ\text{C}$ |

### Thermal Characteristic

|  |                 |    |                           |
|--|-----------------|----|---------------------------|
| Thermal Resistance, Junction-to-Ambient (Note 2) | $R_{\theta JA}$ | 50 | $^\circ\text{C}/\text{W}$ |
|--|-----------------|----|---------------------------|



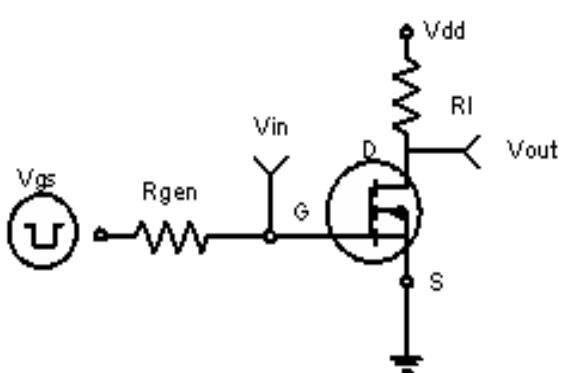
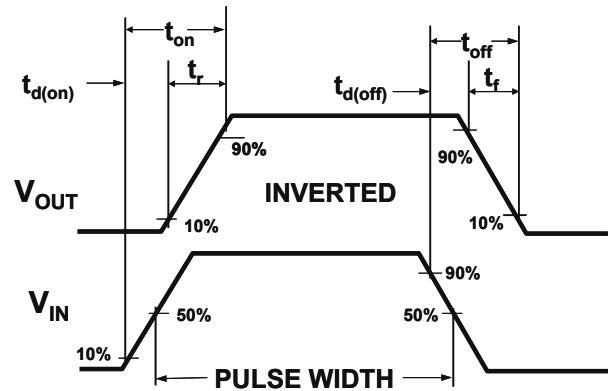
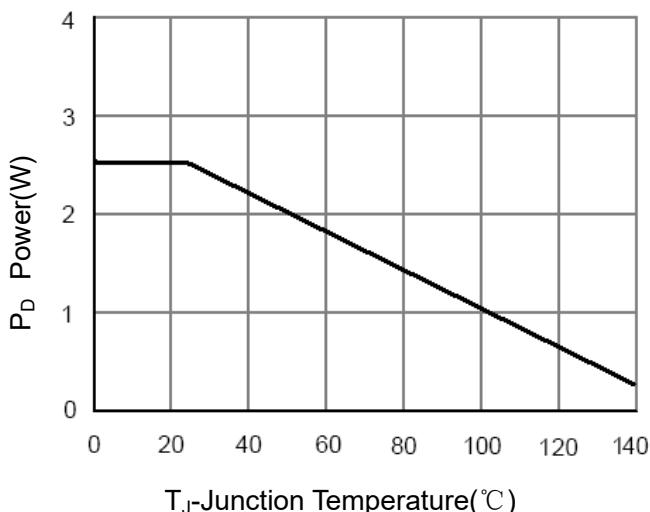
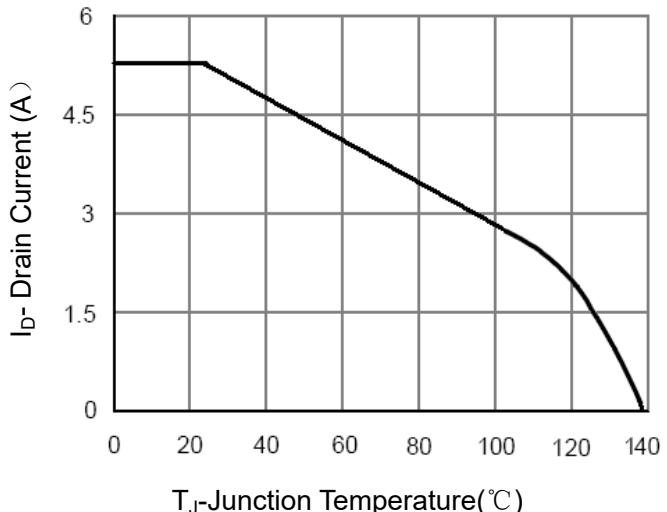
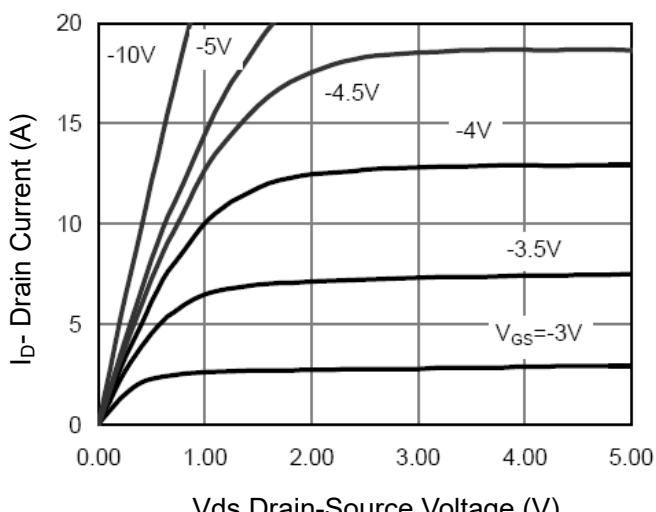
9435A

**-30V P-Channel Enhancement Mode MOSFET****Electrical Characteristics ( $T_A=25^\circ\text{C}$  unless otherwise noted)**

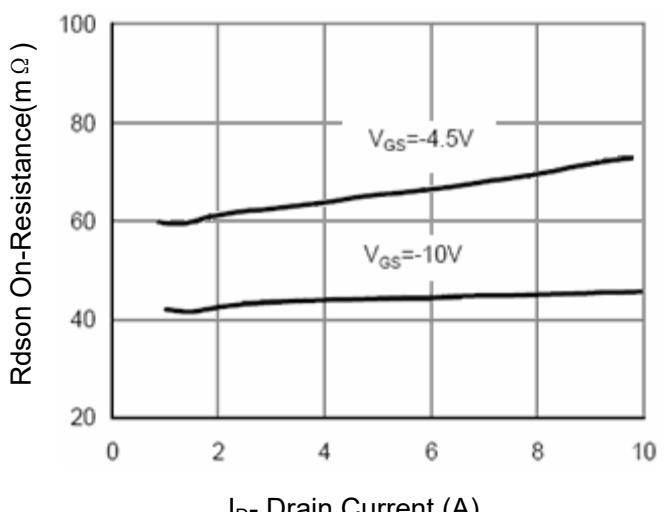
| Parameter                                 | Symbol                     | Condition   | Min | Typ  | Max      | Unit             |
|---|----------------------------|---|-----|------|----------|------------------|
| <b>Off Characteristics</b>                |                            |   |     |      |          |                  |
| Drain-Source Breakdown Voltage            | $\text{BV}_{\text{DSS}}$   | $V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$   | -30 | -33  | -        | V                |
| Zero Gate Voltage Drain Current           | $I_{\text{DSS}}$           | $V_{\text{DS}}=-24\text{V}, V_{\text{GS}}=0\text{V}$  | -   | -    | -1       | $\mu\text{A}$    |
| Gate-Body Leakage Current                 | $I_{\text{GSS}}$           | $V_{\text{GS}}=\pm20\text{V}, V_{\text{DS}}=0\text{V}$  | -   | -    | $\pm100$ | nA               |
| <b>On Characteristics</b> (Note 3)        |                            |   |     |      |          |                  |
| Gate Threshold Voltage                    | $V_{\text{GS}(\text{th})}$ | $V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$   | -1  | -1.6 | -3       | V                |
| Drain-Source On-State Resistance          | $R_{\text{DS}(\text{ON})}$ | $V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-5.1\text{A}$  | -   | 43   | 55       | $\text{m}\Omega$ |
|   |                            | $V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-4.2\text{A}$   | -   | 62   | 90       | $\text{m}\Omega$ |
| Forward Transconductance                  | $g_{\text{FS}}$            | $V_{\text{DS}}=-15\text{V}, I_{\text{D}}=-4.5\text{A}$  | 4   | 7    | -        | S                |
| <b>Dynamic Characteristics</b> (Note 4)   |                            |   |     |      |          |                  |
| Input Capacitance                         | $C_{\text{iss}}$           | $V_{\text{DS}}=-15\text{V}, V_{\text{GS}}=0\text{V}, F=1.0\text{MHz}$                                   | -   | 520  | -        | PF               |
| Output Capacitance                        | $C_{\text{oss}}$           |   | -   | 130  | -        | PF               |
| Reverse Transfer Capacitance              | $C_{\text{rss}}$           |   | -   | 70   | -        | PF               |
| <b>Switching Characteristics</b> (Note 4) |                            |   |     |      |          |                  |
| Turn-on Delay Time                        | $t_{\text{d}(\text{on})}$  | $V_{\text{DD}}=-15\text{V}, I_{\text{D}}=-1\text{A}, V_{\text{GS}}=-10\text{V}, R_{\text{GEN}}=6\Omega$ | -   | 7    | -        | nS               |
| Turn-on Rise Time                         | $t_r$                      |   | -   | 13   | -        | nS               |
| Turn-Off Delay Time                       | $t_{\text{d}(\text{off})}$ |   | -   | 14   | -        | nS               |
| Turn-Off Fall Time                        | $t_f$                      |   | -   | 9    | -        | nS               |
| Total Gate Charge                         | $Q_g$                      | $V_{\text{DS}}=-15\text{V}, I_{\text{D}}=-5.1\text{A}, V_{\text{GS}}=-10\text{V}$                       | -   | 11   | -        | nC               |
| Gate-Source Charge                        | $Q_{\text{gs}}$            |   | -   | 2.2  | -        | nC               |
| Gate-Drain Charge                         | $Q_{\text{gd}}$            |   | -   | 3    | -        | nC               |
| <b>Drain-Source Diode Characteristics</b> |                            |   |     |      |          |                  |
| Diode Forward Voltage (Note 3)            | $V_{\text{SD}}$            | $V_{\text{GS}}=0\text{V}, I_{\text{S}}=-5.1\text{A}$  | -   | -    | -1.2     | V                |

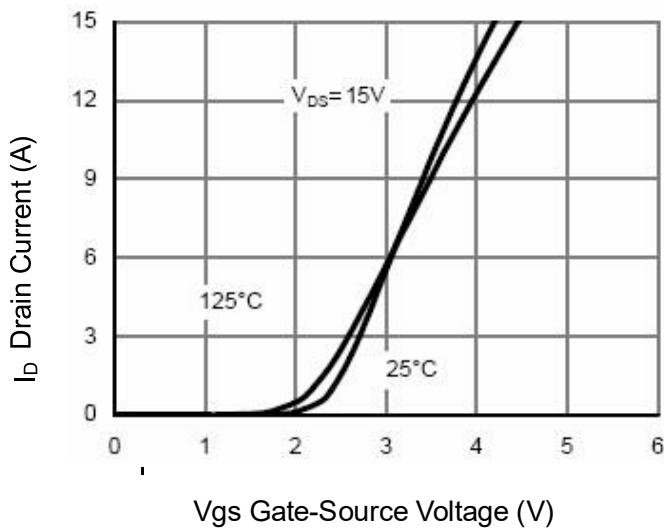
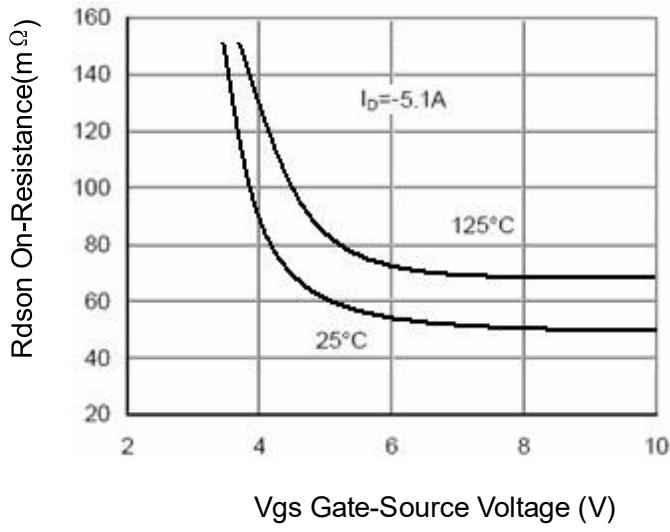
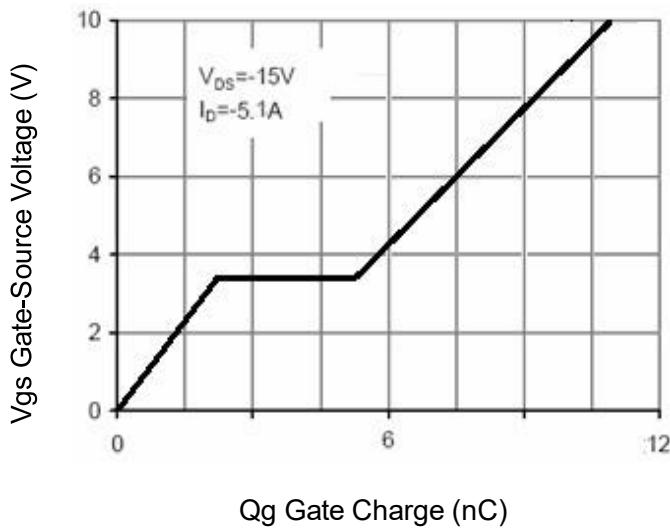
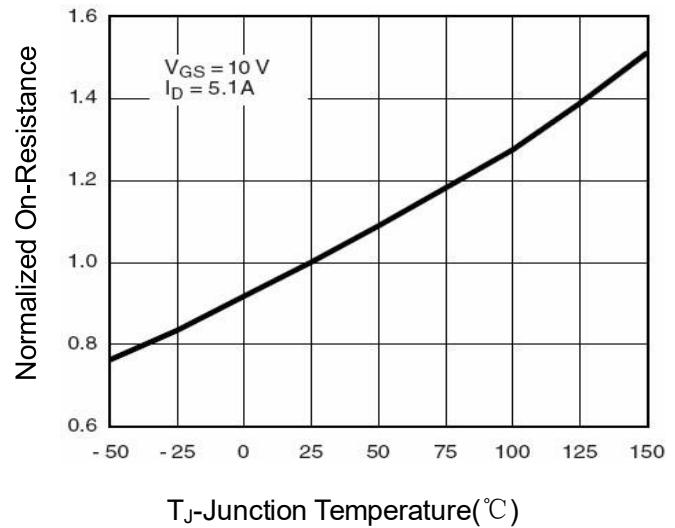
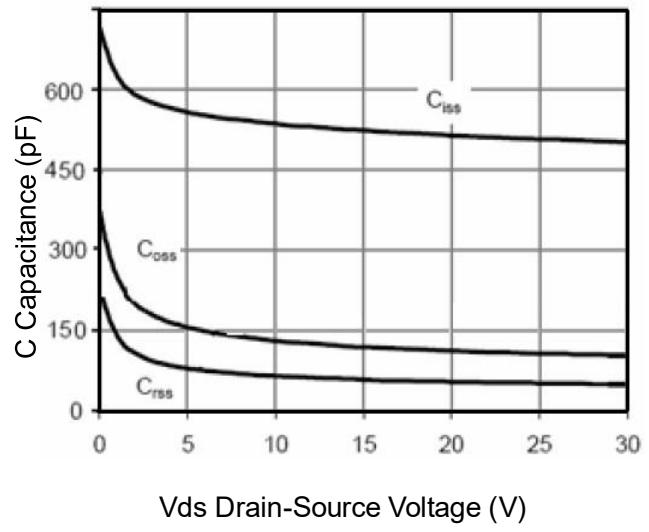
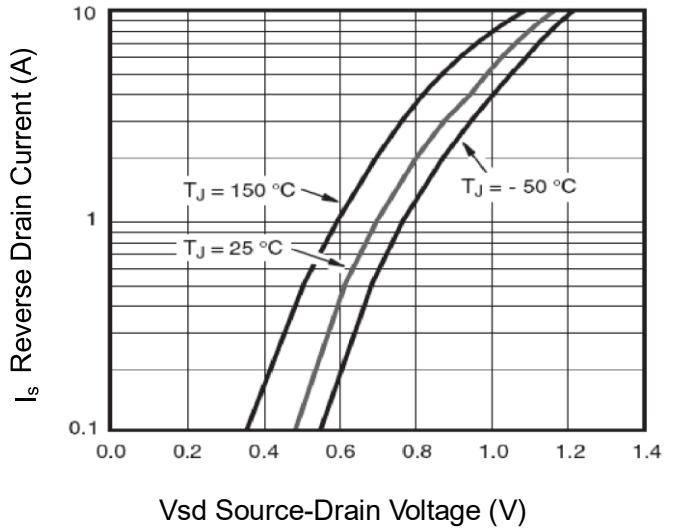
**Notes:**

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production

**9435A****-30V P-Channel Enhancement Mode MOSFET****Typical Electrical and Thermal Characteristics****Figure 1:Switching Test Circuit****Figure 2:Switching Waveforms**T<sub>J</sub>-Junction Temperature(°C)**Figure 3 Power Dissipation**T<sub>J</sub>-Junction Temperature(°C)**Figure 4 Drain Current**

Vds Drain-Source Voltage (V)

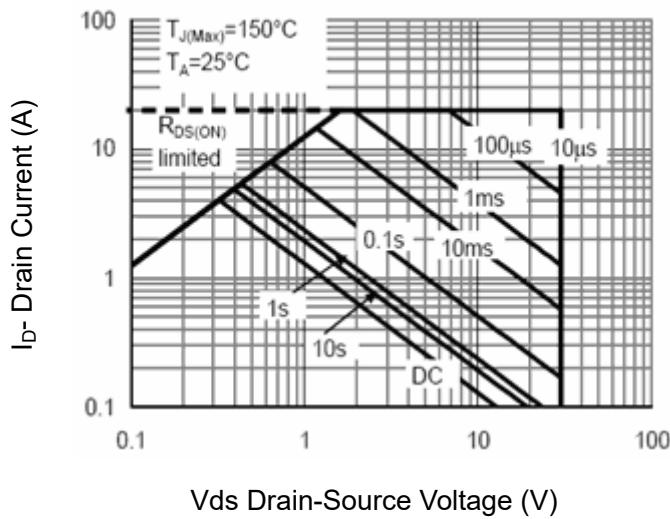
**Figure 5 Output Characteristics**I<sub>D</sub>- Drain Current (A)**Figure 6 Drain-Source On-Resistance**

**9435A****-30V P-Channel Enhancement Mode MOSFET****Figure 5 Output Characteristics****Figure 7 Transfer Characteristics****Figure 9 Rdson vs Vgs****Figure 11 Gate Charge****Figure 8 Drain-Source On-Resistance****Figure 10 Capacitance vs Vds****Figure 12 Source-Drain Diode Forward**

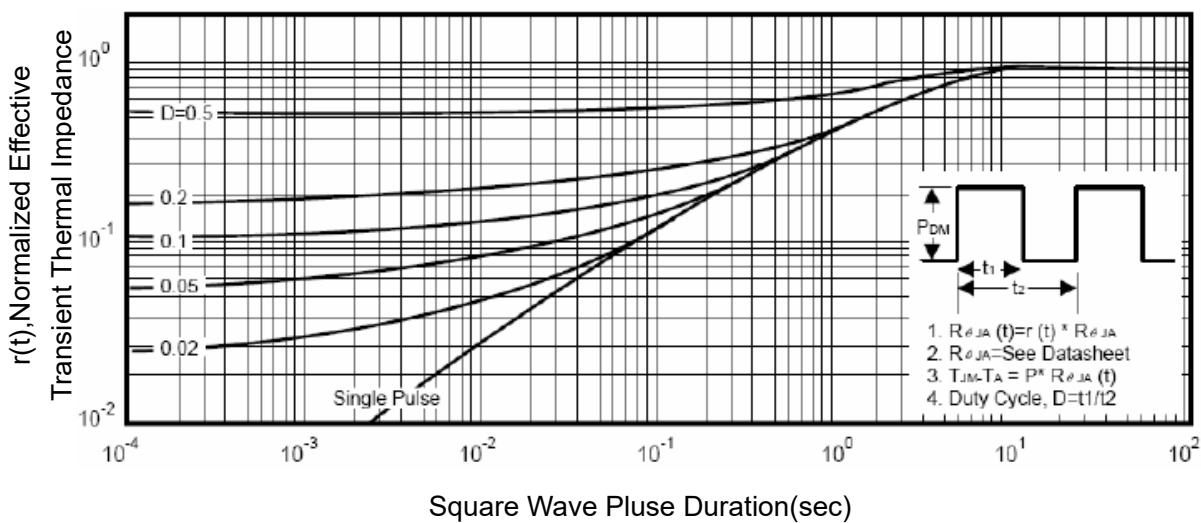


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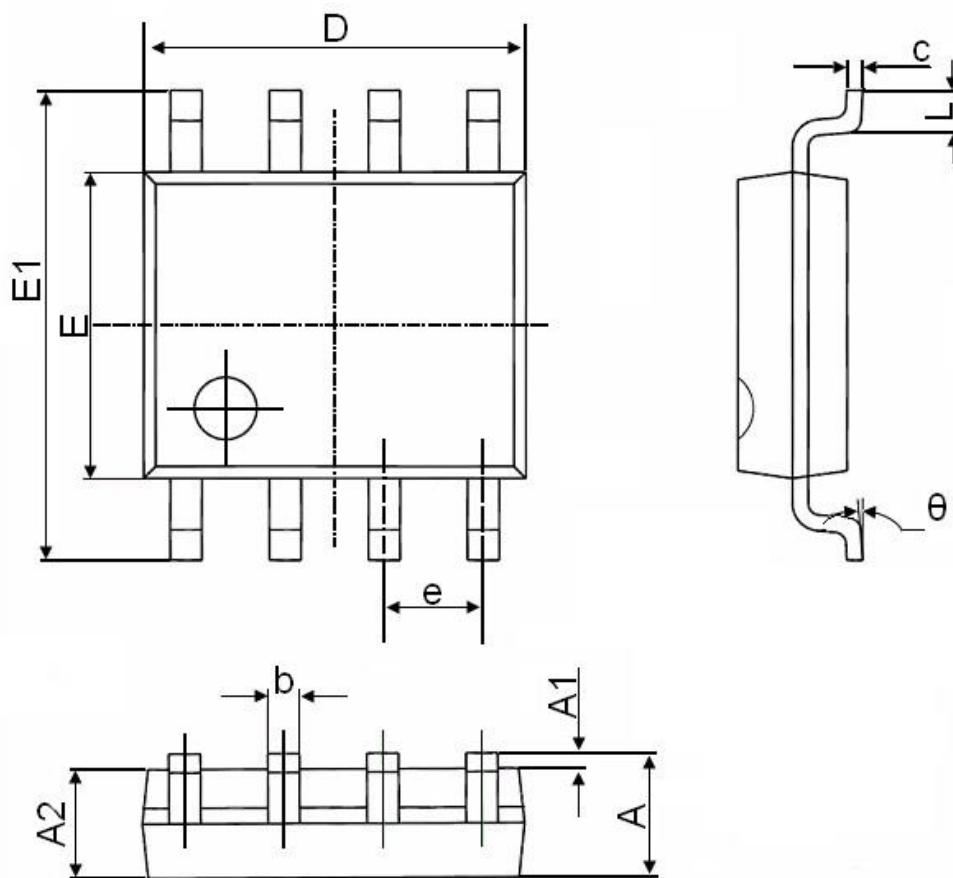
## -30V P-Channel Enhancement Mode MOSFET



**Figure 13 Safe Operation Area**



**Figure 14 Normalized Maximum Transient Thermal Impedance**

**9435A****-30V P-Channel Enhancement Mode MOSFET****SOP-8 Package Information**

| Symbol | Dimensions In Millimeters |       | Dimensions In Inches |       |
|--------|---------------------------|-------|----------------------|-------|
|        | Min.                      | Max.  | Min.                 | Max.  |
| A      | 1.350                     | 1.750 | 0.053                | 0.069 |
| A1     | 0.100                     | 0.250 | 0.004                | 0.010 |
| A2     | 1.350                     | 1.550 | 0.053                | 0.061 |
| b      | 0.330                     | 0.510 | 0.013                | 0.020 |
| c      | 0.170                     | 0.250 | 0.006                | 0.010 |
| D      | 4.700                     | 5.100 | 0.185                | 0.200 |
| E      | 3.800                     | 4.000 | 0.150                | 0.157 |
| E1     | 5.800                     | 6.200 | 0.228                | 0.244 |
| e      | 1.270(BSC)                |       | 0.050(BSC)           |       |
| L      | 0.400                     | 1.270 | 0.016                | 0.050 |
| θ      | 0°                        | 8°    | 0°                   | 8°    |