



DESCRIPTION

The A1085 is a series of low dropout three terminal regulators with a dropout of 1.3V at 3A load current.

Other than a fixed version ($V_{OUT}=1.8V, 2.5V, 3.3V, 5V$), The A1085 has an adjustable version, which can set the output voltage with only two external resistors.

The A1085 offers thermal shut down and current limit functions, to assure the stability of chip and power system. And the A1085 uses trimming technique to guarantee output voltage accuracy within $\pm 2\%$

The A1085 is available in TO-252, TO-263-2 and TO-263-3 package

ORDERING INFORMATION

Package Type	Part Number	
TO-252	D	A1085DR-XX
		A1085DVR-XX
TO-263-2	S2	A1085S2R-XX
		A1085S2VR-XX
TO-263-3	S3	A1085S3R-XX
		A1085S3VR-XX
Note	XX: Output Voltage, 33 = 3.3V, 50 = 5V ADJ: Adjustable R : Tape & Reel V: Green Package	
AiT provides all Pb free products Suffix "V" means Green Package		

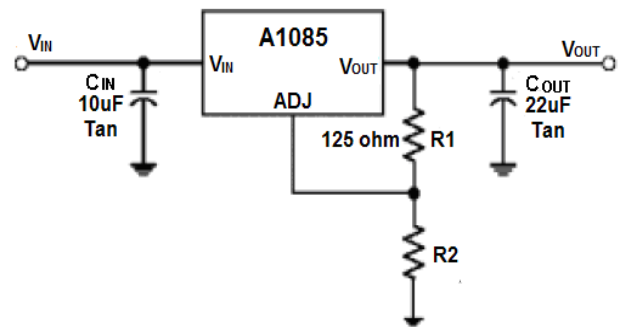
FEATURES

- Provide Fixed Version and an Adjustable Version, Output Value can be customized on command.
 - Maximum Output Current: 3A
 - Output Voltage Accuracy within $\pm 2\%$
 - Range of Operation Input Voltage: Max 15V
 - Line Regulation: 0.2% (Typ.)
 - Load Regulation: 0.4% (Typ.)
 - Environment Temperature: $-50^{\circ}C \sim +140^{\circ}C$
- Available in TO-252, TO-263-2 and TO-263-3 Package

APPLICATION

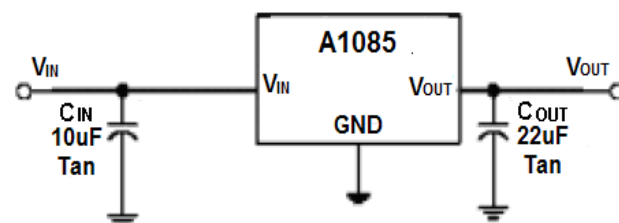
- Power Management for Computer Mother Board, Graphic Card
- Battery Charger
- Microprocessor Supply
- Post Regulators for Switching Supplies

TYPICAL APPLICATION (Schematics 1)



Adjustable Version

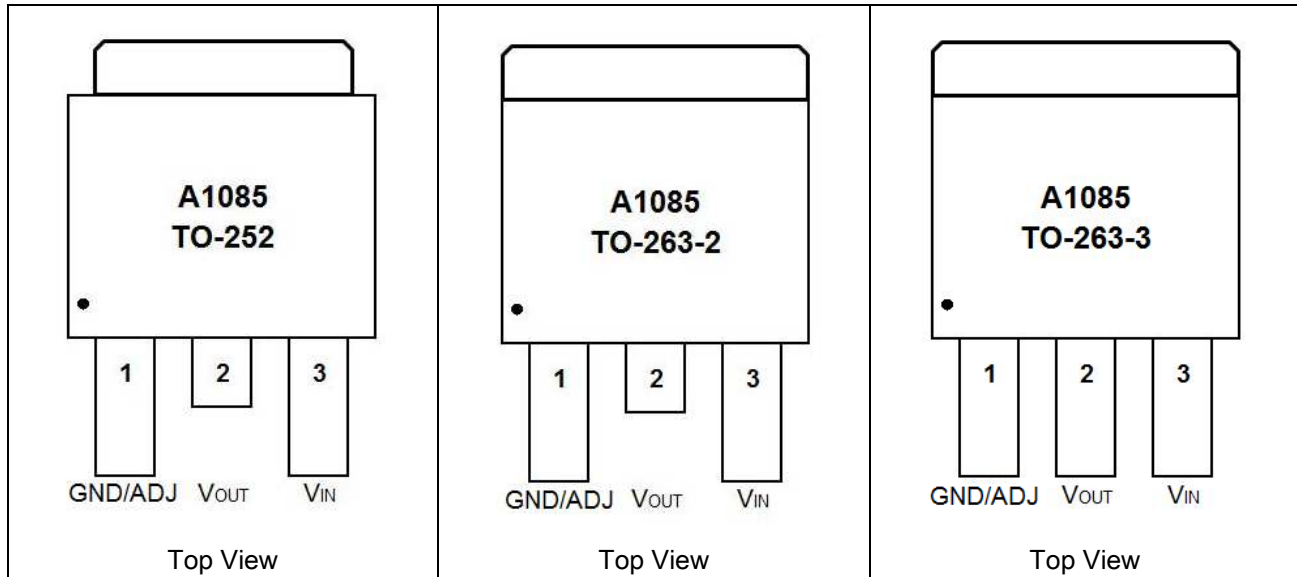
$$V_{OUT} = V_{REF} \times (1 + R2 / R1) + I_{ADJ} \times R2$$



Fixed Output Version



PIN DESCRIPTION



Pin #	TO-252		TO-263-2		TO-263-3	
	A1085DR-XX	A1085DR-ADJ	A1085S2R-XX	A1085S2R-ADJ	A1085S3R-XX	A1085S3R-ADJ
1	GND	ADJ	GND	ADJ	GND	ADJ
2	V _{OUT}	V _{OUT}	V _{OUT}	V _{OUT}	V _{OUT}	V _{OUT}
3	V _{IN}	V _{IN}	V _{IN}	V _{IN}	V _{IN}	V _{IN}

ABSOLUTE MAXIMUM RATINGS

Maximum Input Voltage	15V
Junction Temperature (T _J)	150°C
Environment Temperature (T _A)	80°C
Storage Temperature (T _s)	-65°C to 150°C
Lead Temperature and Time	260°C, 10S

Stresses above may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the Electrical Characteristics is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



ELECTRICAL CHARACTERISTICS

Test Condition: $C_{IN}=10\mu F$, $C_{OUT}=22\mu F$, $T_A = 25^\circ C$, unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
V_{REF}	Reference Voltage	$I_{OUT}=10mA$, $V_{IN}-V_{OUT}=3V$ $10mA \leq I_{OUT} \leq 3A$, $5V \leq V_{IN}-V_{OUT} \leq 5V$	1.238 1.225	1.25 1.25	1.262 1.275	V
V_{OUT}	Output Voltage	A1085-1.8V $I_{OUT}=0mA$, $V_{IN}=4.8V$, $T_J=25^\circ C$ $10mA \leq I_{OUT} \leq 3A$, $3.4V \leq V_{IN} \leq 7V$	1.782 1.764	1.80 1.80	1.818 1.836	V
		A1085-2.5V $I_{OUT}=0mA$, $V_{IN}=4.8V$, $T_J=25^\circ C$ $10mA \leq I_{OUT} \leq 3A$, $4.1V \leq V_{IN} \leq 7V$	2.475 2.450	2.50 2.50	2.525 2.550	V
		A1085-3.3V $I_{OUT}=0mA$, $V_{IN}=6.3V$, $T_J=25^\circ C$ $10mA \leq I_{OUT} \leq 3A$, $4.9V \leq V_{IN} \leq 8V$	3.267 3.234	3.3 3.3	3.333 3.366	V
		A1085-5.0V $I_{OUT}=0mA$, $V_{IN}=8.0V$, $T_J=25^\circ C$ $10mA \leq I_{OUT} \leq 3A$, $6.6V \leq V_{IN} \leq 10V$	4.95 4.90	5.0 5.0	5.05 5.10	V
ΔV_{OUT}	Line Regulation (Note1)	A1085-ADJ $I_{OUT}=10mA$, $2.85V \leq V_{IN} \leq 10V$		0.035	0.2	%
		A1085-1.8V $I_{OUT}=10mA$, $3.4V \leq V_{IN} \leq 10V$		1	5	mV
		A1085-2.5V $I_{OUT}=10mA$, $4.9V \leq V_{IN} \leq 10V$		1	5	mV
		A1085-5.0V $I_{OUT}=10mA$, $6.6V \leq V_{IN} \leq 10V$		1	5	mV
ΔV_{OOT}	Load Regulation (Note1, 2)	A1085-ADJ $V_{IN}-V_{OUT}=3V$, $10mA \leq I_{OUT} \leq 3A$		0.2	0.4	%
		A1085-1.8V $V_{IN}-V_{OUT}=3V$, $0 \leq I_{OUT} \leq 3A$		3	15	mV
		A1085-2.5V $V_{IN}-V_{OUT}=3V$, $0 \leq I_{OUT} \leq 3A$		3	15	mV
		A1085-3.3V $V_{IN}-V_{OUT}=3V$, $0 \leq I_{OUT} \leq 3A$		3	15	mV
		A1085-5.0V $V_{IN}-V_{OUT}=3V$, $0 \leq I_{OUT} \leq 3A$		3	15	mV
$V_{IN}-V_{OUT}$	Dropout Voltage (Note3)	ΔV_{OUT} , $\Delta V_{REF}=1\%$, $I_{OUT}=3A$		1.3	1.5	V
I_{LIMIT}	Current Limit	$V_{IN}-V_{OUT}=3V$, $T_J=25^\circ C$	3.2	4.5		A



Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
	Minimum Load Current (Note 4)	A1085-ADJ		3	10	mA
I _q	Quiescent Current	V _{IN} =10V		4	10	mA
I _{ADJ}	Adjust Pin Current	V _{IN} =4.25V, I _{OUT} =10mA		45	110	uA
	Ripple Rejection	F=120Hz, C _{OUT} =25uF (Tan) I _{OUT} =3A, V _{IN} -V _{OUT} =3V	60			dB
I _{CHANGE}	Adjust Pin Current Change	10mA ≤ I _{OUT} ≤ 3A, 1.5V ≤ V _{IN} ≤ 6V		0.2	5	uA
	Temperature Stability	I _{OUT} =10mA, V _{IN} -V _{OUT} =1.5V			0.5	%
θ _{JC}	Thermal Resistance Junction to Case	TO-252 TO-263-2 and TO-263-3		12.5 3		°C/W

Note 1: The parameters of Line Regulation and Load Regulation are tested under constant junction temperature.

Note 2: When I_{OUT} varies between 0~3A, V_{IN}-V_{OUT} varies between 1.5V~6V under constant junction temperature, the parameter is satisfied the criteria in above mentioned. If temperature varies between -50°C ≤ T_A ≤ 140°C, needs output current to be larger than 10mA to satisfy the criteria.

Note 3: Dropout Voltage is specified over I_{OUT}=3A and the following testing conditions:

First step is to find out the V_{OUT} Value (V_{OUT1}), when V_{IN1} = V_{OUT} + 1.5V,

Second step is to decrease V_{IN} (V_{IN2}) until V_{OUT} value is equal to 99% x V_{OUT1} (V_{OUT2}). V_{DROPOUT} = V_{IN2} - V_{OUT2}.

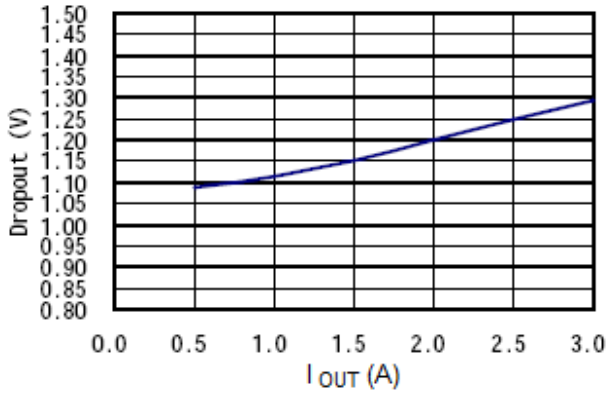
Note 4: Minimum Load Current is defined as the minimum output current required to maintain regulation.

When 1.5V ≤ V_{IN}-V_{OUT} ≤ 6V, the device is guaranteed to regulate if the output current is greater than 10mA.

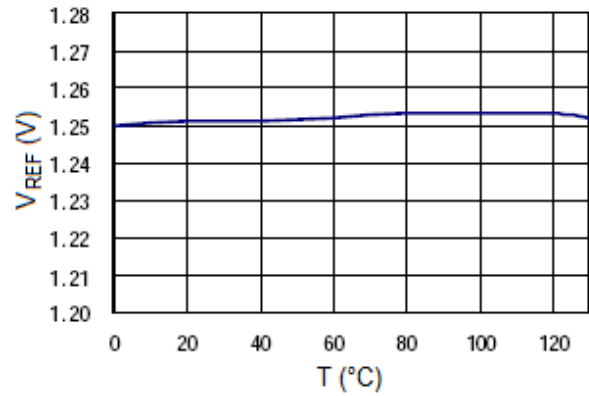


TYPICAL PERFORMANCE CHARACTERISTICS

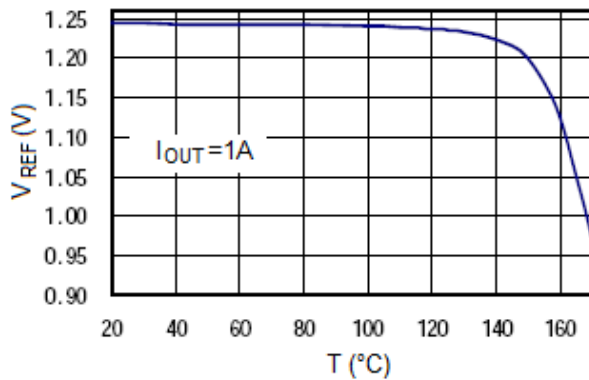
1. Dropout Voltage vs. Output Current



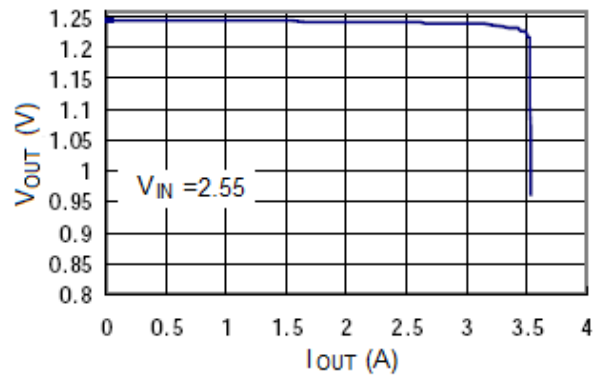
2. Reference Voltage vs. Temperature



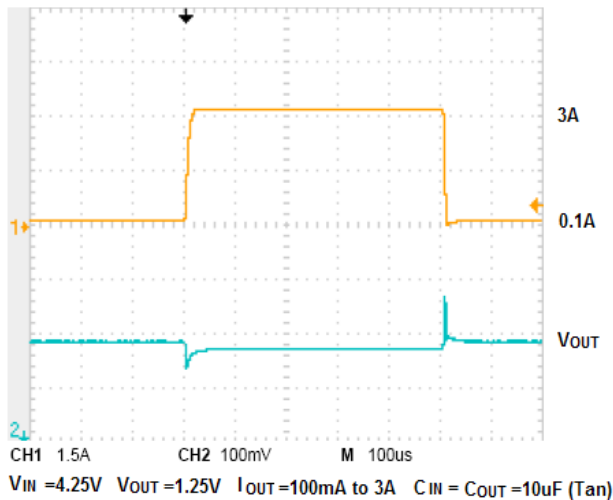
3. Reference Voltage vs. Thermal Protection



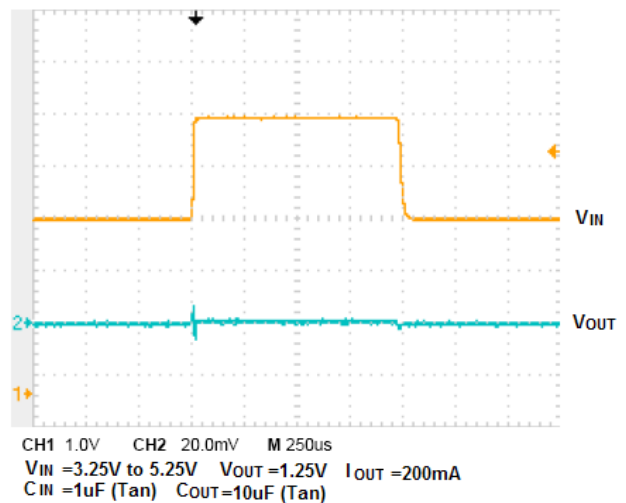
4. Output Voltage vs. Output Current



5. Load Transient Response

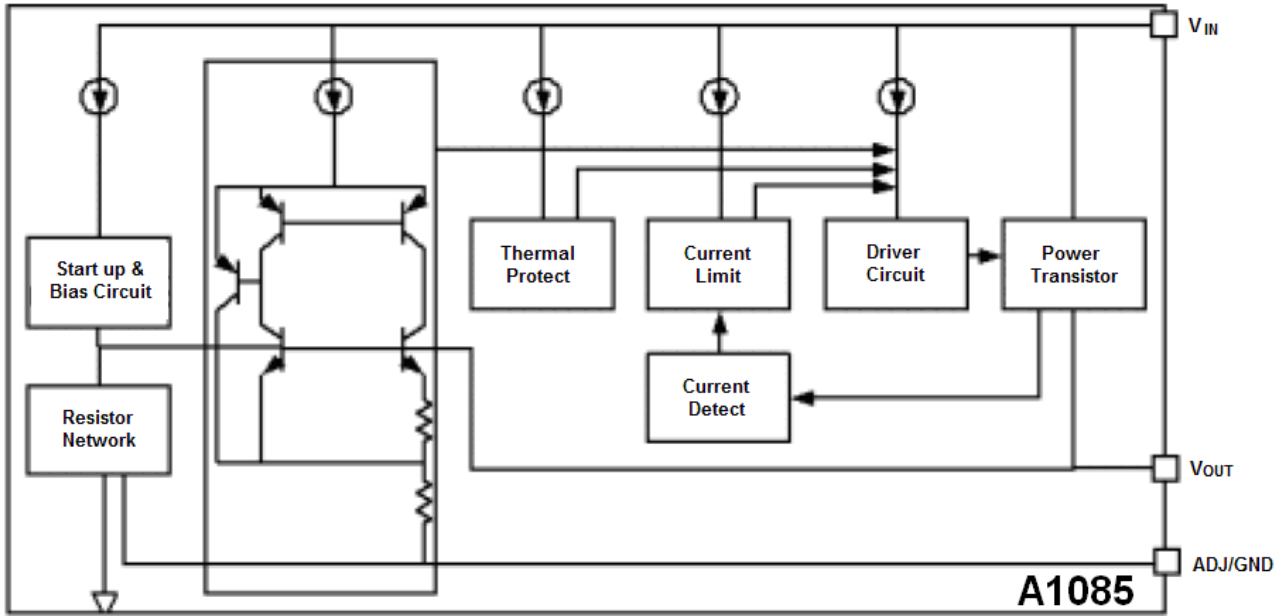


6. Line Transient Response





BLOCK DIAGRAM





DETAILED INFORMATION

The A1085 is a series of low dropout voltage, three terminal regulators which application circuit of fixed version only needs two capacitors and the adjustable version only needs two resistors and two capacitors to work. It is composed of some modules including start-up circuit, bias circuit, band-gap, thermal shutdown, current limit, power transistors and its driver circuit and so on.

Thermal Shutdown and Current Limit

The A1085 thermal shutdown and current limit modules can assure chip and its application system working safety when the environment temperature is larger than 140°C or output current is larger than 3.2A.

Stable Reference Output Voltage

The band-gap module provides stable reference voltage, whose temperature coefficient is compensated by careful design considerations. The temperature coefficient is under 100ppm/°C. And the accuracy of output voltage is guaranteed by trimming technique.

Application Hints

See Schematic 1

1. Recommend 10uF Tan capacitor (C_{IN}) as bypass capacitor for all application circuit.
2. Recommend 22uF Tan capacitor (C_{OUT}) to assure circuit stability.
3. Using a bypass capacitor (C_{ADJ}) between the adjust terminal and ground can improve ripple rejection, this bypass capacitor prevents ripple from being amplified as the output voltage is increase. The impedance of C_{ADJ} should be less than the resistor's ($R1$) which is normally in the range of 120Ω ~ 200Ω, the value of C_{ADJ} should satisfy this equation: $2 \times F_{RIPPLE} \times < R1$. Recommend using 10uF Tan capacitor.

Output Voltage of Adjustable Version

The A1085-ADJ adjustable version provides 1.25V reference voltage. Any output voltage between 1.25V ~ 13.8V can be available by choosing two external resistors (connection method is shown in Schematic 1), $R1$ and $R2$ are the two external resistors.



Example

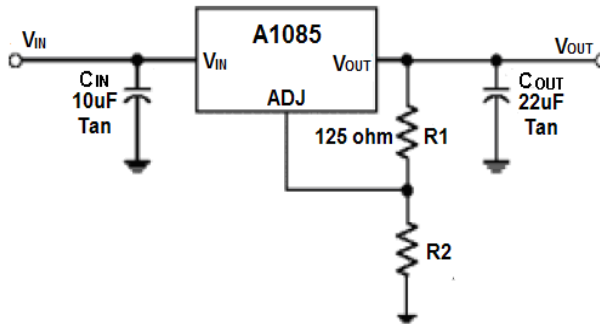
The output voltage of A1085-ADJ adjustable version satisfies this followed equation:

$$V_{OUT} = V_{REF} \times (1 + R_2/R_1) + I_{ADJ} \times R_2$$

We can ignore I_{ADJ} because I_{ADJ} (about 50uA) is much less than the current of R_1 (about 4mA).

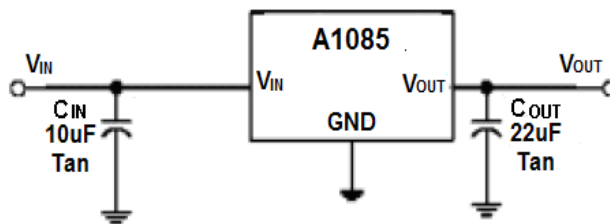
The value of R_1 should be in the range $120\Omega \sim 200\Omega$ to assure chip working normally without any load. To assure the electrical performance showed in Table 1, the output than 4mA, the best working condition is to assure that the output current exceeds 10mA.

(Schematic 1)



Adjustable Version

$$V_{OUT} = V_{REF} \times (1 + R_2/R_1) + I_{ADJ} \times R_2$$

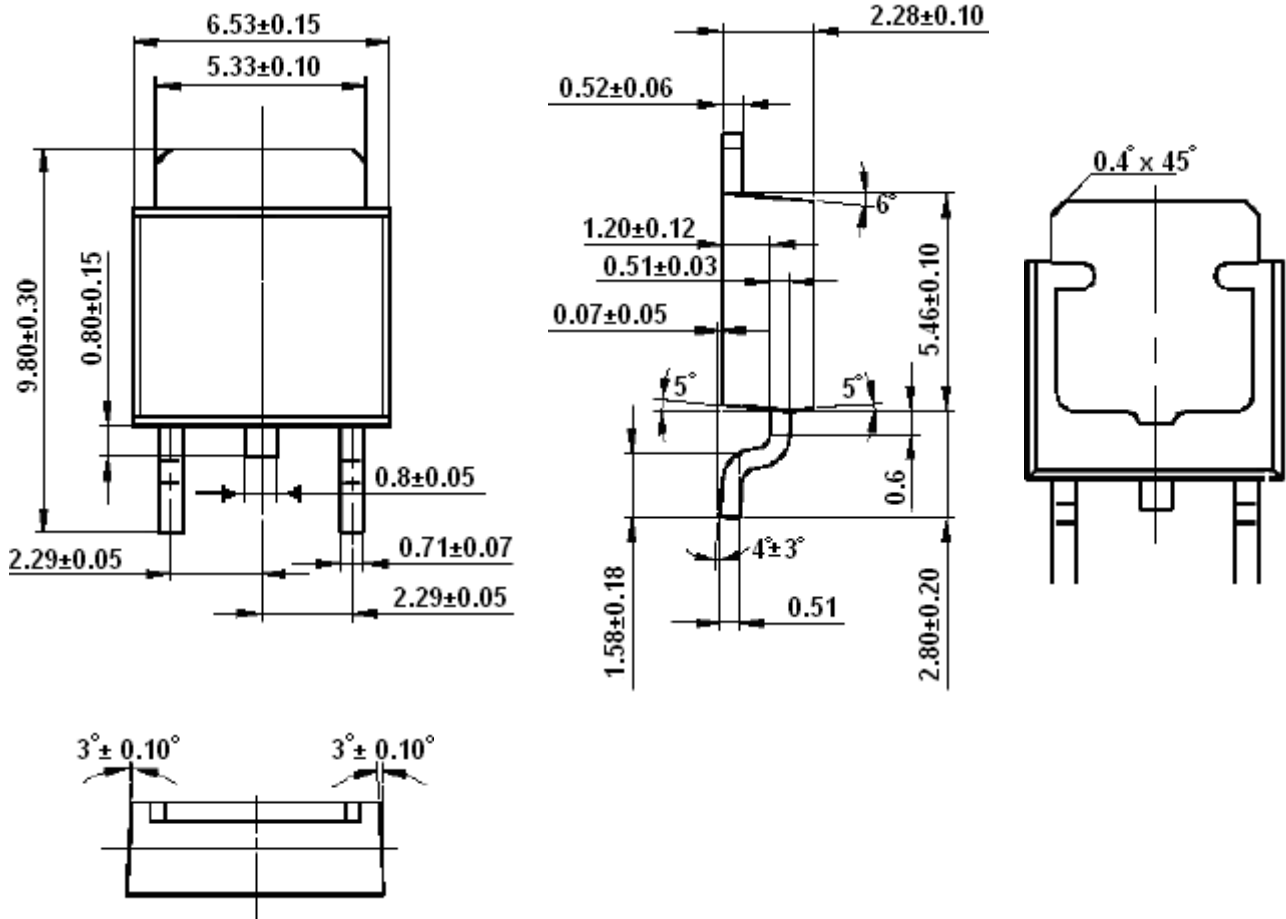


Fixed Output Version

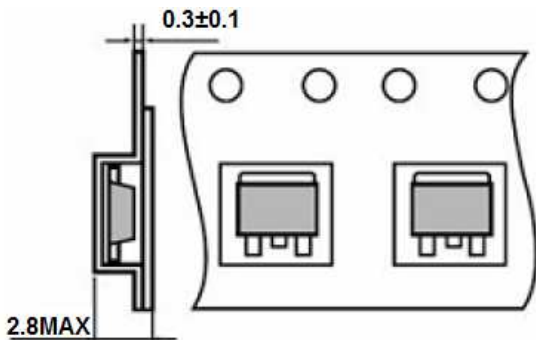


PACKAGE INFORMATION

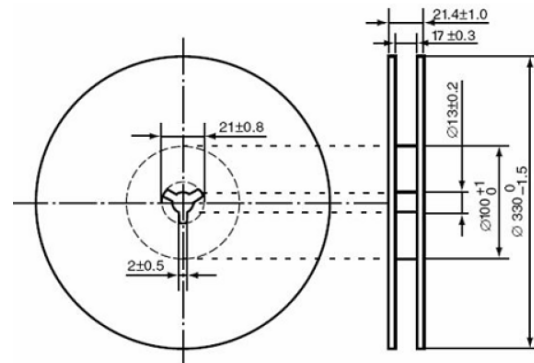
Dimension in TO-252 (Unit: mm)



Tape Dimension

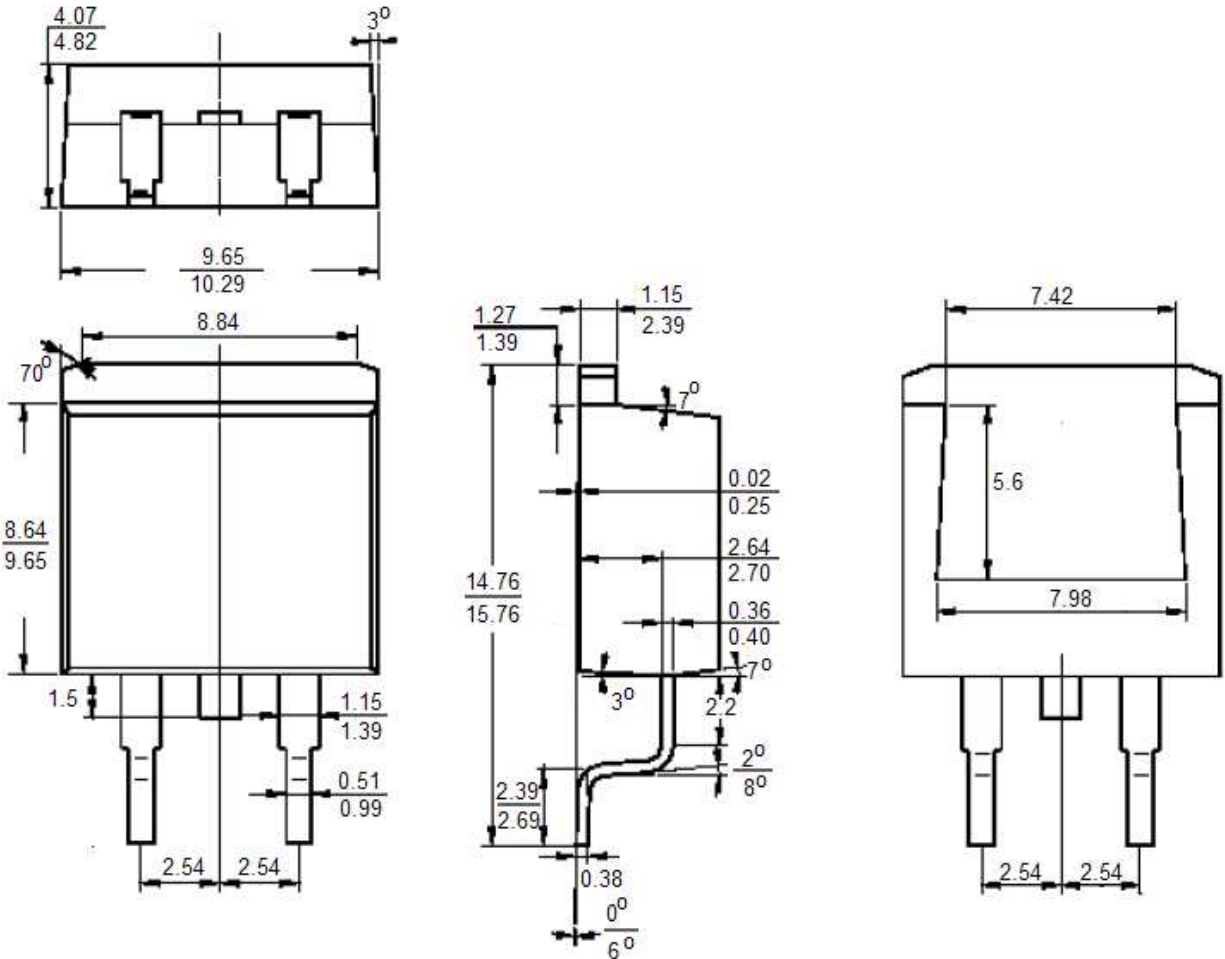


Reel Dimension



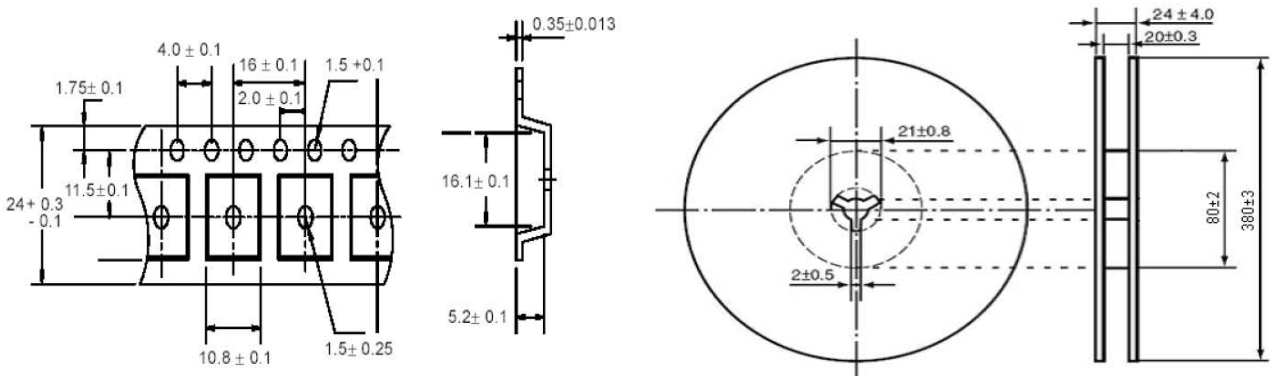


Dimension in TO-263-2 (Unit: mm)



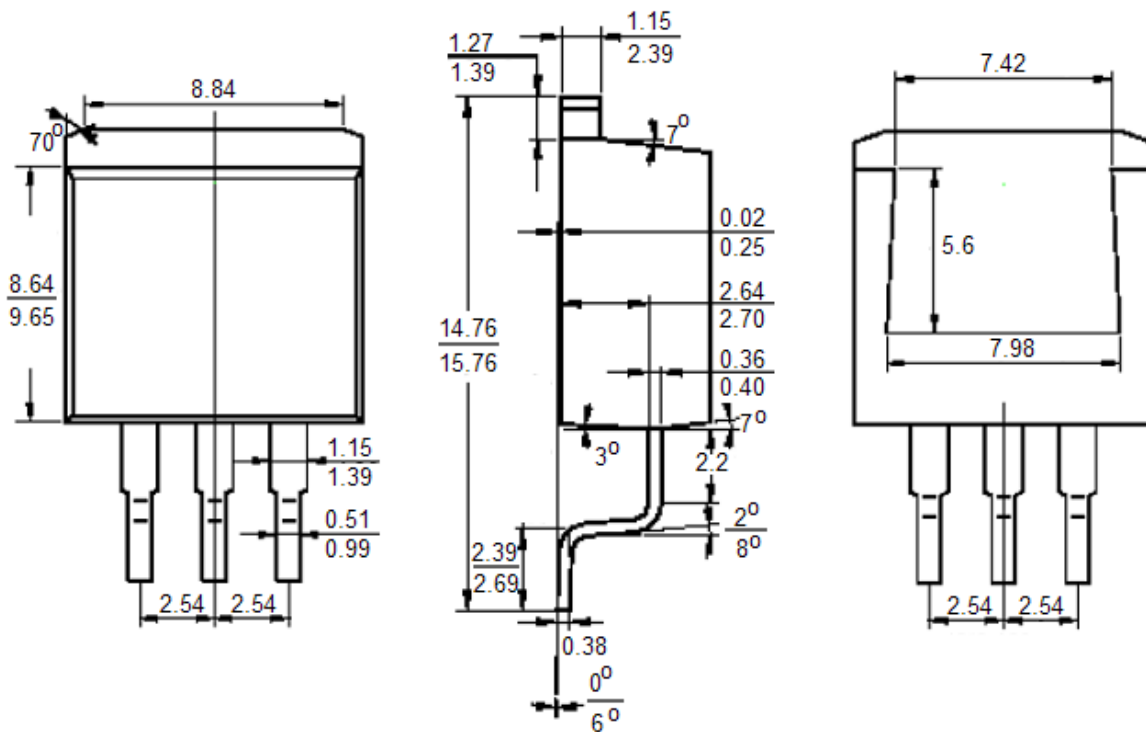
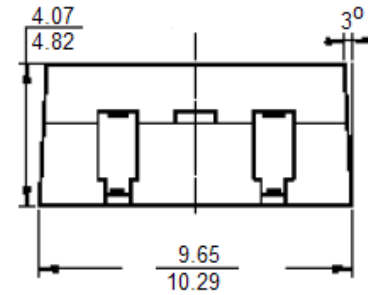
Tape Dimension

Reel Dimension

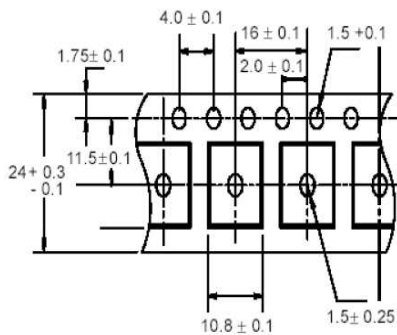




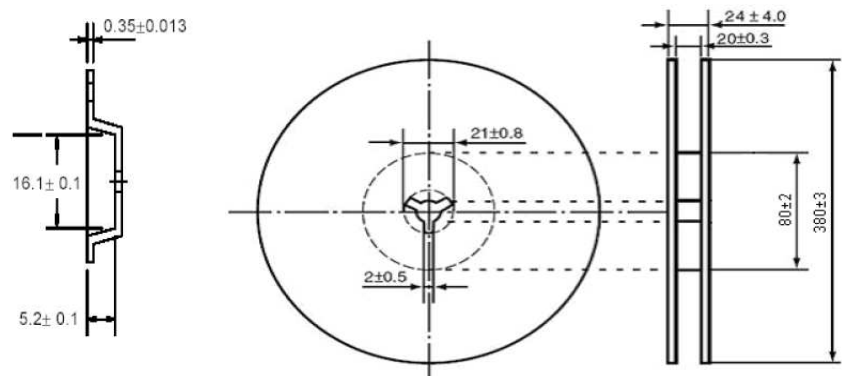
Dimension in TO-263-3 (Unit: mm)



Tape Dimension



Reel Dimension





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