

SILICON POWER TRANSISTORS 2SA1615, 1615-Z

PNP SILICON EPITAXIAL TRANSISTOR FOR HIGH-SPEED SWITCHING

The 2SA1615 and 1615-Z are available for the large current control in small dimension due to the low saturation and are ideal for high-efficiency DC/DC converters due to the fast switching speed.

FEATURES

Large current capacity:

IC(DC): -10 A, IC(pulse): -15 A

• High hee and low collector saturation voltage:

hfe = 200 MIN. (@Vce = -2.0 V, Ic = -0.5 A)

 $V_{CE(sat)} \le -0.25 \text{ V } (@Ic = -4.0 \text{ A}, IB = -0.05 \text{ A})$

QUALITY GRADES

Standard

Please refer to "Quality Grades on NEC Semiconductor Devices" (Document No. C11531E) published by NEC Corporation to know the specification of quality grade on the devices and its recommended applications.

ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

| Parameter | Symbol | Ratings | Unit |
|------------------------------|------------------------------------------|-------------|------|
| Collector to base voltage | Vсво | -30 | V |
| Collector to emitter voltage | VCEO | -20 | V |
| Emitter to base voltage | V _{EBO} | -10 | V |
| Collector current (DC) | Ic(DC) | -10 | Α |
| Collector current (pulse) | IC(pulse)* | -15 | Α |
| Base current (DC) | I _{B(DC)} | -0.5 | Α |
| Total power dissipation | P _T (T _a = 25°C)** | 1.0 | W |
| Total power dissipation | P⊤ (T _c = 25°C) | 15 | W |
| Junction temperature | Tj | 150 | °C |
| Storage temperature | T _{stg} | -55 to +150 | °C |

^{*} PW \leq 10 ms, duty cycle \leq 50%

The information in this document is subject to change without notice. Before using this document, please confirm that this is the latest version. Not all devices/types available in every country. Please check with local NEC representative for availability and additional information.

^{**} Printing board mounted



ELECTRICAL CHARACTERISTICS (Ta = 25°C)

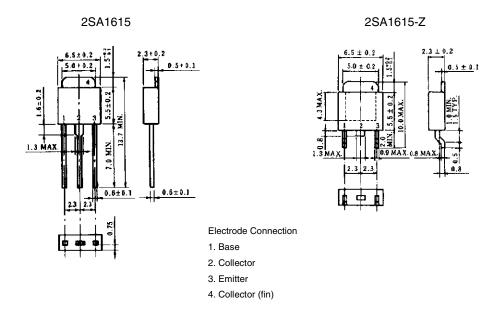
| Parameter | Symbol | Conditions | MIN. | TYP. | MAX. | Unit |
|------------------------------|------------------------|-------------------------------------------------------------|------|------|-------|------|
| Collector cutoff current | Ісво | $V_{CB} = -20 \text{ V}, I_E = 0$ | | | -1.0 | μΑ |
| Emitter cutoff current | Ієво | $V_{EB} = -8.0 \text{ V}, I_{C} = 0$ | | | -1.0 | μΑ |
| DC current gain | h _{FE1} * | $V_{CE} = -2.0 \text{ V}, I_{C} = -0.5 \text{ A}$ | 200 | | 600 | |
| DC current gain | h _{FE2} * | $V_{CE} = -2.0 \text{ V}, I_{C} = -4.0 \text{ A}$ | 160 | | | |
| Collector saturation voltage | V _{CE(sat)} * | $I_{C} = -4.0 \text{ A}, I_{B} = -0.05 \text{ A}$ | | -0.2 | -0.25 | V |
| Base saturation voltage | V _{BE(sat)} * | $I_{C} = -4.0 \text{ A}, I_{B} = -0.05 \text{ A}$ | | -0.9 | -1.2 | V |
| Gain bandwidth product | f⊤ | $V_{CE} = -5.0 \text{ V}, I_E = 1.5 \text{ A}$ | | 180 | | MHz |
| Output capacity | Cob | $V_{CB} = -10 \text{ V}, I_E = 0, f = 1.0 \text{ MHz}$ | | 220 | | pF |
| Turn-on time | ton | $I_C = -5.0 \text{ A}, I_{B1} = -I_{B2} = 0.125 \text{ A},$ | | 80 | | ns |
| Storage time | tstg | $R_L = 2.0 \Omega$, $V cc \cong -10 V$ | | 300 | | ns |
| Fall time | tf | | | 60 | | ns |

^{*} Pulse test PW \leq 350 μ s, duty cycle \leq 2%

hfe CLASSIFICATION

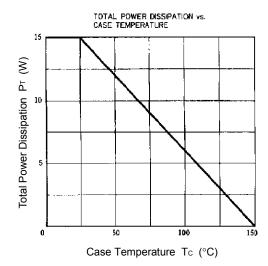
| Marking | L | K | | |
|------------------|------------|------------|--|--|
| h _{FE2} | 200 to 400 | 300 to 600 | | |

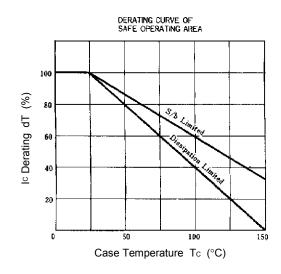
PACKAGE DRAWING (UNIT: mm)

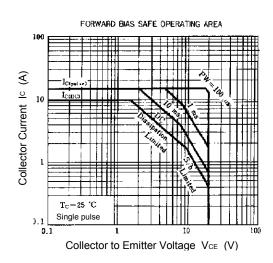


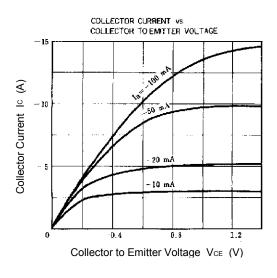
NEC

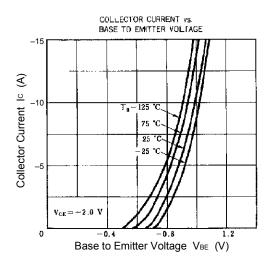
TYPICAL CHARACTERISTICS (Ta = 25 °C)

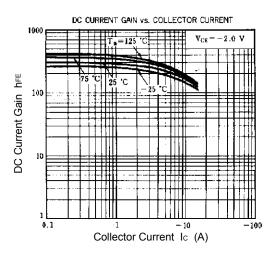


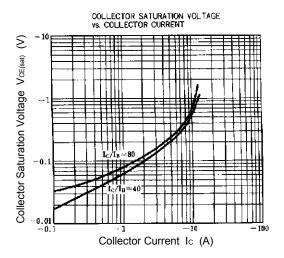


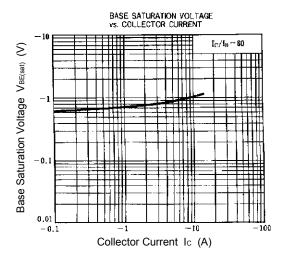




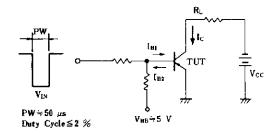


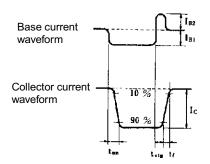






SWITCHING TIME (ton, tstg, tf) TEST CIRCUIT







[MEMO]

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