AiT Semiconductor Inc.

### DESCRIPTION

The A2430 is a digital audio power amplifier IC with maximum output of 2.1W ( $R_L = 4\Omega$ ) x 2ch, which directly drives speakers while reducing distortion of pulse output signal and reducing noise on the signal, and realizes the highest standard low distortion rate characteristics and low noise characteristics.

The A2430 detects output signal clip due to the over level input signal and suppress the output signal clip automatically. Also the non-clip output control function can adapt the output clip caused by power supply voltage down with battery. Attack time and release time can be freely set by external resistances or capacitances.

The independent power-down function for L channel and R channel minimizes consumption current at standby. As for protection function, short-current protection function for speaker output terminal, over-temperatue protection function for inside of the device, and low supply voltage malfunction preventing function are prepared.

The A2430 is available in QFN20(4x4) package.

#### **ORDERING INFORMATION**

Package Type	Part Number		
QFN20	020	A2430Q20R	
(4x4)	Q20	A2430Q20VR	
Note	R: Tape & Reel		
Note	V: Green Package		
AiT provides all Pb free products			
Suffix " V " means Green Package			

## FEATURES

- Maximum output 2.1W x 2ch  $(V_{DDP} = V_{DDA} = 5.0V, R_L = 4\Omega, THD+N = 1\%)$ 0.75W x 2ch  $(V_{DDP} = V_{DDA} = 3.6V, R_L = 8\Omega, THD+N = 10\%)$
- Distortion Rate (THD+N)
  0.03 %
  (V<sub>DDP</sub> = V<sub>DDA</sub> = 3.6V, R<sub>L</sub> = 8Ω, P<sub>O</sub> = 0.4W, 1kHz)
- Efficiency
  84 % (V<sub>DDP</sub> = V<sub>DDA</sub> = 3.6V, R<sub>L</sub> = 8Ω, P<sub>O</sub> ==
  600mW)
- Channel separation
  95dB
  (V<sub>DDP</sub> = V<sub>DDA</sub> = 3.6V, R<sub>L</sub> = 8Ω, A<sub>V</sub> = 18dB, 1kHz)
- Non-clip output
- 2ch independent power-down control function
- Thermal Protection function
- Available in QFN20(4x4) package.

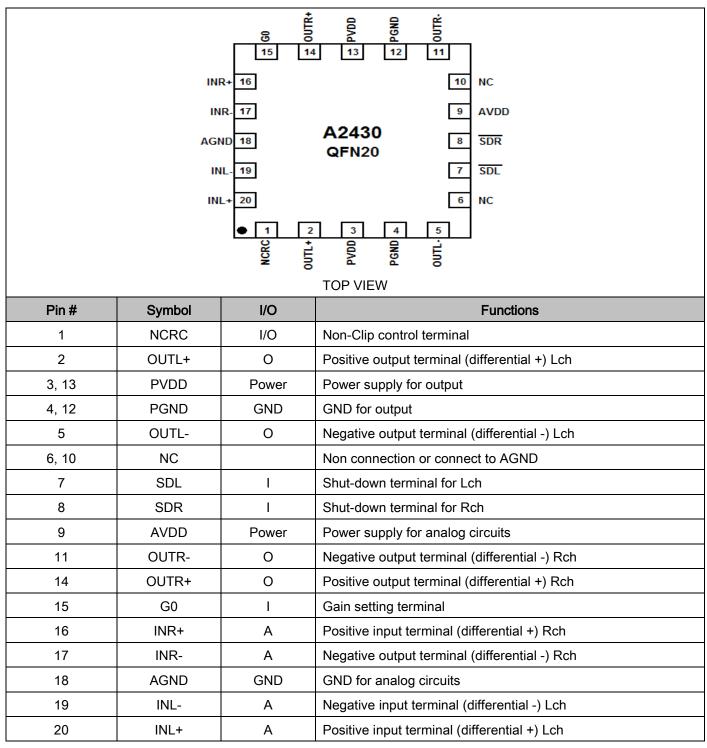
### APPLICATION

- Multimedia monitors
- Portable and desktop computers
- Portable televisions





# PIN DESCIPTION



NOTE: I: Input terminal; O: Output terminal; A: Analog terminal.



# ABSOLUTE MAXIMUM RATINGS

V <sub>DDP</sub> , PVDD Voltage Range	-0.3V ~ 6.0V
V <sub>DDA</sub> , AVDD Voltage Range	-0.3V ~ 6.0V
V <sub>IN</sub> , Analog Input terminal Voltage Range	-0.3V ~ V <sub>DDA</sub> + 0.3V
T <sub>JMAX</sub> , Junction Temperature	125°C
T <sub>STG</sub> , Storage Temperature	-50°C ~ 125°C

Stress beyond above listed "Absolute Maximum Ratings" may lead permanent damage to the device. These are stress ratings only and operations of the device at these or any other conditions beyond those indicated in the operational sections of the specifications are not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



# ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
AVDD consumption current	I <sub>DD</sub>	$V_{DDA}$ = 3.6, no load	-	6.0	-	mA
PVDD consumption current	IDD	V <sub>DDA</sub> = 3.6, no load	-	2.0	-	mA
		No signal input				
Power-down current	IPD	SDL = SDR = 0	-	-	1	uA
SDL, SDR ,G0 H level input voltage	Vін		1.35	-	-	V
SDL, SDR ,G0 L level input voltage	VIL		-	-	0.35	V

**DC Characteristics** (V<sub>DDP</sub> = V<sub>DDA</sub> = 2.7V to 5.5V, T<sub>A</sub> = -40°C to 85°C, unless otherwise specified)

AC Characteristics (V<sub>DDP</sub> = V<sub>DDA</sub> = 2.7V to 5.5V, T<sub>A</sub> = -40°C to 85°C, unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Start-up time	TSTUP		-	3.5	-	ms
Input cut-off frequency	fc	C <sub>IN</sub> = 0.1uF, A <sub>V</sub> = 18dB	-	57	-	Hz
	Тат	V <sub>DDA</sub> = 3.6 A <sub>V</sub> = 10dB	-	10	-	ms
Attack time		Cex = 1uF, Rex = $1M\Omega$				
Release time T <sub>RL</sub>	Τ	V <sub>DDA</sub> = 3.6 A <sub>V</sub> = 10dB		0.0		
	I RL	Cex = 1uF, Rex = $1M\Omega$	-	0.8	-	S
Carrier clock frequency	f <sub>PWM</sub>		-	500	-	kHz

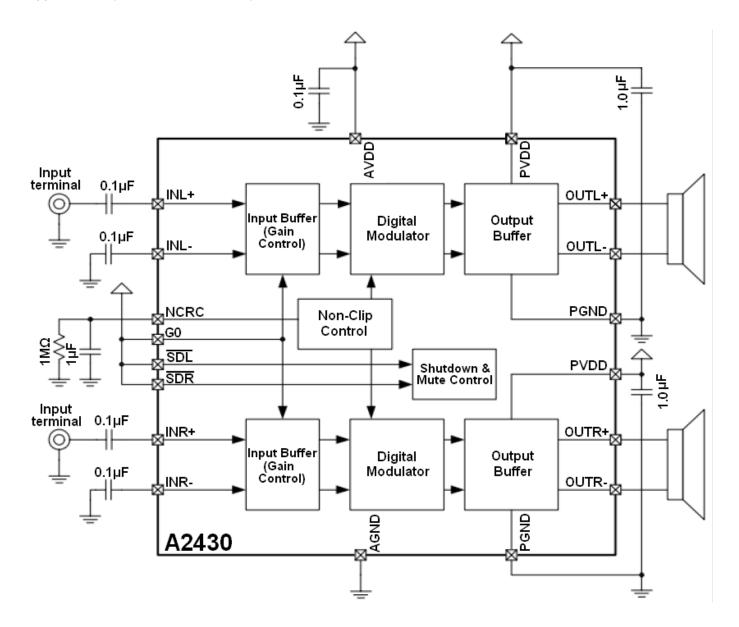
#### Analog Characteristics ( $V_{DDP} = V_{DDA} = 3.6V$ , $T_A = 25^{\circ}C$ , $R_L = 8\Omega$ , Non-clip off ,unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
		R∟ = 4Ω, f = 1kHz				
Maximum output	Po	THD + N = 10%	-	2.1	-	W
		$V_{DDA} = V_{DDP} = 5$				
Voltage Gain	Av	G0 = L	-	12	-	dB
		G0 = H	-	18	-	
		$V_{DDA}$ = 3.6, $A_V$ = 10dB		0.03	-	%
THD + Noise		Cex =1uF, Rex =1MΩ	-			
PSRR		217Hz to PVDD	-	-85	-	dB
Non-Clip maximum attenuation gain	Aa		-	-10	-	dB



### TYPICAL APPLICATION

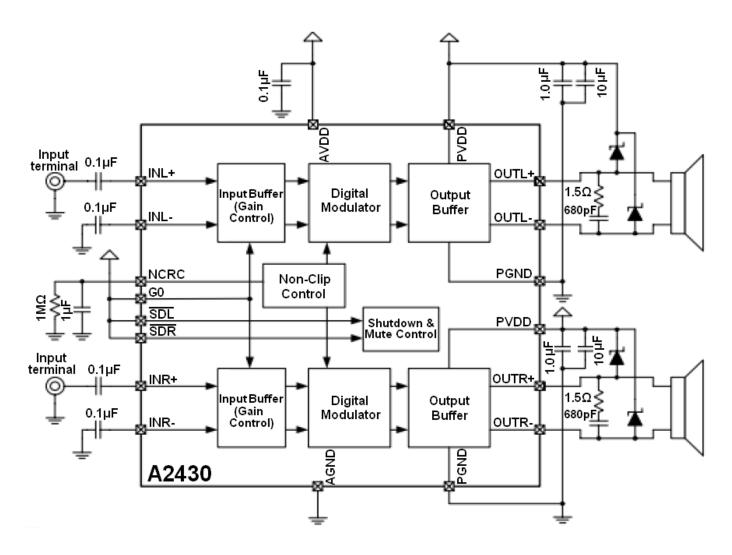
Application 1. (2.7V  $\leq$  PVDD  $\leq$  4.5V)





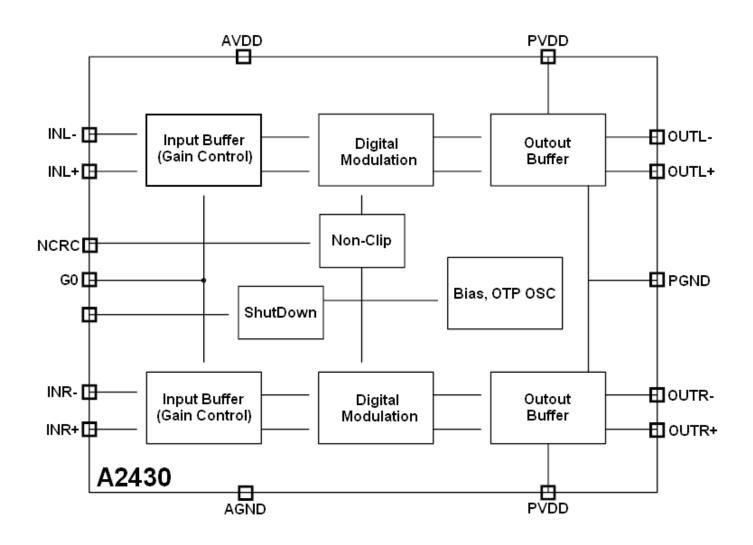


#### Application 2. (4.5V ≦ PVDD)





# **BLOCK DIAGRAM**





### DEATILED INFORMATION

#### First Stage Amplifier Gain Setting Function

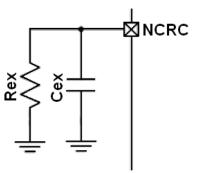
G0 terminal can set the Gain of A2430. When Non-Clip function is disabled, the relation between G0 terminal setting and Gain is as follows.

G0	Gain	Input Impedance(Zin)
L	12dB	44kΩ
Н	18dB	28kΩ

#### Non-Clip control Function

This is the function to control the output in order to obtain a maximum output level without distortion when an excessive input which causes clipping at the differential signal output is applied. A2430 follows also to the clip of the output wave form due to the decrease in the power-supply voltage.

Connecting a resistor (Rex) and a capacitor (Cex) to NCRC terminal can set Attack Time and Release Time of the Non-Clip control.

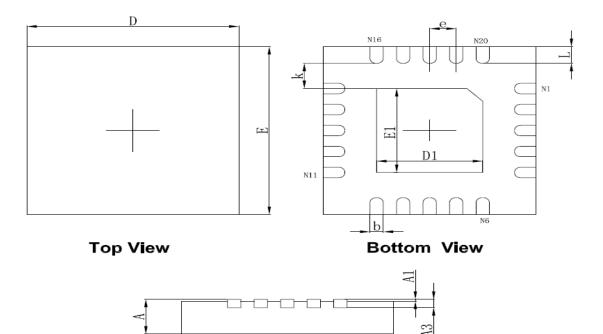


Rex (MΩ)	1	4.7	1	1
Cex (uF)	1	1	0.47	4.7
Attack Time (ms)	10	10	4.7	47
Release Time (s)	0.8	3.8	0.38	3.8



# PACKAGE INFORMATION

Dimension in QFN20 (Unit: mm)



Side View

Symbol	Min	Max			
A	0.700/0.800	0.800/0.900			
A1	0.000	0.050			
A3	0.203REF				
D	3.900	4.100			
E	3.900	4.100			
D1	1.900	2.100			
E1	1.900	2.100			
k	0.200MIN				
b	0.180 0.300				
е	0.500TYP				
L	0.300 0.500				



## IMPORTANT NOTICE

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