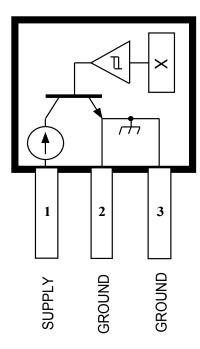
Suffix 'UA' Pinning (ultra-mini SIP)



Dwg. PH-003-7A

Pinning is shown viewed from branded side.

ABSOLUTE MAXIMUM RATINGS at $T_A = +25^{\circ}C$

 T_A -40°C to +85°C Storage Temperature Range,

 T_{s} -65°C to +170°C

This Hall-effect switch is a monolithic integrated circuit designed to operate continuously over extended temperatures to +85°C. The unipolar switching characteristic makes this device ideal for use with a simple bar or rod magnet. The A3163ELT and A3163EUA are identical except for package.

Each device includes a voltage regulator for operation with supply voltages of 3.5 to 24 volts, reverse-battery protection, quadratic Hall-voltage generator for low offset, temperature compensation circuitry, small-signal amplifier, Schmitt trigger, and a constant-current open-collector output, wired internally for true 2-wire operation. Noise radiation is limited by control of the output-current slew rate.

Two package styles provide a magnetically optimized package for most applications. Suffix 'LT' is a miniature SOT89/TO-243AA transistor package for surface-mount applications; suffix 'UA' is a three-lead ultra-mini SIP for through-hole mounting; it is also available with lead forming for surface-mount applications (suffix 'UA-TL').

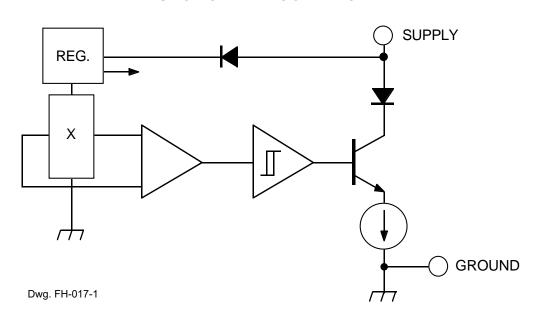
FEATURES and BENEFITS

- Internal Current Regulator for 2-Wire Operation
- Output Slew Rate Controlled
- 3.5 V to 24 V Operation ... Needs Only An Unregulated Supply
- Reverse Battery Protection
- Excellent Temperature Stability
- Activate with Small, Commercially Available Permanent Magnets
- Small Size
- Solid-State Reliability ... No Moving Parts
- Resistant to Physical Stress

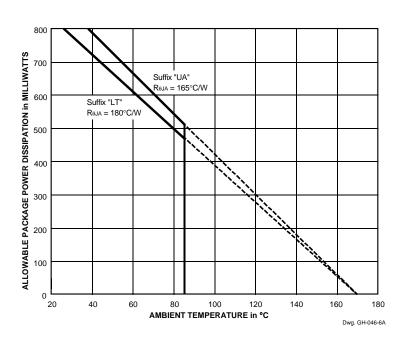
Always order by complete part number, e.g., A3163ELT.

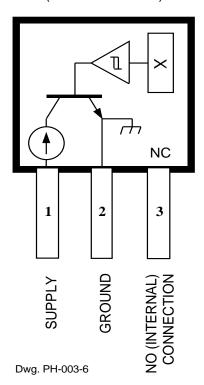


FUNCTIONAL BLOCK DIAGRAM



Suffix 'LT' Pinning (SOT89/TO-243AA)





Pinning is shown viewed from branded side.



ELECTRICAL CHARACTERISTICS over operating voltage and temperature ranges.

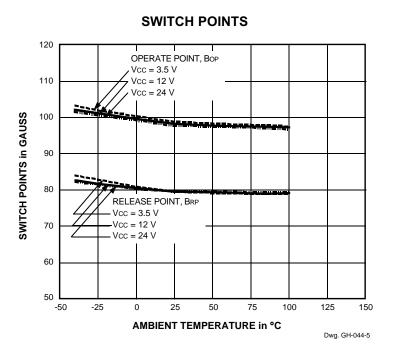
			Limits			
Characteristic	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Supply Voltage	V _{CC}	Operating	3.5	_	24	V
Output Current	I _{OUT(H)}	B > B _{OP}	12	14	17	mA
	I _{OUT(L)}	B < B _{RP}	5.0	5.6	6.9	mA
Output Slew Rate	di/dt	C _L = 20 pF	_	7.0	20	mA/μs
Output Settling Time	t _{sd}	C _L = 20 pF		_	20	μs
Reverse Battery Current	I _{CCR}	V _{RCC} = -16 V	_	_	-15	mA

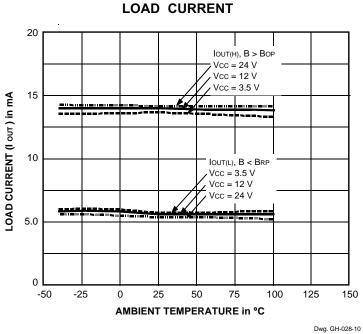
MAGNETIC CHARACTERISTICS over operating supply voltage and temperature ranges.

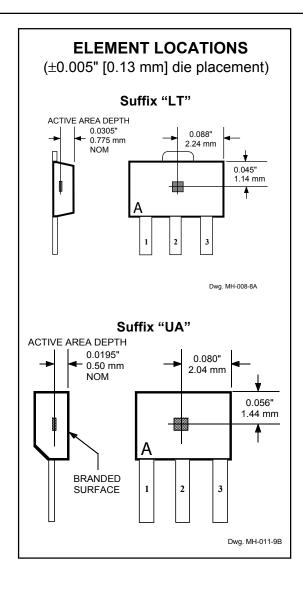
		Limits			
Characteristic	Symbol	Min.	Тур.	Max.	Units
Operate Point (output turns on)	B _{OP}	_	98	160	G
Release Point (output turns off)	B _{RP}	30	79	_	G
Hysteresis (B _{OP} – B _{RP})	B _{hys}	5.0	19	40	G

NOTES: 1. Typical Data is at $T_A = +25^{\circ}C$ and $V_{CC} = 12 \text{ V}$ and is for design information only. 2. 1 gauss (G) is exactly equal to 0.1 millitesla (mT).

TYPICAL OPERATING CHARACTERISTICS



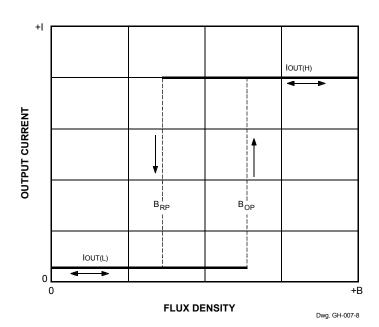




FUNCTIONAL DESCRIPTION

Operation. The output of these devices turns on when a magnetic field (south pole) perpendicular to the Hall element exceeds the operate point threshold (B_{OP}). After turn on, the output will source current ($I_{OUT(H)}$) equal to the device operating current plus a current source. When the magnetic field is decreased (south pole) below the release point (B_{RP}), the output will source current ($I_{OUT(L)}$) equal to the Hall-effect device operating current with the current source turned off. The difference in the magnetic operate and release points is the hysteresis (B_{hys}) of the device. The hysteresis allows clean switching of the output even in the presence of external mechanical vibration or electrical noise.

Powering up in the absence of a magnetic field (less than B_{OP} and higher than B_{RP}) will allow an indeterminate output state. The correct state is warranted after the first excursion beyond B_{OP} or B_{RP} .



APPLICATIONS INFORMATION

External Components. It is strongly recommended that an external bypass capacitor be connected (in close proximity to the Hall element) between the supply and ground of the device.

Power Derating. Due to the internal device power dissipation, the junction temperature (T₁) will be higher than the ambient temperature (T_A) . To ensure that the absolute maximum junction temperature is not exceeded, the following equations should be applied:

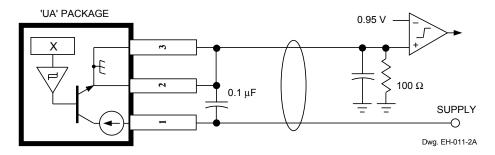
$$T_{J} = T_{A} + (P_{D} \times R_{\theta JA})$$

 $T_{_J} = T_{_A} + (P_{_D} \ x \ R_{_{\theta JA}})$ where $P_{_D}$ is the maximum supply power

$$I_{OUT(H)} \times V_{CC}$$

 $I_{_{OUT(H)}}\,x\,\,V_{_{CC}}$ and $R_{_{\theta JA}}$ is the package thermal resistance. The specified limit for I_{OUT(H)} should be used to ensure a margin of safety.

Magnets. The simplest form of magnet that will operate this device is a ring magnet. Other methods of operation, such as linear magnets, are possible.



TYPICAL APPLICATION

Extensive applications information for Hall-effect devices is available in:

- Hall-Effect IC Applications Guide, Application Note 27701;
- Hall-Effect Devices: Soldering, Gluing, Potting, Encapsulating, and Lead Forming, Application Note 27703.1;
- Soldering of Through-Hole Hall-Sensor Dervices, Application Note 27703;
- Soldering of Surface-Mount Hall-Sensor Devices, Application Note 27703.2; and
- Two-Wire Hall-Effect Sensors, Application Note 27704.

All are provided in Allegro Electronic Data Book, AMS-702 or at



CRITERIA FOR DEVICE QUALIFICATION

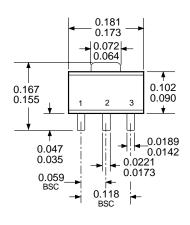
All Allegro devices are subjected to stringent qualification requirements prior to being released to production. To become qualified, except for the destructive ESD tests, no failures are permitted.

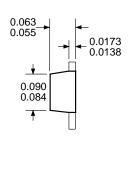
Qualification Test	Test Method and Test Conditions	Test Length	Samples	Comments
Biased Humidity (HAST)	T _A = 130°C, RH = 85%	50 hrs	77	$V_{CC} = V_{OUT} = 5 V$
High-Temperature Operating Life (HTOL)	JESD22-A108, T _A = 150°C, T _J ≤ 165°C	408 hrs	77	V _{CC} = 24 V, V _{OUT} = 20 V
Accelerated HTOL	T _A = 175°C, T _J = 190°C	504 hrs	77	V _{CC} = 24 V, V _{OUT} = 20 V
Autoclave, Unbiased	JESD22-A102, Condition C, $T_A = 121^{\circ}C$, 15 psig	96 hrs	77	
High-Temperature (Bake) Storage Life	MIL-STD-883, Method 1008, T _A = 170°C	1000 hrs	77	
Temperature Cycle	MIL-STD-883, Method 1010, -65°C to +150°C	500 cycles	77	
Latch-Up	_	Pre/Post Reading	6	
Electro-Thermally Induced Gate Leakage	_	Pre/Post Reading	6	
ESD, Human Body Model	CDF-AEC-Q100-002	Pre/Post Reading	3 per test	Test to failure, All leads > 4 kV
ESD, Machine Model	JESD22-A115	Pre/Post Reading	3 per test	Test to failure, All leads >700 V
Electrical Distributions	Per Specification	_	30	

PACKAGE DESIGNATOR 'LT' (SOT89/TO-243AA)

Dimensions in Inches (for reference only)

Dimensions in Millimeters (controlling dimensions)





Dwg. MA-009-3A in

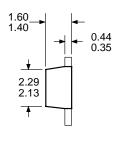
2.60 4.25 3.94 2.29 0.48 1.20 0.36 0.89 0.56 0.44 1.50 BSC 3.00 BSC

4.60

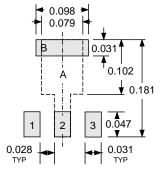
4.40

1.83

1.62



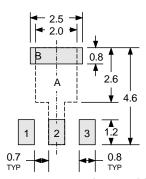
Dwg. MA-009-3A mm



Pads 1, 2, 3, and A — Standard SOT89 Layout Pads 1, 2, 3, and B — Low-Stress Version

Pads 1, 2, and 3 only — Lowest Stress, But Not Self Aligning

Dwg. MA-012-3 in



- Standard SOT89 Layout Pads 1, 2, 3, and A -

Pads 1, 2, 3, and B — Low-Stress Version

Pads 1, 2, and 3 only — Lowest Stress, But Not Self Aligning

Dwg. MA-012-3 mm

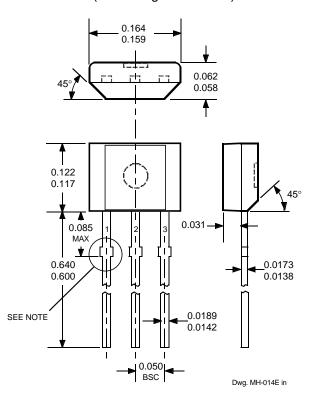
- NOTES: 1. Exact body and lead configuration at vendor's option within limits shown.
 - 2. Supplied in bulk pack (500 pieces per bag) or add "TR" to part number for tape and reel.
 - Only low-temperature (≤240°C) reflow-soldering techniques are recommended for SOT89 devices.



PACKAGE DESIGNATOR 'UA'

Dimensions in Inches

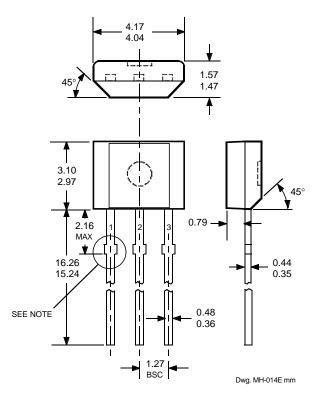
(controlling dimensions)



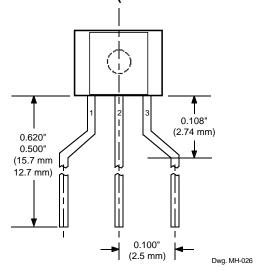
NOTES: 1. Tolerances on package height and width represent allowable mold offsets. Dimensions given are measured at the widest point (parting line).

- 2. Exact body and lead configuration at vendor's option within limits shown.
- 3. Height does not include mold gate flash.
- 4. Recommended minimum PWB hole diameter to clear transition area is 0.035" (0.89 mm).
- 5. Where no tolerance is specified, dimension is nominal.
- 6. Supplied in bulk pack (500 pieces per bag).

Dimensions in Millimeters (for reference only)



Radial Lead Form (order A3163xUA-LC)



NOTE: Lead-form dimensions are the nominals produced on the forming equipment. No dimensional tolerance is implied or guaranteed for bulk packaging (500 pieces per bag).

The products described herein are manufactured under one or more of the following U.S. patents: 5,045,920; 5,264,783; 5,442,283; 5,389,889; 5,581,179; 5,517,112; 5,619,137; 5,621,319; 5,650,719; 5,686,894; 5,694,038; 5,729,130; 5,917,320; and other patents pending.

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