A3976

Dual Full Bridge Protected Motor Driver

Discontinued Product

These parts are no longer in production The device should not be purchased for new design applications. Samples are no longer available.

Date of status change: May 1, 2006

Recommended Substitutions:

For new customers or new applications, refer to the <u>3980</u>.

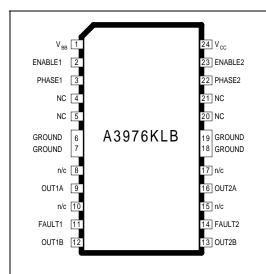
NOTE: For detailed information on purchasing options, contact your local Allegro field applications engineer or sales representative.

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3976

PRELIMINARY DATASHEET - 8/24/04 (Subject to change without notice)



ABSOLUTE MAXIMUM RATINGS at $T_A = +25^{\circ}C$

Operating Supply Voltage, V _{BB}	27 V
Non-Operating Supply Voltage	35 V
Output Current, I _{OUT}	±500 mA*
Logic Supply Voltage, V _{CC}	7.0 V
Fault Output Voltage	7.0 V
Logic Input Voltage Range,	
V _{IN} 0.3 V to V	$c_{\rm CC} + 0.3 \text{ V}$
Package Power Dissipation ($T_A = +25^{\circ}$	°C), P _D
A3976KLB	2.2 W
Operating Temperature Range,	
T _A 40°C	to +125°C
Junction Temperature, T _J	+150°C
Storage Temperature Range,	
T _S 55°C	to +150°C
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* Output current rating may be limited by duty cycle, ambient temperature, and heat sinking. Under any set of conditions, do not exceed the specified current rating or a

junction temperature of 150°C.

DUAL FULL BRIDGE PROTECTED MOTOR DRIVER

The A3976 is designed to drive both windings of a bipolar stepper motor or bidirectionally control two DC Motors. Both H-Bridges are capable of continuous output currents of up to +/- 500 mA and operating voltages to 27V. Free wheeling, substrate isolated diodes are included for output transient suppression when switching motors or other inductive loads. For each bridge the PHASE input controls load current polarity by selecting the appropriate source and sink driver pair. The ENABLE input, when held high, enables the respective output H-bridge. When both ENABLE pins are held low the device will enter SLEEP mode and consume less than 100µA.

The A3976 is protected to ensure safe operation in harsh operating environments and was designed specifically for automotive applications. Protection circuitry will check for open or shorted load, motor lead short to ground or supply, VBB overvoltage, VCC undervoltage, and thermal shutdown. If any of these conditions are detected the outputs will be disabled and fault information will be output to diagnostic pins FAULT1 and FAULT2.

The A3976 is supplied in a 24-lead plastic SOIC with a copper batwing tab (suffix 'LB').

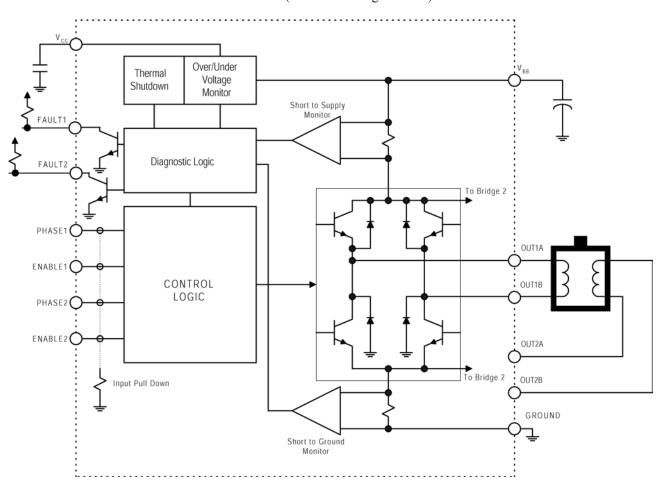
FEATURES

- ±500 mA Continuous Output Rating
- 35V Load Dump Survival
- Output Short Circuit Protection
- Coded Fault Diagnostic Outputs
- Low Current Standby Mode
- Open Load Monitor
- Low Current Standby Mode
- VBB Over Voltage Shutdown
- Internal Thermal Shutdown Circuitry
- Internal Low Parasitic Free Wheeling Diodes
- Crossover Current Protection



Functional Block Diagram

(one of two bridges shown)



Input Logic

PHASE	ENABLE	OUTA	OUTB
Х	0	Off	Off
0	1	Low	High
1	1	High	Low

Fault Logic

Fault Condition	FAULT1	FAULT2
Thermal Shutdown	Low	Low
Short to Battery or Open Load	Low	High
Short to Ground	High	Low
Normal Operation	High	High



ELECTRICAL CHARACTERISTICS at T_A = -40°C to +125°C, V_{BB} = 16V, V_{CC} = 4.5 to 5.5 V (Unless noted otherwise)

	Symbol Test Cor		Limits			
Characteristics		Test Conditions	Min.	Тур.	Max.	Units
Output Drivers						
Load Supply Voltage Range	V_{BB}	Operating	6	-	27	V
Logic Supply Range	V _{CC}	Operating	4.5		5.5	V
Output Current Limit	I _{CL}	Outputs A or B = GROUND	0.7	1.1	_	Α
		Outputs A or B = V _{BB}	0.7	1.1	_	Α
Output Leakage Current	I _{CEX}	V _{OUT} = V _{BB}	_	<1.0	50	μΑ
		V _{OUT} = 0 V	-	<1.0	-50	μΑ
Output Saturation Voltage	$V_{CE(SAT)}$	Source Driver, I _{OUT} = -350 mA	-	1.0	1.2	V
		Source Driver, I _{OUT} = -500 mA	-	1.2	1.3	
		Sink Driver, I _{OUT} = +350 mA	-	0.2	0.4	V
		Sink Driver, I _{OUT} = +500 mA	-	0.4	0.6	V
Clamp Diode Forward Voltage	V _F	I _F = 500 mA (Sink / Ground Clamp)	-	1.2	1.5	V
(Flyback or Ground Clamp)		I _F = 500 mA (Source / Flyback)	-	1.55	2.0	V
Control Logic						
Motor Supply Current	I _{BB}	ENABLE1 = ENABLE2 = High	_	7	10	mA
(No Load)		Sleep Mode ENABLE1 = ENABLE2 = Low	-	_	25	μA
Logic Supply Current	Icc	ENABLE1 = ENABLE2 = High	_	45	50	mA
		Sleep Mode, ENABLE1 = ENABLE2 = Low	_	_	75	μA
Logic Input Voltage	V _{IN(1)}		0.7V _{CC}	_	-	V
	V _{IN(0)}		_	_	.3V _{cc}	V
Logic Input Current	I _{IN(1)}	V _{IN} = 5 V	_	50	100	μA
	I _{IN(0)}	V _{IN} = 0 V	_	-1.0	-2.0	μA



ELECTRICAL CHARACTERISTICS at T_A = -40°C to +125°C, V_{BB} = 16 V, V_{CC} = 4.5 to 5.5 V (Unless noted otherwise)

				Limits			
Characteristics	Symbol	Test Conditions	Min.	Тур.	Max.	Units	
Fault Circuitry							
Fault Output Leakage Current	I _{F-LEAK}	V _F = 5 V, No Faults	-	-	10	μΑ	
Fault Output Voltage	V_{FL}	Fault Condition Asserted, I_F = 25 μ A		-	8.0	V	
Fault Output Current	I _F	V _F = 5 V, ENABLE = High	25	50	75	μА	
Output Open Load Current	I _{OUT-O.L.}	V _{BB} = 12 V	-	2	_	mA	
Thermal Fault Temp.	T_F		ı	170	_	°C	
Thermal Fault Hysteresis	T _{FHYS}		-	10	_	°C	
OVLO Enable Threshold		V _{BB} rising	27	30	33	V	
OVLO Hysteresis				1.5		V	
UVLO Enable Threshold		V _{CC} rising	3.5	4	4.49	V	
UVLO Hysteresis			-	400	_	mV	
Timing							
Open Fault to Reset Delay	t_{rs}	V _{BB} = 12 V	-	-	25	μS	
PHASE_X Delay to OPEN Fault	t _{of}	V _{BB} = 12 V	-	8	_	μS	
PHASE_X Delay to S.C. Fault	t _{sc}	V _{BB} = 12 V	-	5	_	μS	
ENABLE_X Delay to Fault	t _{of}	V _{BB} = 12 V	_	5	_	μS	
Propagation Delay Times	^t pd	I _{OUT} = ±0.35A, 50%TO 90%					
		ENABLE ON TO SOURCE ON	-	0.3		μs	
		ENABLE OFF TO SOURCE OFF	-	0.4		μs	
		ENABLE ON TO SINK ON	_	0.3		μs	
		ENABLE OFF TO SINK OFF	-	0.2		μs	
		PHASE CHANGE TO SINK ON	_	4.0		μs	
		PHASE CHANGE TO SINK OFF	_	0.3		μs	
		PHASE CHANGE TO SOURCE ON	-	4.0		μs	
		PHASE CHANGE TO SOURCE OFF	_	0.4		μs	
Cross Over Dead Time	t _{CODT}	1KΩ (1k+1k) Load (V _{BB} to OUT to GND)	0.5	3.0	6.0	μs	



APPLICATION NOTES

Open Load Protection. During normal PWM operation, diagnostic circuitry will look for a minimum source current level after a bridge is signalled on. Additionally, the diode flyback is monitored on the proper output. The logic will signal a fault and disable the outputs if it determines that the current is below the minimum level AND no flyback is occurring. If the inductive load is too large, the current would take too long to reach the minimal level and a false open load would be reported. It is recommended that the inductive load be less than 38 mH.

Short Circuit Protection. Internal sense resistors in series with V_{BB} and ground will trip the fault circuitry if greater than 1.1 A is detected. To prevent false overcurrent events due to reverse recovery spikes of the clamp diodes, the current monitor is blanked for $5\mu s$ after a high side is signalled to turn on. If an additional input command follows after the initial fault, the selected pair of drivers will pulse on for the $5\mu s$ blanking duration. The short circuit fault will remember the input state where the fault occurred and will wait for that particular logic state after the short has been removed before normal operation is allowed to resume.

Thermal Protection. Circuitry turns OFF all drivers when the junction temperature reaches 170°C typically. It is intended only to protect the device from failures due to excessive junction temperatures and has a hysteresis of approximately 15°C.

Layout. The printed wiring board should use a heavy ground plane. For optimum electrical and thermal

performance, the driver should be soldered directly onto the board. The load supply terminal, V_{BB} , should be decoupled with an electrolytic capacitor (> 47 μ F is recommended) placed as close to the device as possible.

Fault Outputs. Both Fault outputs are open-collectorsink types. These turn on (go low) when there is a fault. Each requires a pull-up resistor, rated at approximately $100 \text{ k}\Omega$ at 5 V.



Terminal List

Name	Description	SOIC - 24
V_{BB}	Motor Supply Voltage	1
ENABLE1	Control Logic – Bridge 1	2
PHASE1	Control Logic – Bridge 1	3
GROUND	Power Ground Return	18,19,6,7
OUT1A	Bridge 1 Output A	9
FAULT1	Diagnostic Output	11
OUT1B	Bridge 1 Output B	12
OUT2B	Bridge 2 Output B	13
FAULT2	Diagnostic Output	14
OUT2A	Bridge 2 Output A	16
PHASE2	Control Logic – Bridge 2	22
ENABLE2	Control Logic – Bridge 2	23
V _{cc}	Logic Supply	24
NC	No Connect	4,5,8,15,17,20,21

