



DESCRIPTION

The A4732 is a Low-Power, Two-Port, High-Speed, USB2.0 (480Mbps) double-pole double-throw (DPDT) Analog Switch featuring an On-Resistance of 4.5 ohm at $V_{CC}=3V$ and a Low On Capacitance 3.7pf Typical.

The A4732 is compatible with the requirements of USB2.0 and the wide bandwidth needed to pass the third harmonic, resulting in signals with minimum edge and phase distortion. Superior channel-to-channel crosstalk also minimizes interference. Break-before-make function for both parts eliminate signal disruption during switching from preventing both switches being enabled simultaneously.

The A4732 contains special circuitry on the switch I/O pins for applications where the V_{CC} supply is powered-off ($V_{CC}=0$), which allows the device to withstand an over-voltage condition. This device is designed to minimize current consumption even when the control voltage applied to the Sel pin is lower than the supply voltage (V_{CC}). This feature is especially valuable to ultra-portable applications, such as cell phones, allowing for direct interface with the general purpose I/Os of the baseband processor. Other applications include switching and connector sharing in portable cell phones, PDAs, digital cameras, printers, and notebook computers.

The A4732 is available in TQFN10(1.8x1.4) and MSOP10 packages.

FEATURES

- Wide Power Supply Range: 2.3V to 5V
- Low On Capacitance 3.7pF Typical
- Low On Resistance 4.5Ω (typ.) At 3V V_{DD} when $V_{SW}=0.4V$
- High Bandwidth (-3db): >720MHz without C_L and >550MHz with $C_L=5pF$
- Low Power Consumption: 1uA Maximum
- ESD: pass 8KV HBM test
- Over voltage tolerance (OVT) on all USB ports up to 5.25V without external components
- TTL/CMOS Compatible
- Break-Before-Make Switching
- Operation Temperature Range: -40°C to 85°C

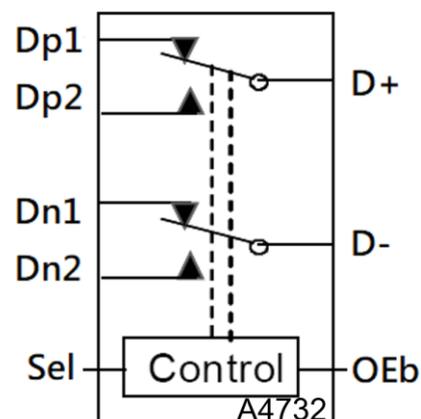
APPLICATION

- Cell phone
- PDAs
- Digital camera
- Notebook
- LCD Monitor
- TV
- SET-TOP BOX

ORDERING INFORMATION

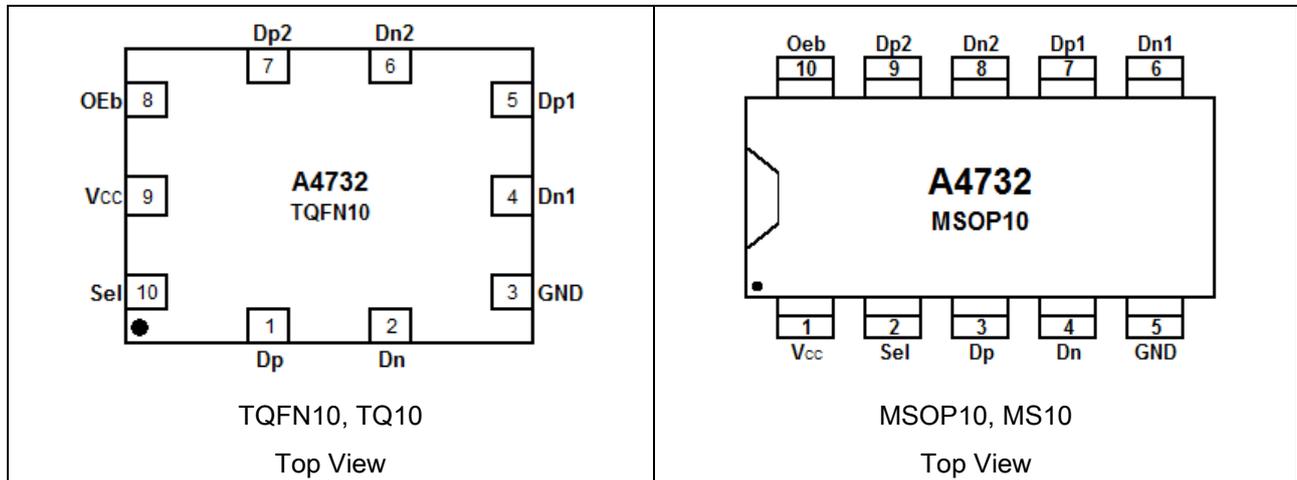
Package Type	Part Number	
TQFN10 SPQ:3,000pcs/Reel	TQ10	A4732TQ10R
		A4732TQ10VR
MSOP10 SPQ:3,000pcs/Reel	MS10	A4732MS10R
		A4732MS10VR
Note	V: Halogen free Package R: Tape & Reel	
AiT provides all RoHS products Suffix " V " means Halogen free Package		

TYPICAL APPLICATION





PIN DESCRIPTION



Pin #		Symbol	Type	Function
TQFN10-B	MSOP10			
1	3	Dp	Input / Output	USB Data BUS
2	4	Dn	Input / Output	USB Data BUS
3	5	GND	Ground	Ground
4	6	Dn1	Input / Output	Data Port
5	7	Dp1	Input / Output	Data Port
6	8	Dn2	Input / Output	Data Port
7	9	Dp2	Input / Output	Data Port
8	10	OEB	Input	Switch enable
9	1	Vcc	PWR	Power Supply
10	2	Sel	Input	Switch select

FUNCTION TABLE

OEB	Sel	Function
1	X	Disconnect
0	0	Dp, Dn=Dp1, Dn1
0	1	Dp, Dn=Dp2, Dn2



ABSOLUTE MAXIMUM RATINGS

V _{CC} , DC Supply Voltage	-0.5V ~ 5.5V
Dpn/Dnn/Dp/Dn, DC Switch Voltage	-0.5V ~ V _{CC} + 0.3V
V _{OEb} / V _{Sel} , DC Input Voltage	-0.5V ~ V _{CC} V
I _(Dpn/Dnn/Dp/Dn) , Continuous Current	-50mA~ +50mA
I _{PEAK(Dpn/Dnn/Dp/Dn)} , Peak Current ^{NOTE1}	-100mA~ +100mA
T _A , Operating Temperature Range	-40°C~ +85°C

Stress beyond above listed "Absolute Maximum Ratings" may lead permanent damage to the device. These are stress ratings only and operations of the device at these or any other conditions beyond those indicated in the operational sections of the specifications are not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

NOTE1: Pulsed at 1ms, 50% duty circle

ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Conditions	Min.	Typ. ⁽²⁾	Max.	Unit
Analog Switch						
Analog Signal Range	V _{Pn} /V _{Nn} /V _p /V _n		0		V _{CC}	V
On-Resistance ^{NOTE3}	R _{ON}	V _{CC} =3V, V _{SW} =0.4V, I _{ON} =-8mA		4.5		Ω
On-Resistance Match Between Channels ^{NOTE4}	ΔR _{ON}	V _{CC} = 3V, V _{SW} =0.4V, I _{ON} =-8mA		0.1		Ω
Current						
Source Off Leakage Current	I _{Pn} / N _n (OFF)	V _{CC} =3.6V, V _p /V _n = 3.6V/0.3V V _{Pn} / V _{Nn} =0.3V/3.6V	-1		1	uA
Channel on Leakage Current	I _{Pn} / N _n (ON)	V _{CC} =3.6V, V _p /V _n =3.6V/0.3V V _{Pn} / V _{Nn} =3.6V/0.3V	-1		1	uA
POWER OFF Leakage Current	I _{OFF}	V _{CC} = 0V, V _{SW} =0V to 3.6V, V _{control} =0 or V _{CC}	-1		1	uA
Quiescent Supply Current	I _{CC}	V _{CC} =3V, V _{control} =0 or V _{CC} , I _{OUT} =0			1	uA
Increase in I _{CC} Current Per Control Voltage and V _{CC}	I _{CC} T	V _{CC} =3.6V, V _{control} =2.6V			4	uA
Input Leakage Current	I _{OEb} / Sel	V _{OEb} / Sel 0 or V _{CC}			1	uA
Digital I/O						
Input Voltage High	V _{IH}	V _{CC} = 3.0-3.6V	1.6			V
Input Voltage Low	V _{IL}	V _{CC} = 3.0-3.6V			0.5	V

NOTE2: Typical characteristics are at +25°C

NOTE3: Measured by the voltage drop between Dpn/Dnn and Dp/Dn pins at the indicated current through the switch. On resistance is determined by the lower of the voltage on the two (Dpn/Dnn and Dp/Dn ports).

NOTE4: ΔR_{ON}= R_{ON(MAX)} – R_{ON(MIN)}, between Dp and Dn .



Parameter	Symbol	Conditions	Min.	Typ. ⁽²⁾	Max.	Unit
DRIVER CHARACTERISTICS						
Turn-On Time	t _{ON}	V _{CC} =3.3V, R _L =50Ω, C _L =5pF, V _{SW} =0.8V		10	30	ns
Turn-Off Time	t _{OFF}	V _{CC} =3.3V, R _L =50Ω, C _L =5pF, V _{SW} =0.8V		20	25	ns
Break-Before-Make Time	t _{BBM}	V _{CC} =3.3V, R _L =50Ω, C _L =5pF, V _{SW1,2} =0.8V	2.0	3	6.5	ns
Propagation Delay	t _{PD}	V _{CC} =3.3V, R _L =50Ω, C _L =5pF		0.2		ns
CAPACITANCE						
Control Capacitance	C _{IN}	V _{CC} =0V		1.5		pF
ON Capacitance	C _{ON}	V _{CC} = 3.3V, OE=0V, f=240MHz		3.7		pF
OFF Capacitance	C _{OFF}	V _{CC} = 3.3V, OE=3.3V, f=240MHz		2.0		pF
APPLICATION CHARACTERISTICS						
3dB Bandwidth	f _{3dB}	V _{CC} = 3.3V, R _L =50Ω, C _L =0pF		720		MHz
		V _{CC} = 3.3V, R _L =50Ω, C _L =5pF		550		MHz
Off Isolation ^{NOTE5}	V _{iso}	V _{CC} = 3.3V, R _L =50Ω, f=250MHz		-30		dB
Channel crosstalk	XTALK	V _{CC} = 3.3V, R _L =50Ω, f=250MHz		-35		dB

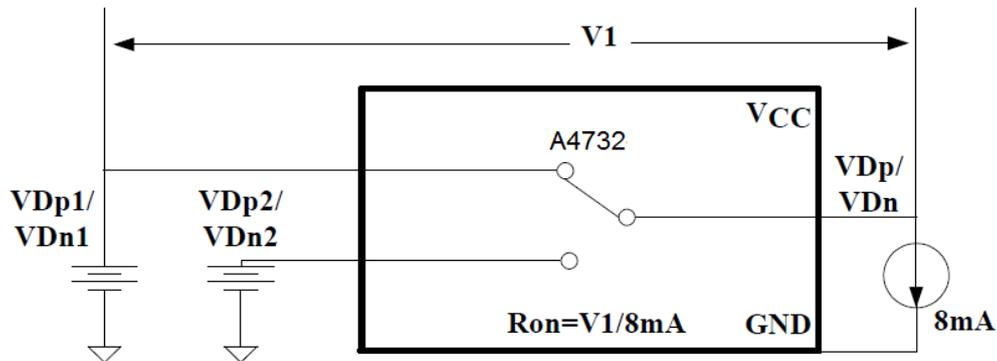
NOTE2: Typical characteristics are at 25°C

NOTE5: Off Channel Isolation = 20log₁₀ [(V_{P1P2})/V_P] or 20log₁₀ [(V_{N1N2})/V_N]

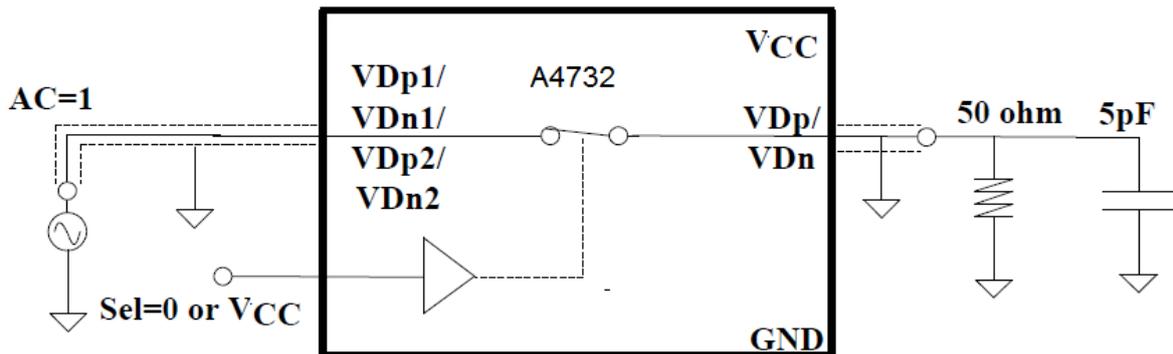


TEST CIRCUIT

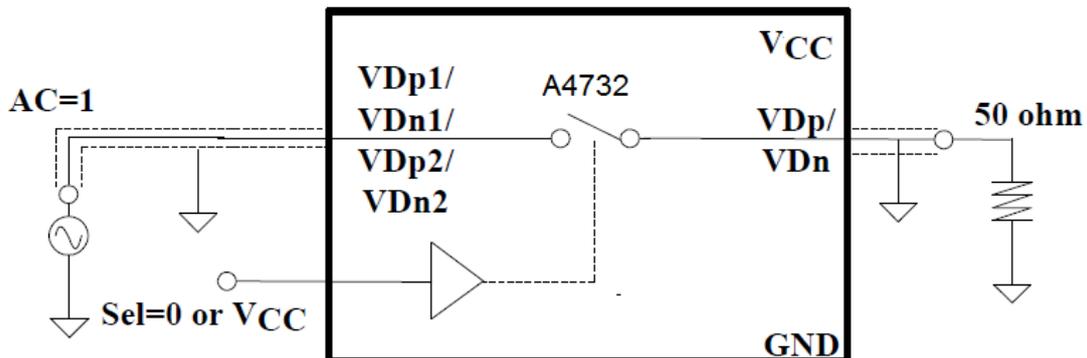
1. Test Circuit for On Resistor



2. Test Circuit for Bandwidth

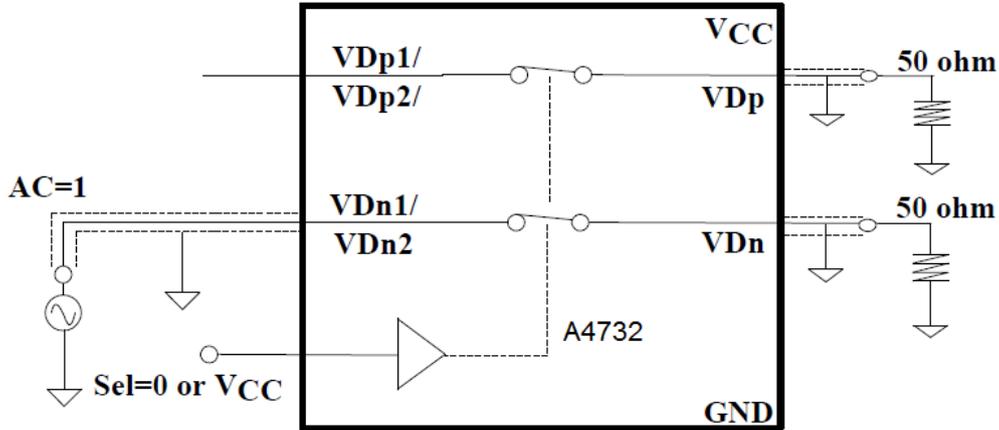


3. Test Circuit for Off Isolation

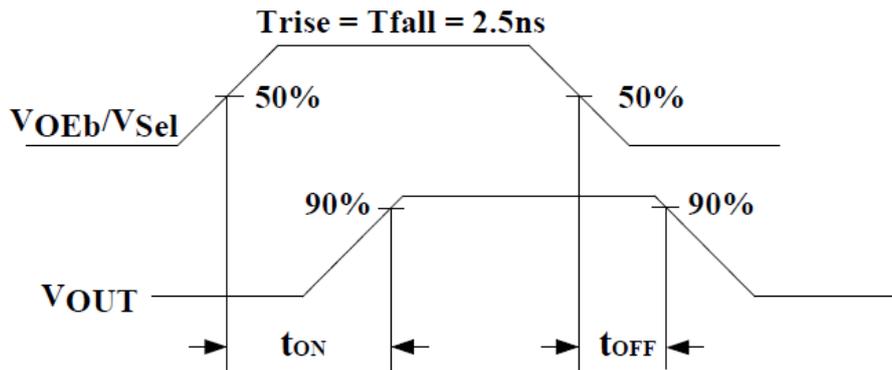
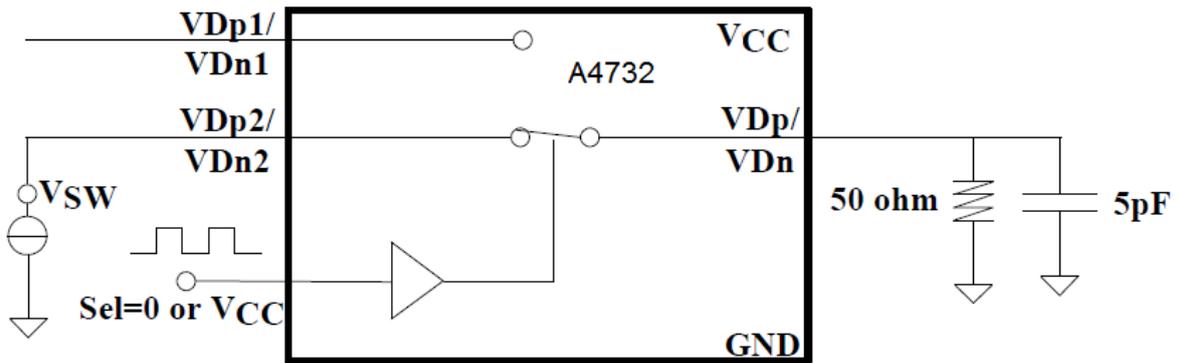




4. Test Circuit for Crosstalk

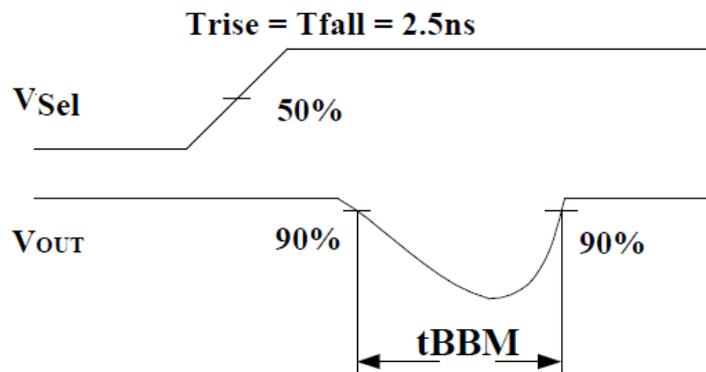
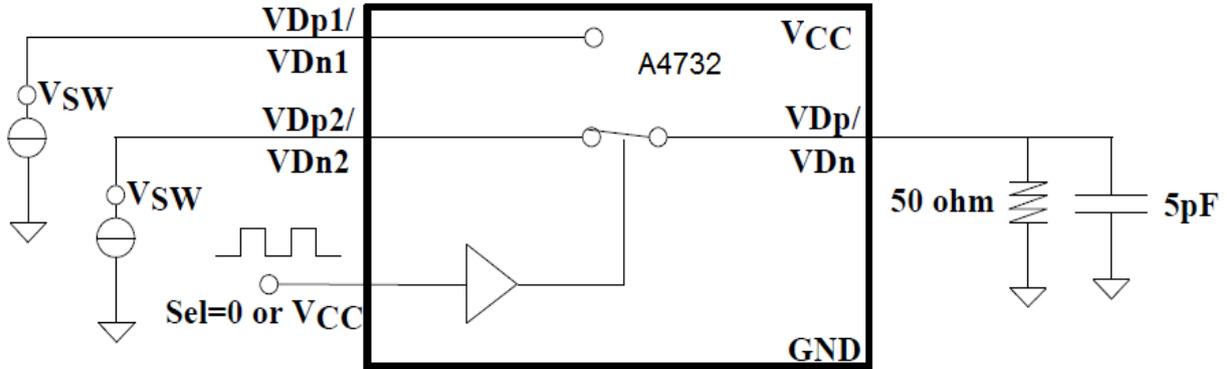


5. Test Circuit for Switch Times

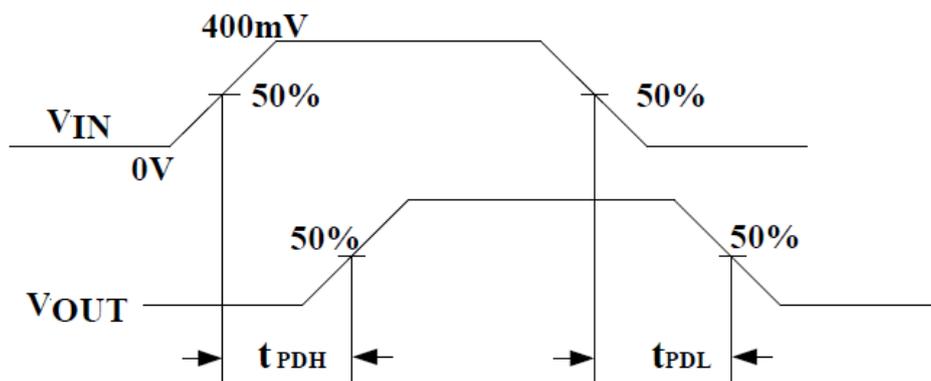
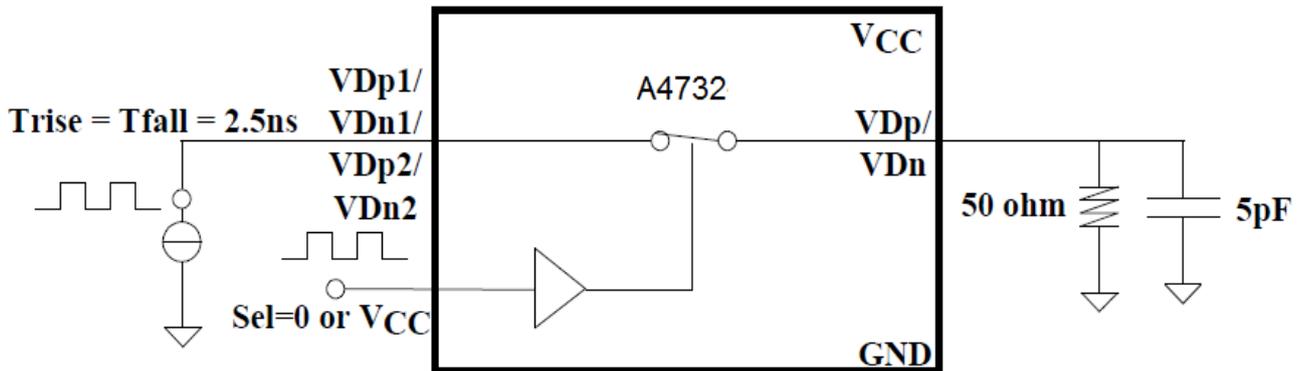




6. Test Circuit for Break-Before-Make Time Delay, t_{BBM}

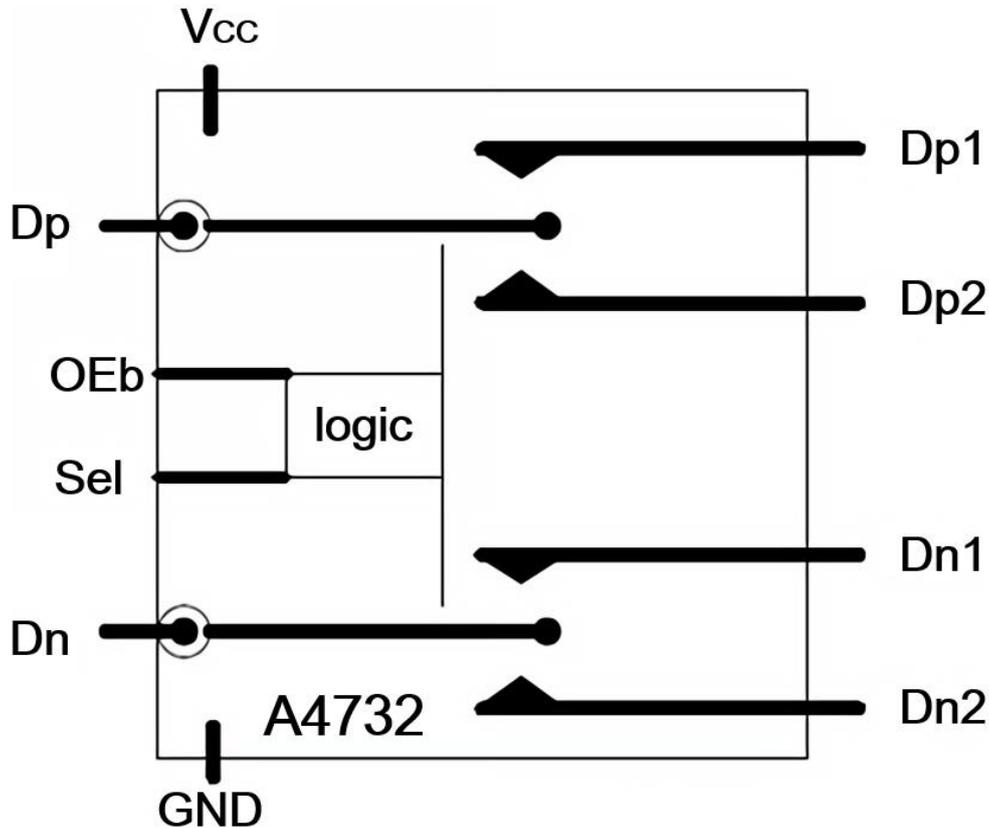


7. Test Circuit for Propagation Delay, t_{PD}





BLOCK DIAGRAM



DETAILED INFORMATION

Meeting USB 2.0 V_{BUS} Short Requirements

(1) Power-Off Protection

For a V_{BUS} short circuit the switch is expected to withstand such a condition for at least 24 hours. The A4732 has the specially designed circuit which prevents unintended signal bleed through as well as guaranteed system reliability during a power-down, over-voltage condition. The protection has been added to the common pins (Dp, Dn).

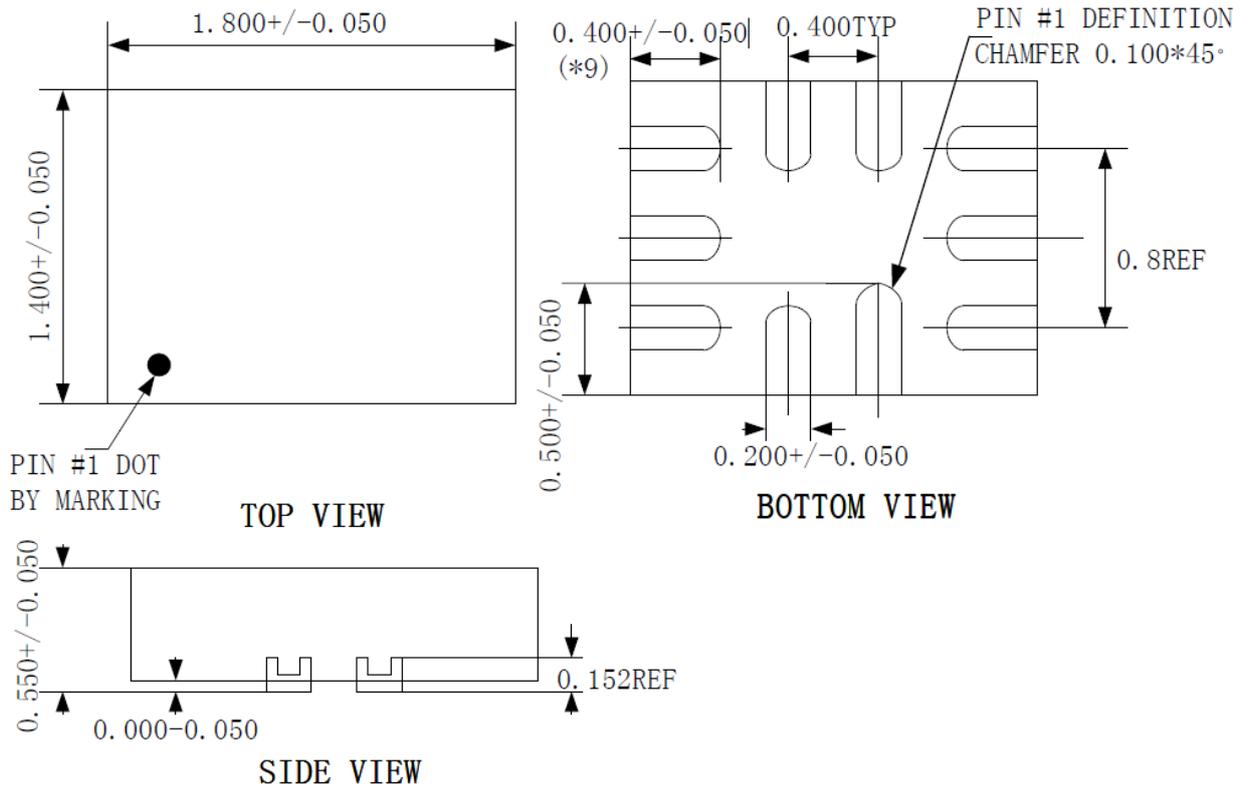
(2) Power-On Protection

The USB 2.0 specification also notes that the USB device should be capable of withstanding a V_{BUS} short during transmission of data. This modification works by limiting current flow back into the V_{CC} rail during the over-voltage event so current remains within the safe operating range.



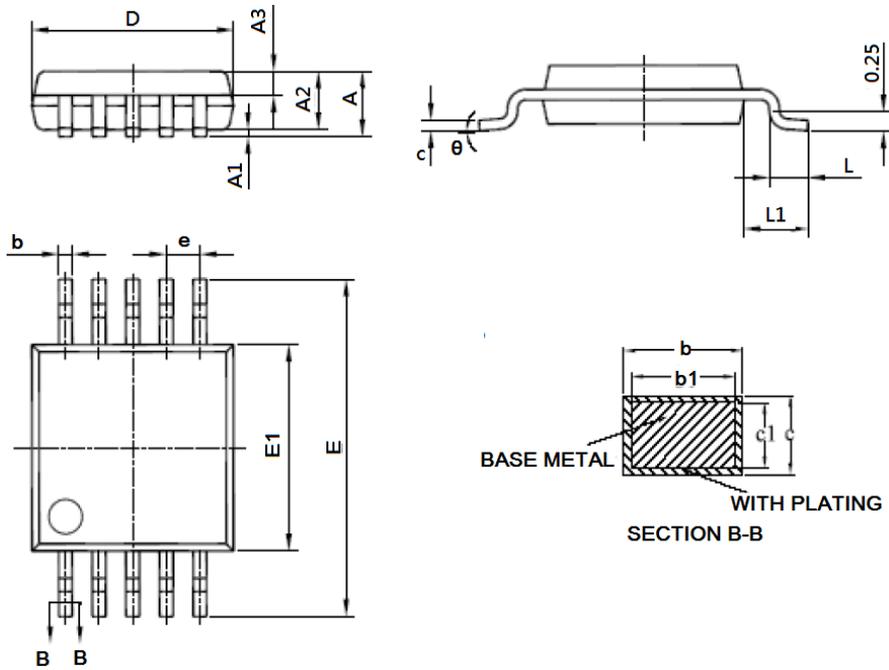
PACKAGE INFORMATION

Dimension in TQFN10 (Unit: mm)





Dimension in MSOP10 (Unit: mm)



Symbol	Min	Max
A	-	1.100
A1	0.050	0.150
A2	0.750	0.950
A3	0.300	0.400
b	0.190	0.280
b1	0.180	0.230
c	0.150	0.200
c1	0.140	0.160
D	2.900	3.100
E	4.700	5.100
E1	2.900	3.100
e	0.500(BSC)	
L	0.400	0.700
L1	0.95(BSC)	
θ	0°	8°
L/P(Mil)	71 x 96	



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