DESCRIPTION

The A4805 is a series of high voltage high-precision voltage detectors developed using standard CMOS process. The detection voltage is fixed internally with an accuracy of ±2.4%. Two output forms, Nch open-drain output, are available low current consumption and miniature package lineup can meet demand from the portable device applications.

A4805 is available in SOT89-3 package.

FEATURES

- Low current consumption 3µA typ.(V_{IN}=8V)
- High-precision detection voltage ±2.4%
- Operating voltage range -0.3 V to 22 V
- Detection voltage 2.4 V to 7.0 V (0.1V step)
- Output form Nch open-drain output (Active Low)
- Available in SOT89-3 package

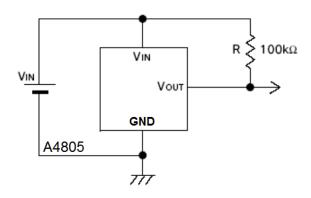
APPLICATION

- Battery checkers
- Power failure detectors
- Power monitor for portable equipments such as pagers, calculators, electronic notebooks and remote controllers.
- Constant voltage power monitor for cameras,
 video equipments and communication devices.
- Power monitor for microcomputers and reset for CPUs.

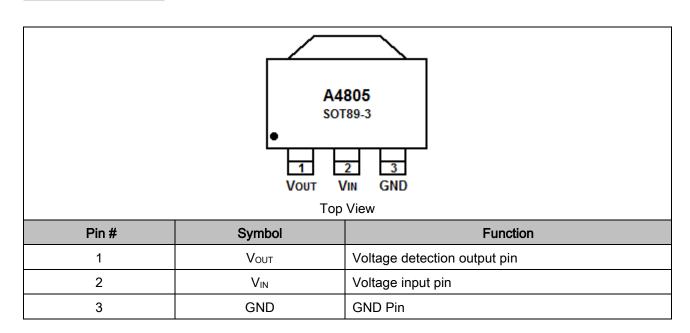
ORDERING INFORMATION

Package Type	Part Number	
SOT89-3	K3	A4805K3R-XXN
30109-3		A4805K3VR-XXN
	XX: Detect Voltage	
	39=3.9V, 45=4.5V	
Note	N: N-channel open drain	
	R: Tape & Reel	
	V: Halogen free Package	
AiT provides all RoHS products		
Suffix "V" means Halogen free Package		

TYPICAL APPLICATION



PIN DESCRIPTION



ABSOLUTE MAXIMUM RATINGS

 $T_A = 25^{\circ}C$

14 20 0				
V _{IN} , Power Supply Voltage		22V		
Ιουτ, Output Current		50mA		
V _{OUT} , Output Voltage	N-ch	V _{SS} -0.3V ~22V		
P _D , Power Dissipation	SOT89-3	500mW		
T _{OPR} , Operation Ambient Temperature		-40°C~+85°C		

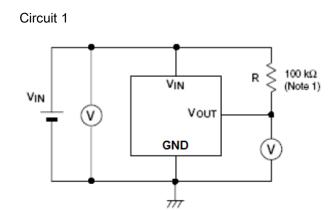
Stresses beyond may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated in the Electrical Characteristics are not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

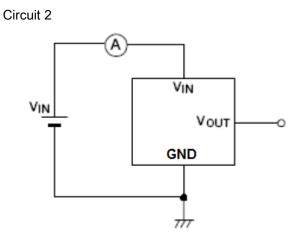
ELECTRICAL CHARACTERISTICS

 $V_{DF(T)}$ = 2.4 to 7.0V ±2.4% T_A=25°C

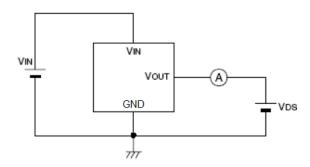
Parameter	Symbol	Conditions		Min	Тур.	Max	Unit	Test circuit
Detection Voltage	V _{DF}			V _{DF} x0.976	V _{DF}	V _{DF} x1.024	V	1
Release Voltage VHY	V _{HYS}	V _{DF} x0.02		V_{DF}	V_{DF}	V_{DF}	V	1
	VIIIS			x0.05	x0.08	,		
Current Consumption	I _{SS}	V _{IN} = 8V			3	5.6	μΑ	2
Output Sink Current	lou	V _{DD} =3.0V	V _{OUT} =0.3V	0.8	1.5		mA	3
		V _{DD} =3.3V	V _{OUT} =0.33V	1	2			
		V _{DD} =3.6V	V _{OUT} =0.36V	1.2	2.5			
		V _{DD} =4.1V	V _{OUT} =0.41V	6.0	11.5			
Temperature		-40~+85°C			±100	100	ppm/°C	
Coefficient					±100			

TEST CIRCUIT



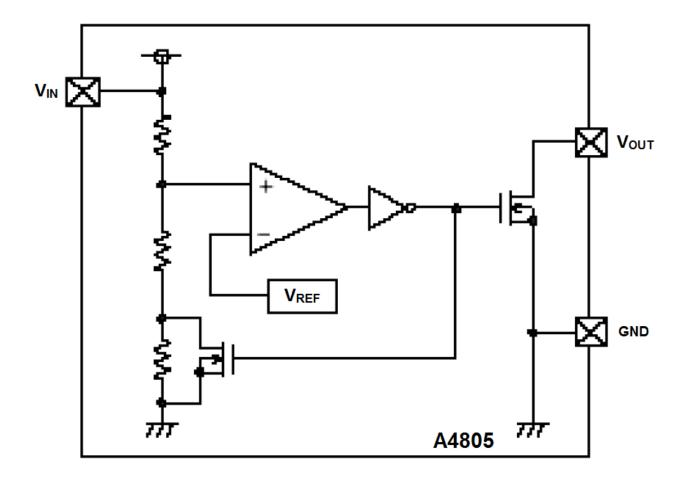


Circuit 3



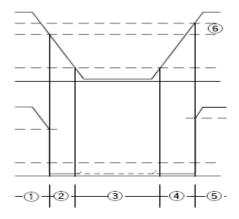
Note1: The resistor is selected according the custom's system

BLOCK DIAGRAM

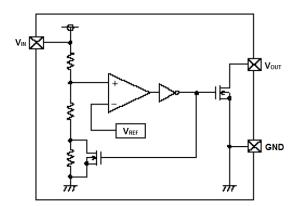


DETAILED INFORMATION

Timing Chart



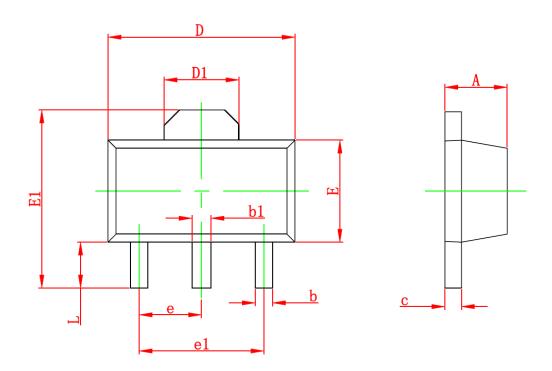
Operation



- 1-1. When the power supply voltage (V_{DD}) is higher than the release voltage (V_{DF}), the Nch transistor is OFF , V_{OUT} is pull up by external resistor to high logic.
- 1-2. When the power supply voltage (VDD) is lower than the release voltage (VDF), the Nch transistor is ON Vout is low.
- **1-3.** When the V_{DD} falls below the minimum operating voltage, the output becomes undefined.
- 1-4. The V_{SS} level appears when the V_{DD} rises above the minimum operating voltage. The V_{SS} level still appears even when the V_{DD} surpasses – V_{DF} , as long as it does not exceed the release voltage + V_{DF} .
- 1-5. When the V_{DD} rises above + V_{DF} the Nch transistor becomes OFF, V_{OUT} is pull up by external resistor to high logic.

PACKAGE INFORMATION

Dimension in SOT89-3 Package (Unit: mm)



Symbol	Min	Max		
А	1.400	1.600		
b	0.320	0.520		
b1	0.400	0.580		
С	0.350	0.440		
D	4.400	4.600		
D1	1.550 REF			
E	2.300	2.600		
E1	3.940 4.250			
е	1.500 TYP			
e1	3.000 TYP			
L	0.900 1.200			



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