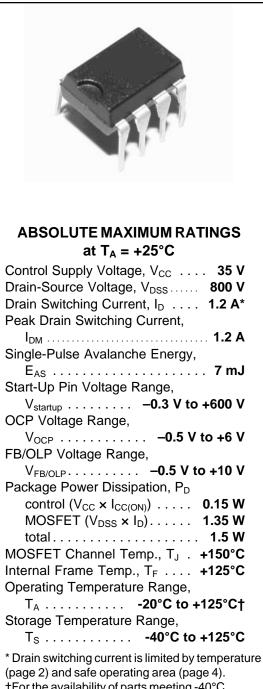


# **STR-A6169**



(page 2) and safe operating area (page 4). †For the availability of parts meeting -40°C requirements, contact Allegro's Sales Representative.

# Universal-Input/5 W Flyback Switching Regulator

The STR-A6169 is a PRC topology (fixed off-time) regulator specifically designed to satisfy the requirements for increased integration and reliability in flyback converters. It incorporates a primary control and drive circuit with avalanche-rated 800 V power MOSFET.

Covering the power range from below 8 watts for a 230 VAC input, or 5 watts for a universal (85 to 264 VAC) input, this device can be used in a range of applications, from DVD and VCR players to ac adapters for cellular phones and digital cameras. An auto-standby function reduces power consumption at light load, while multiple protections, including the avalanche-energy guaranteed MOSFET, provide high reliability of system design. Devices with an increased output power rating are the STR-A6151 and STR-A6159.

Cycle-by-cycle current limiting, undervoltage lockout with hysteresis, overvoltage protection, and thermal shutdown protect the power supply during the normal overload and fault conditions. Overvoltage protection and thermal shutdown are latched after a short delay. The latch may be reset by cycling the input supply. Low start-up current and a low-power standby mode selected from the secondary circuit completes a comprehensive suite of features. The STR-A6169 is provided in an 8pin mini-DIP plastic package.

#### FEATURES AND BENEFITS

- Rugged 800 V Avalanche-Rated MOSFET Simplified Surge Absorption No V<sub>DSS</sub> Derating Required
- $\blacksquare 19.2 \Omega r_{\text{DS(on)}}$
- Two Operational Modes by Automatic Switching: PRC Mode for Normal Operation Burst Mode for Stand-By Operation or Light Loads
- Built-In Leading Edge Blanking
- Low Start-Up Current Start-Up Circuit Disabled in Operation
- Low Operating Current (1.5 mA typ)
- Automatic Burst Stand-By (intermittent operation) Input Power <0.1 W at No Load</p>

-continued

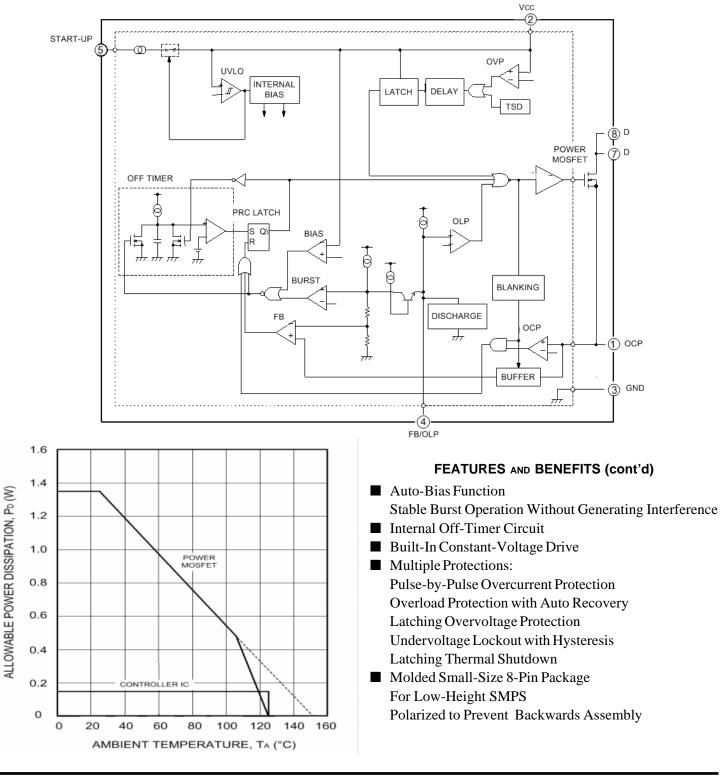
Always order by complete part number, e.g., STR-A6169 .



Sanken Power Devices from Allegro MicroSystems







#### FUNCTIONAL BLOCK DIAGRAM



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#### **ELECTRICAL CHARACTERISTICS** at $T_A = 25^{\circ}C$ , $V_{CC} = 20$ V (unless otherwise specified).

	Pin			Ratings			
Characteristic	No.	Symbol	Test Conditions	Min	Тур	Max	Units
Drain-to-Source Breakdown Volt.	8 - 1	V <sub>DSS</sub>	I <sub>D</sub> = 300 μA,	800	-	-	V
			$V_1 - V_3 = 0 V$ (short)				
Drain Leakage Current	8 - 1	I <sub>DSS</sub>	V <sub>DS</sub> = 800 V,	-	-	300	μA
			$V_1 - V_3 = 0 V$ (short)				
On-State Resistance	8 - 1	r <sub>DS(on)</sub>	$I_{\rm D} = 0.4  {\rm A}$	-	-	19.2	Ω
MOSFET Switching Time	8 - 3	t <sub>f</sub>	-	-	-	250	ns
Operation Start Voltage	2 - 3	V <sub>CC(ON)</sub>	V <sub>CC</sub> = 0 → 19.2 V	16	17.5	19.2	V
Operation Stop Voltage	2 - 3	V <sub>CC(OFF)</sub>	V <sub>CC</sub> = 19.2 → 9 V	9.0	10	11	V
Circuit Current in Operation	2 - 3	I <sub>CC(ON)</sub>	-	-	-	4.0	mA
Circuit Current in Non-Operation	2 - 3	I <sub>CC(OFF)</sub>	V <sub>CC</sub> = 14 V	-	-	50	μA
Auto-Bias Threshold Voltage	2 - 3	V <sub>CC(bias)</sub>	V <sub>CC</sub> = 20 → 9.6 V	9.6	10.6	11.6	V
$V_{CC(bias)} - V_{CC(OFF)}$	-	-	-	0.2	0.6	-	V
Maximum OFF Time	8 - 3	t <sub>OFF</sub>	-	7.3	8.0	8.7	μs
OCP Threshold Voltage	1 - 3	V <sub>OCP</sub>	-	0.69	0.77	0.86	V
Leading Edge Blanking Time	8 - 3	t <sub>b</sub>	-	200	320	480	ns
Burst Threshold Voltage	4 - 3	V <sub>burst</sub>		0.70	0.79	0.88	V
OLP Threshold Voltage	4 - 3	V <sub>OLP</sub>	-	6.5	7.2	7.9	V
Current at OLP Operation	4 - 3	I <sub>OLP</sub>	-	-18	-26	-35	μA
Maximum FB Current	4 - 3	I <sub>FB(MAX)</sub>	-	227	300	388	μA
Start-Up Current	5 - 3	I <sub>startup</sub>	V <sub>CC</sub> = 15 V	340	790	1230	μA
Start-Up Circuit Leakage Current	5 - 3	I <sub>start(leak)</sub>	-	-	-	30	μA
OVP Operation Voltage	2 - 3	V <sub>CC(OVP)</sub>	$V_{CC} = 0 \rightarrow 34.1 \text{ V}$	28.7	31.2	34.1	V
OVP/TSD Latch Sustaining Current	2 - 3	I <sub>CC(H)</sub>	V <sub>CC</sub> =34.1 → 8.5 V	-	-	200	μA
OVP/TSD Latch Release Voltage	2 - 3	V <sub>cc</sub>	V <sub>CC</sub> =34.1 → 6.6 V	6.6	7.3	8.0	V
Thermal Shutdown	-	Τ <sub>J</sub>	-	135	-	-	°C
Thermal Resistance	-	$R_{_{ extsf{ heta}JF}}$	-	-	-	52	°C/W

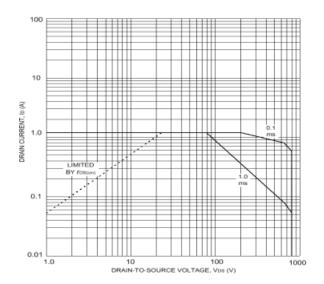
4 WA

WARNING — These devices are designed to be operated at lethal voltages and energy levels. Circuit designs that embody these components must conform with applicable safety requirements. Precautions must be taken to prevent accidental contact with power-line potentials. Do not connect grounded test equipment.

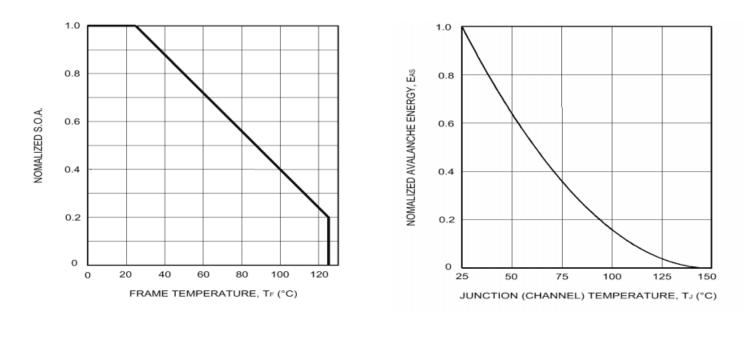
The use of an isolation transformer is recommended during circuit development and breadboarding.



**MOSFET TYPICAL CHARACTERISTICS** 



Avalanche energy is measured at  $V_{\text{DD}}$  = 99 V, L = 20 mH,  $I_{\text{L}}$  = 1.2 A.



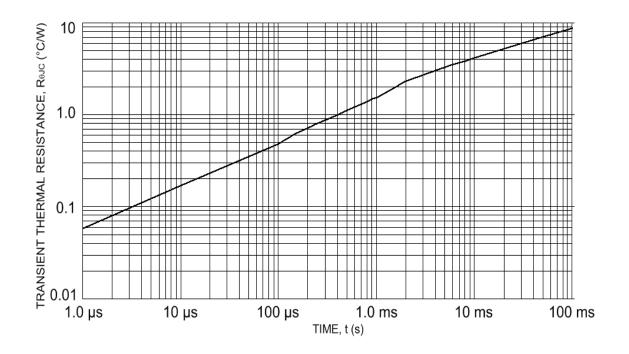


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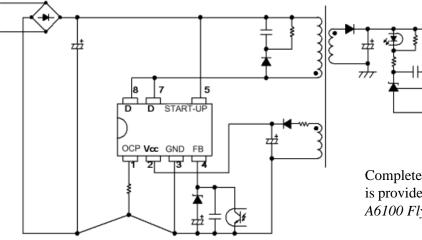


#### MOSFET TYPICAL CHARACTERISTICS (cont'd)





#### **APPLICATIONS INFORMATION**



#### **Typical Application**

Complete product description and applications information is provided in Application Note 28102.20, *Series STR-A6100 Flyback Switching Regulators*.

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#### **PACKAGE DIMENSIONS**

