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SPECIFICATION FOR LCM MODULE

MODULE NO.: ABC016002F04-YKY-R-01 DOC.REVISION 00

	SIGNATURE	DATE
PREPARED BY (RD ENGINEER)		Dec-05-2007
PREPARED BY (QA ENGINEER)		
CHECKED BY		
APPROVED BY		



DOCUMENT REVISION HISTORY

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1. FUNCTIONS & FEATURES

1.1. Format : 16x2 characters

1.2. LCD mode : STN /Positive/Transflective/Y-G

1.3. Viewing direction : 12 O'clock

1.4. Driving scheme : 1/16 Duty, 1/5 Bias

 $\begin{array}{lll} \text{1.5. Power supply voltage } (V_{DD}) & : 5.0V \\ \text{1.6. LCD driving voltage} (Vop) & : 4.2V \\ \text{1.7. Operation temp} & : -20 \sim 70\,^{\circ}\text{C} \\ \text{1.8. Storage temp} & : -30 \sim 80\,^{\circ}\text{C} \\ \text{1.9. Backlight color} & : Bottom Y-G \\ \end{array}$

2. MECHANICAL SPECIFICATIONS

2.1. Module size : 85.0mm(L)*30.0mm(W)*13.5mm(H)max

 2.2. Viewing area
 : 65.0mm(L)*17.2mm(W)

 2.3 Character pitch
 : 3.65mm(L)*5.05mm(W)

 2.4 Character size
 : 2.95mm(L)*4.35mm(W)

 2.5. Dot pitch
 : 0.60mm(L)*0.55mm(W)

 2.6. Dot size
 : 0.55mm(L)*0.50mm(W)

2.7. Weight : Approx.

3. BLOCK DIAGRAM

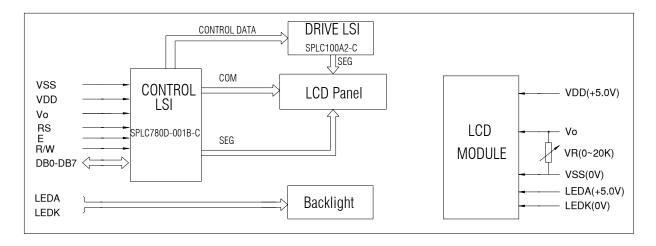


Figure 2. Block diagram



4. DIMENSIONAL OUTLINE

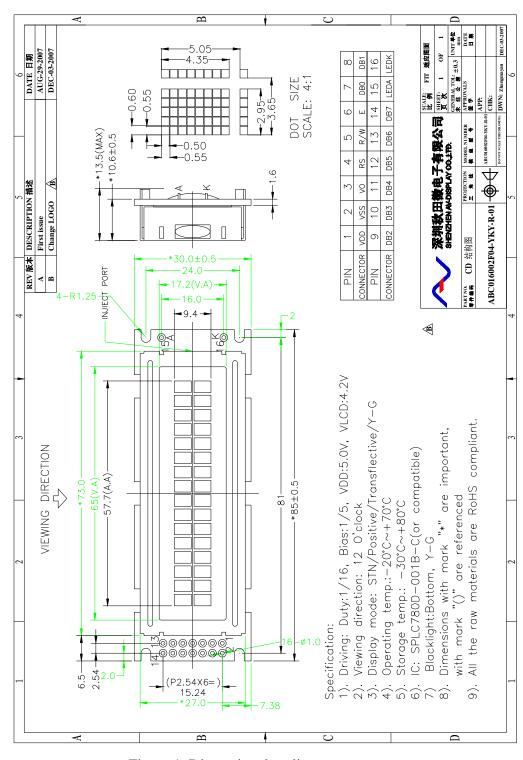


Figure 1. Dimensional outline



5. PIN DESCRIPTION

No.	Symbol	Function
1	VDD	Power supply for Logic(+5V)
2	VSS	Power ground (0V)
3	V0	Power supply for LCD drive
4	RS	Register selection (H: Data register, L:Instruction register)
5	R/W	Read/write selection (H: Read, L: Write)
6	Е	Enable signal.
7-14	DB0~DB7	Data Bus line
15	LEDA	Power supply for Backlight(+5V)
16	LEDK	Power supply for Backlight(0V)

6. MAXIMUM ABSOUTE LIMIT

Item	Symbol	MIN	MAX	Unit
Supply Voltage for Logic	V_{DD}	-0.3	7.0	V
Supply Voltage for LCD	V0	V _{DD} -12.0	V _{DD} +0.3	V
Input Voltage	Vin	-0.3	V _{DD} +0.3	V
Supply Current for Backlight	$I_F(Ta = 25^{\circ}C)$		240	mA
Reverse Voltage for Backlight	$V_R(Ta = 25^{\circ}C)$		10	V
Operating Temperature	Тор	-20	70	$^{\circ}\mathbb{C}$
Storage Temperature	Tst	-30	80	$^{\circ}\mathbb{C}$

7. ELECTRICAL CHARACTERISTICS

Item	Symbol	Condition	Min	Тур	Max	Unit
Supply Voltage for Logic	$V_{\text{DD-}}V_{\text{SS}}$	$Ta = 25^{\circ}C$	4.8	5.0	5.2	V
Input High Voltage	V_{IH}	$Ta = 25^{\circ}C$	$0.7V_{\mathrm{DD}}$		V_{DD}	V
Input Low Voltage	VIL	$Ta = 25^{\circ}C$	-0.3		0.2 V _{DD}	V
Output High Voltage(TTL)	Voh	$Ta = 25^{\circ}C$	$0.75V_{DD}$			V
Output Low Voltage(TTL)	Vol	$Ta = 25^{\circ}C$			0.2Vdd	V
Supply Current	I_{DD}	$Ta = 25^{\circ}C$			3.0	mA



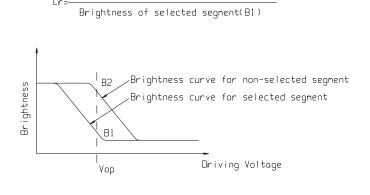
8. BACK LIGHT CHARACTERISTICS

LCD Module with Bottom Y-G Backlight **ELECTRICAL RATINGS**

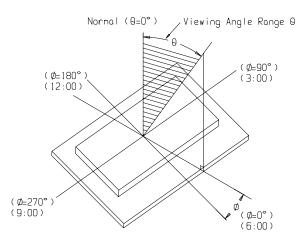
 $Ta = 25^{\circ}C$

Item	Symbol	Condition	Min	Тур	Max	Unit				
Forward Voltage	VF	IF=120mA	4.0	4.2	4.4	V				
Reverse Current	IR	VR=10V		120	600	uA				
Luminous Intensity (Without LCD)	IV	IF=120mA	200	270		Cd/m ²				
Wave length	λр	IF=120mA	569	572	575					
Color		Y-G								

Item	Symbo	Condition	Min	Тур	Max	Unit	
	l						
		$Ta = -20^{\circ}C$	4.6	4.8	5.0		
Operating Voltage	Vop	$Ta = 25^{\circ}C$	4.0	4.2	4.4	V	
		$Ta = 70^{\circ}C$	$Ta = 70^{\circ}C$ 3.5 3.7		3.9		
Dagnonga tima	Tr	Ta = 25°C		185		ms	
Response time	Tf	1a – 25 C		200		ms	
Contrast	Cr	$Ta = 25^{\circ}C$		4			
Viousing angle range	θ	Cr≥2	-40		+40	deg	
Viewing angle range	Ф	Cr > 2	-40		+40	deg	



Brightness of non-selected segment(B2)





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10. TIMING CHARACTERISTICS

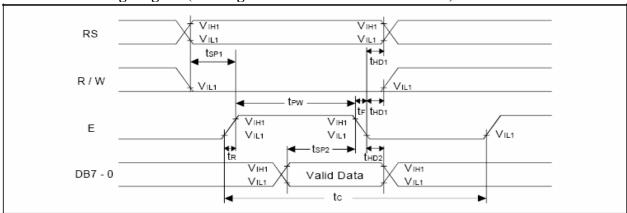
Write mode(writing data from MPU to SPLC780D)

Characteristics	Cumbal		Limit		Unit	Test Condition
Characteristics	Symbol	Min.	Тур.	Max.	VIII.	Test Condition
E Cycle Time	tc	1000	-		ns	Pin E
E Pulse Width	t _{PW}	450			ns	Pin E
E Rise/Fall Time	t _R , t _F	-		25	ns	Pin E
Address Setup Time	t _{SP1}	60	-		ns	Pins: RS, R/W, E
Address Hold Time	t _{HD1}	20			ns	Pins: RS, R/W, E
Data Setup Time	t _{SP2}	195	9 - 4	- 1-	ns	Pins: DB0 - DB7
Data Hold Time	t _{HD2}	10	-	7	ns	Pins: DB0 - DB7

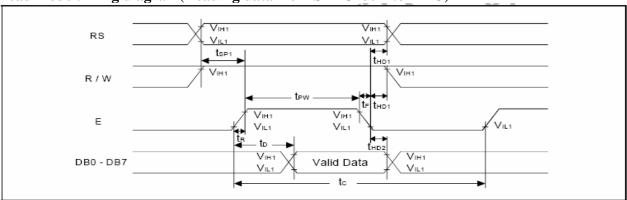
Read mode(reading data from SPLC780D to MPU)

Chamastanistica	Symbol		Limit		11:4	Took Condition
Characteristics	Symbol	Min.	Тур.	Max.	Unit	Test Condition
E Cycle Time	tc	1000	-	-	ns	Pin E
E Pulse Width	t _W	450	-	-	ns	Pin E
E Rise/Fall Time	t <u>e,</u> t _F	-	-	25	ns	Pin E
Address Setup Time	t _{SP1}	60	-	-	ns	Pins: RS, R/W, E
Address Hold Time	thoi	20	-	,	ns	Pins: RS, R/W, E
Data Output Delay Time	t ^o	-	-	360	ns	Pins: DB0 - DB7
Data hold time	t _{HD2}	5.0	-	-	ns	Pin DB0 - DB7

Write mode timing diagram(Writing data from MPU to SPLC780D)



Read mode timing diagram(Reading data from SPLC780D to MPU)





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11. CONTROL AND DISPLAY COMMAND

l				Ins	tructi	on Co	ode				Donaistica.		ecution til emp = 25°	
Instruction	RS	RW	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Description	Fosc= 190KHz	Fosc= 270KHz	Fosc= 350KHz
Clear Display	0	0	0	0	0	0	0	0	0	1	Write "20H" to DDRAM and set DDRAM address to "00H" from AC	2.16ms	1.52ms	1.18ms
Return Home	0	0	0	0	0	0	0	0	1	-	Set DDRAM address to "00H" from AC and return cursor to its original position if shifted. The contents of DDRAM are not changed.	2.16ms	1.52ms	1.18ms
Entry Mode Set	0	0	0	0	0	0	0	1	I/D	Ø	Assign cursor moving direction and enable the shift of entire display	53µs	38µs	29µs
Display ON/ OFF Control	0	0	0	0	0	0	1	D	С	В	Set display (D), cursor(C), and blinking of cursor(B) on/off control bit.	53µs	38µs	29µs
Cursor or Display Shift	0	0	0	0	0	1	s/c	R/L	7		Set cursor moving and display shift control bit, and the direction, without changing of DDRAM data.	53μs	38µs	29µs
Function Set	0	°	0	0	0:- !	DL	2	, L			Set interface data length (DL: 8-bit/4-bit), numbers of display line (N: 2-line/1-line) and, display font type (F:5x10 dots/5x8 dots)	53μs	38µs	29µs
Set CGRAM Address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	Set CGRAM address in address counter.	53µs	38µs	29µs
Set DDRAM Address	0	0		AC6	AC5	AC4	AC3	AC2	AC1	AC0	Set DDRAM address in address counter	53µs	38µs	29µs
Read Busy Flag and Address Counter	0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.			
Write Data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM/CGRAM).	53µs	38µs	29µs
Read Data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM/CGRAM).	53µs	38μs	29µs

Note1: "--": don't care

Note2: In the operation condition under -20℃ ~ 75℃, the maximum execution time for majority of instruction sets is 100us, except two instructions, "Clear Display" and "Return Home", in which maximum execution time can take up to 4.1ms.



12. Font Characteristic

LZ. F (<i>)</i> 11t	CII	ar a	CULI	1911	<u>_</u>										
Upper 4 bit 4 bit	ш	штн	11.HL	LIRH	LHLL	ГНГН	LHHL	ІННН	нш	ншн	HLHL	нин	HHILL	ншн	нянь	ннн
LLLL					Ð	P		P					7	=.	œ	P
LLIH					Ħ		3	-			-	ľ	7	Ä		q
LLHI					В	R	b	r				ď	IJ	×	F	8
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LHLL			\$	4	D	T	d	ŧ.				I	ŀ	t		97
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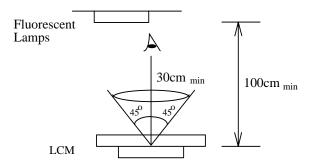


13.QUALITY SPECIFICATIONS

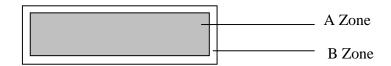
13.1 Standard of the product appearance test

Manner of appearance test: The inspection should be performed in using 20W x 2 fluorescent lamps. Distance between LCM and fluorescent lamps should be 100 cm or more. Distance between LCM and inspector eyes should be 30 cm or more.

Viewing direction for inspection is 45° from vertical against LCM.



Definition of zone:



A Zone: Active display area (minimum viewing area). B Zone: Non-active display area (outside viewing area).

13.2 Specification of quality assurance

AQL inspection standard

Sampling method: MIL-STD-105E, Level II, single sampling



Defect classification (Note: * is not including)

Classify		Item	Note	AQL
Major	Display state	Short or open circuit	1	0.65
		LC leakage		
		Flickering		
		No display		
		Wrong viewing direction		
		Contrast defect (dim, ghost)	2	
		Back-light	1,8	
	Non-display	Flat cable or pin reverse	10	
		Wrong or missing component	11	
Minor	Display	Background color deviation	2	1.0
	state	Black spot and dust	3	
		Line defect, Scratch	4	
		Rainbow	5	
		Chip	6	
		Pin hole	7	
		Protruded	12	
	Polarizer	Bubble and foreign material	3	
	Soldering	Poor connection	9	
	Wire	Poor connection	10	
	TAB	Position, Bonding strength	13	



Note on defect classification

No.	Item	Criterion				
1	Short or open circuit	Not allow				
	LC leakage					
	Flickering					
	No display					
	Wrong viewing direction					
	Wrong Back-light					
2	Contrast defect	Refer to approval sample				
	Background color deviation					
3	Point defect, Black spot, dust	↑Y			Point Size	Acceptable Qty.
	(including Polarizer)	\mathbf{X}^{\dagger}			φ <u><</u> 0.10	Disregard
				0	.10<φ≤0.20	3
	$\phi = (\mathbf{V} \cdot \mathbf{V})/2$			0	.20<∮≤0.25	2
	$\phi = (X+Y)/2$			0	.25<∮≤0.30	1
					φ>0.30	0
			Uni	t:	mm	
4	Line defect,	<u> </u>				
•			Line		Line	Acceptable Qty.
	Scratch	$\left \longleftrightarrow\right $	L		W	
		L			0.015≥W	Disregard
			3.0≥ 2.0≥		0.03≥W	2
			1.0≥		0.05≥W 0.1>W	1
					0.05 <w< td=""><td>Applied as point defect</td></w<>	Applied as point defect
			ττ	J ni	t: mm	
5	Rainbow	Not more than two color changes across the viewing area.				



No	Item	Criterion		
6	Chip Remark: X: Length direction Y: Short	Acceptable criterion $\begin{array}{c ccccccccccccccccccccccccccccccccccc$		
	direction Z: Thickness direction t: Glass thickness W: Terminal Width	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$		
		Acceptable criterion $\begin{array}{ c c c c c c c }\hline X & Y & Z\\\hline \leqslant 3 & \leqslant 2 & \leqslant t\\\hline \text{shall not reach to ITO} \end{array}$		
		Acceptable criterion $\frac{Y}{X} \downarrow \frac{X}{X} \frac{Y}{X} \frac{Z}{Z}$ Disregard $\leq 0.2 \leq t$		
		$\begin{array}{c ccccccccccccccccccccccccccccccccccc$		



No.	Item	Criterion		
7	Segment pattern $W = \text{Segment width}$ $\phi = (X+Y)/2$	(1) Pin hole $\phi < 0.10 \text{mm is acceptable.}$ Y		
8	Back-light	(1) The color of backlight should correspond its specification.(2) Not allow flickering		
9	Soldering	(1) Not allow heavy dirty and solder ball on PCB. (The size of dirty refer to point and dust defect) (2) Over 50% of lead should be soldered on Land. Lead Land 50% lead		
10	Wire	 (1) Copper wire should not be rusted (2) Not allow crack on copper wire connection. (3) Not allow reversing the position of the flat cable. (4) Not allow exposed copper wire inside the flat cable. 		
11*	PCB	(1) Not allow screw rust or damage.(2) Not allow missing or wrong putting of component.		



No	Item	Criterion		
12	Protruded W: Terminal Width	Acceptable criteria: $Y \le 0.4$		
13	TAB	1. Position $\begin{array}{cccccccccccccccccccccccccccccccccccc$		
		2 TAB bonding strength test TAB P (=F/TAB bonding width) ≥650gf/cm ,(speed rate: 1mm/min) 5pcs per SOA (shipment)		
14	Total no. of acceptable Defect	A. Zone Maximum 2 minor non-conformities per one unit. Defect distance: each point to be separated over 10mm B. Zone It is acceptable when it is no trouble for quality and assembly in customer's end product.		



13.3 Reliability of LCM

Reliability test condition:

Item	Condition	Time (hrs)	Assessment
High temp. Storage	80°C	48	
High temp. Operating	70°C	48	
Low temp. Storage	-30°C	48	No abnormalities
Low temp. Operating	-20°C	48	in functions
Humidity	40°C/ 90%RH	48	and appearance
Temp. Cycle	-20°C ← 25°C →70°C	10cycles	
	$(30 \min \leftarrow 5 \min \rightarrow 30 \min)$		

Recovery time should be 24 hours minimum. Moreover, functions, performance and appearance shall be free from remarkable deterioration within 50,000 hours under ordinary operating and storage conditions room temperature ($20\pm8^{\circ}$ C), normal humidity (below 65% RH), and in the area not exposed to direct sun light.

13.4 Precaution for using LCD/LCM

LCD/LCM is assembled and adjusted with a high degree of precision. Do not attempt to make any alteration or modification. The followings should be noted.

General Precautions:

- 1. LCD panel is made of glass. Avoid excessive mechanical shock or applying strong pressure onto the surface of display area.
- 2. The polarizer used on the display surface is easily scratched and damaged. Extreme care should be taken when handling. To clean dust or dirt off the display surface, wipe gently with cotton, or other soft material soaked with isoproply alcohol, ethyl alcohol or trichlorotriflorothane, do not use water, ketone or aromatics and never scrub hard.
- 3. Do not tamper in any way with the tabs on the metal frame.
- 4. Do not made any modification on the PCB without consulting AV.
- 5. When mounting a LCM, make sure that the PCB is not under any stress such as bending or twisting. Elastomer contacts are very delicate and missing pixels could result from slight dislocation of any of the elements.



- 6. Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pixels and also cause rainbow on the display.
- 7. Be careful not to touch or swallow liquid crystal that might leak from a damaged cell. Any liquid crystal adheres to skin or clothes, wash it off immediately with soap and water.

Static Electricity Precautions:

- 1. CMOS-LSI is used for the module circuit; therefore operators should be grounded whenever he/she comes into contact with the module.
- 2. Do not touch any of the conductive parts such as the LSI pads; the copper leads on the PCB and the interface terminals with any parts of the human body.
- 3. Do not touch the connection terminals of the display with bare hand; it will cause disconnection or defective insulation of terminals.
- 4. The modules should be kept in anti-static bags or other containers resistant to static for storage.
- 5. Only properly grounded soldering irons should be used.
- 6. If an electric screwdriver is used, it should be grounded and shielded to prevent sparks.
- 7. The normal static prevention measures should be observed for work clothes and working benches.
- 8. Since dry air is inductive to static, a relative humidity of 50-60% is recommended.

Soldering Precautions:

- 1. Soldering should be performed only on the I/O terminals.
- 2. Use soldering irons with proper grounding and no leakage.
- 3. Soldering temperature: 280°C+10°C
- 4. Soldering time: 3 to 4 second.
- 5. Use eutectic solder with resin flux filling.
- 6. If flux is used, the LCD surface should be protected to avoid spattering flux.
- 7. Flux residue should be removed.

Operation Precautions:

- 1. The viewing angle can be adjusted by varying the LCD driving voltage Vo.
- 2. Since applied DC voltage causes electro-chemical reactions, which deteriorate the display, the applied pulse waveform should be a symmetric waveform such that no DC component remains. Be sure to use the specified operating voltage.
- 3. Driving voltage should be kept within specified range; excess voltage will shorten display life.
- 4. Response time increases with decrease in temperature.
- 5. Display color may be affected at temperatures above its operational range.
- 6. Keep the temperature within the specified range usage and storage. Excessive temperature and humidity could cause polarization degradation, polarizer peel-off or generate bubbles.
- 7. For long-term storage over 40°C is required, the relative humidity should be kept below 60%, and avoid direct sunlight.



Limited Warranty

AV LCDs and modules are not consumer products, but may be incorporated by AV's customers into consumer products or components thereof, AV does not warrant that its LCDs and components are fit for any such particular purpose.

- 1. The liability of AV is limited to repair or replacement on the terms set forth below. AV will not be responsible for any subsequent or consequential events or injury or damage to any personnel or user including third party personnel and/or user. Unless otherwise agreed in writing between AV and the customer, AV will only replace or repair any of its LCD which is found defective electrically or visually when inspected in accordance with AV general LCD inspection standard. (Copies available on request)
- 2. No warranty can be granted if any of the precautions state in handling liquid crystal display above has been disregarded. Broken glass, scratches on polarizer mechanical damages as well as defects that are caused accelerated environment tests are excluded from warranty.
- 3. In returning the LCD/LCM, they must be properly packaged; there should be detailed description of the failures or defect.