

### SPECIFICATION FOR LCM MODULE

### MODULE NO.: ABC016004A01-YHY-R DOC.REVISION: 00

**Customer Approval:** 

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	SIGNATURE	DATE
PREPARED BY (RD ENGINEER)		Nov-21-2005
PREPARED BY (QA ENGINEER)		
CHECKED BY		
APPROVED BY		



### **DOCUMENT REVISION HISTORY**

Version	DATE	DESCRIPTION	CHANGED BY
A00	Nov-21-2005	First issue	

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: 16x4 characters

: 1/16 Duty, 1/5 Bias

: STN / Positive transflective mode / Y-G

### **<u>1. FUNCTIONS & FEATURES</u>**

- 1.1. Format
- 1.2. LCD mode
- 1.3. Viewing direction
- 1.4. Driving scheme
- 1.5. Power supply voltage  $(V_{DD})$
- 1.6. LCD driving voltage(Vop)
- 1.7. Operation temp
- 1.8. Storage temp
- 1.9. Backlight color
- 2.0 Driver IC

: 4.2V : -20~70 : -30~80

: 5.0V

: 6 o'clock

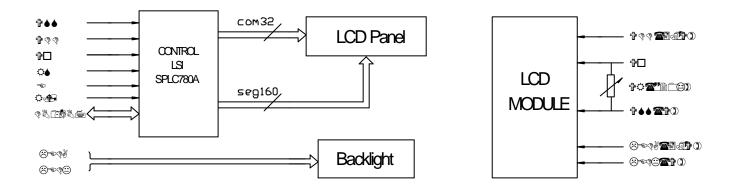
- : Yellow-Green
- : SPLC780A

### 2. MECHANICAL SPECIFICATIONS

- 2.1. Module size
- 2.2. Viewing area
- 2.3 Character pitch
- 2.4 Character size
- 2.5. Dot pitch
- 2.6. Dot size
- 2.7. Weight

- : 87.0mm(L)\*60.0mm(W)\*14.5max mm(H)
- : 61.8mm(L)\*25.2mm(W)
- : 3.55mm(L)\*5.35mm(W)
- : 2.96mm(L)\*4.76mm(W)
- : 0.6mm(L)\*0.6mm(W)
- : 0.56mm(L)\*0.56mm(W)
- : Approx.

### **<u>3. BLOCK DIAGRAM</u>**



#### Figure 1. Block diagram



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### **4. DIMENSIONAL OUTLINE**

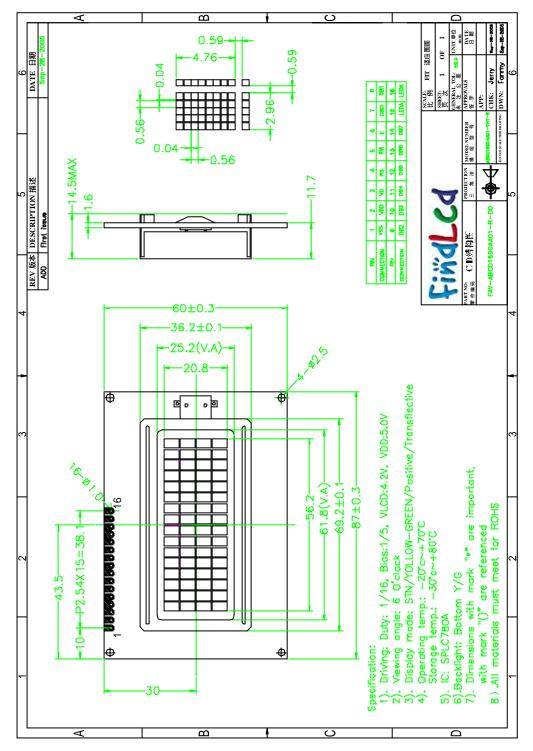


Figure 2. Dimensional outline

### 5. PIN DESCRIPTION

No.	Symbol	Function
1	VSS	GND (0V)
2	VDD	Power supply for Logic (+5V)
3	V0	Power supply for LCD drive
4	RS	Register selection (H: Data register, L: Instruction register)
5	R/W	Read/write selection (H:Read, L: Write)
6	E	Enable signal for LCM
7-14	DB0~DB7	Data Bus line
15	LEDA	Power supply for Backlight(+5V)
16	LEDK	Power supply for Backlight(0V)

### 6. MAXIMUM ABSOLUTE LIMIT

Maximum Absolute Power Ratings

### ABSOLUTE MAXIMUM RATINGS

Characteristics	Symbol	Ratings
Operating Voltage	Vdd	-0.3V to +7.0V
Driver Supply Voltage	VLCD	VDD-12V to VDD+0.3V
Input Voltage Range	Vin	-0.3V to V <sub>DD</sub> + 0.3V
Operating Temperature	TA	0 ℃ to +60 ℃
Storage Temperature	Тѕто	-55 ℃ to +125 ℃

### 7. ELECTRICAL CHARACTERISTICS

### 7.1 DC CHARACTERISTICS

DC CHARACTERISTICS ( $T_A = 25 \ ^{\circ}C$ , VDD = 5V)

			Limit			
Characteristics	Symbol	Min	Тур	Max	Unit	Test Condition
Operating Voltage	Vdd	4.5	-	5.5	V	
Operating Current	DD	-	0.55	0.8	mA	External clock (Note)
Input High Voltage	VIH1	2.2	-	Vdd	V	Pins:( E, RS, R/W,
Input Low Voltage	VIL1	-0.3	-	0.6	V	DB0 - DB7 )
Input High Voltage	VIH2	VDD-1	-	Vdd	V	Pin OSC1
Input Low Voltage	VIL2	-0.2	-	1.0	V	Pin OSC1
Input High Current	Ін	-	-	2	μA	Pins: ( RS, R/W,
Input Low Current	lı∟	-50	-125	-250	μA	DB0 - DB7 )
Output High	Vон1	2.4	-	Vdd	V	Іон <b>= - 0.1 mA</b>
Voltage(TTL)						Pins: DB0 - DB7
Output Low	Vol1	-	-	0.4	V	lo∟ = 0.1 mA
Voltage(TTL)						Pins: DB0 - DB7
Output High	Vон2	0.9Vdd	-	Vdd	V	Іон <b>= - 40</b> μ <b>A</b> , <b>Pins</b> :
Voltage ( CMOS )						CLK1,CLK2,M,D
Output Low	Vol2	-	-	0.1Vdd	V	lo∟ = 40 μA, Pins:
Voltage ( CMOS )						CLK1,CLK2,M,D
LCD Driver Voltage	VLCD	V <sub>DD</sub> -0.3		12	V	V <sub>DD</sub> - V5
Voltage Drop	Vdcom	-	-	1	V	lo = 0.1 mA
						Pins: COM1-COM16
	Vdseg	-	-	1	V	lo = 0.1 mA
						Pins: SEG1-SEG40

Note: Fosc=250KHz, pin E="L", RS,R/W,DB0-DB7 are open, all outputs are no loads.

### 7.2 AC Characteristics

### (VDD = 4.5V ~ 5.5V, Ta = 25 ) AC CHARACTERISTICS ( TA = 25 $^{\circ}$ C, VDD = 5 V )

Internal Clock Operation

Chanastanistica	Min Typ Max	11	Test Condition			
Characteristics		Min	Тур	Мах	Unit	Test Condition
OSC Frequency	Fosc1	190	270	350	KHz	Rf = 91 K $\Omega\pm$ 2%

#### External clock operation

<b>O</b> hamatariatian	<b>0</b>		Limit		11	Test Carditian
Characteristics	Symbol	Min	Тур	Мах	Unit	Test Condition
External Frequency	Fosc3	125	250	350	KHz	
Duty Cycle		45	50	55	%	
Rise/Fall Time	tr, tr			0.2	μs	

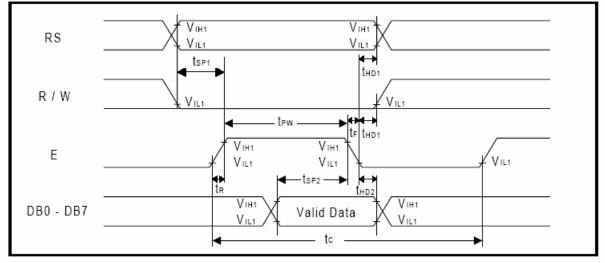
■ Write Mode (Writing data from MPU to SPLC780A)

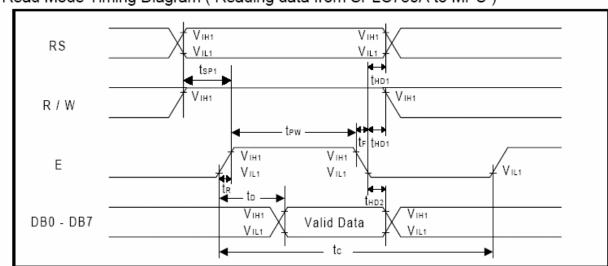
Characteristics	Sumbol		Limit		Unit	Test Condition
Characteristics	Symbol	Min	Min Typ Max		Unit	Test Condition
E Cycle Time	tc	400	-	-	ns	Pin E
E Pulse Width	t <sub>PW</sub>	150	-	-	ns	Pin E
E Rise/Fall Time	t <sub>R</sub> , t <sub>F</sub>	-	-	25	ns	Pin E
Address Setup Time	t <sub>SP1</sub>	30	-	-	ns	Pins: RS, R/W,E
Address Hold Time	t <sub>HD1</sub>	10	-	-	ns	Pins: RS, R/W,E
Data Setup Time	t <sub>SP2</sub>	40	-	-	ns	Pins: DB0 ~ DB7
Data Hold Time	t <sub>HD2</sub>	10	-	-	ns	Pins: DB0 ~ DB7

Chana stanistica	0. matrix at		Limit		11	T	
Characteristics	Symbol	Min	Min Typ		Unit	Test Condition	
E Cycle Time	tc	400	-	-	ns	Pin E	
E Pulse Width	tw	150	-	-	ns	Pin E	
E Rise/Fall Time	t <sub>R</sub> , t <sub>F</sub>	-	-	25	ns	Pin E	
Address Setup Time	t <sub>SP1</sub>	30	-	-	ns	Pins: RS, R/W,E	
Address Hold Time	t <sub>HD1</sub>	10	-	-	ns	Pins: RS, R/W,E	
Data Output Delay Time	t⊳	-	-	100	ns	Pins: DB0 ~ DB7	
Data hold time	t <sub>HD2</sub>	20	-	-	ns	Pin DB0 ~ DB7	

#### ■ Read Mode ( Reading data from SPLC780A to MPU )

### Write Mode Timing Diagram (Writing data from MPU to SPLC780A)





Read Mode Timing Diagram (Reading data from SPLC780A to MPU)

### **8. CONTROL AND DISPLAY INSTRUCTION**

				Ins	tructi	on C	ode					Execution time
Instruction	RS	RW	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Description	(fosc=270KHz)
Clear Display	0	0	0	0	0	0	0	0	0	1	Write "20H" to DDRAM and set DDRAM address to "00H" from AC	1.52ms
Return Home	0	0	0	0	0	0	0	0	1	-	Set DDRAM address to "00H" from AC and return cursor to its original position if shifted. The contents of DDRAM are not changed.	1.52ms
Entry Mode Set	0	0	0	0	0	0	0	1	I/D	s	Assign cursor moving direction and enable the shift of entire display	38µs
Display ON/ OFF Control	0	0	0	0	0	0	1	D	с	В	Set display(D), cursor(C), and blinking of cursor(B) on/off control bit.	38µs
Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	-	-	Set cursor moving and display shift control bit, and the direction, without changing of DDRAM data.	38µs
Function Set	0	0	0	0	1	DL	N	F	-	-	Set interface data length (DL: 8-bit/4-bit), numbers of display line (N: 2-line/1-line) and, display font type (F:5x10 dots/5x8 dots)	38µs
Set CGRAM Address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	Set CGRAM address in address counter.	38µs
Set DDRAM Address	0	0	1	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Set DDRAM address in counter	38µs
Read Busy Flag and Address Counter	0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.	
Write Data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM/CGRAM).	38µs
Read Data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM/CGRAM).	38µs



### 9. CHARACTER GENERATOR ROM

				-	-	_						-
0000	0010	0011	0100	0101	0110	0111	1010	1011	1100	1101	1110	1111
CG RAM (1)		Ŀ	ı <u>ı</u> i		••	₽			<b>ÿ</b>		Û	P
(2)	ł			Q		q	CI	Ţ	÷	Ĺ		q
(3)	••	2		R		r	Ĩ	4	Ņ	×		8
(4)			C	5	C.	5	_1	ņ	Ţ	<b>.</b>		60
(5)	\$	4	D		Ċ	ŧ.	•.			<b>†</b> 7	<b>.</b>	Ω
(6)	2				e	Li		7	<u>.</u>		G	ü
(7)	8	6		Ų	<b>.</b>	Ų	Ţ	ŢŢ	•••	•••••	ρ	Σ
CG RAM	2	7			<b></b>	IJ			<u>;</u> ;;	7		Л
CG	Ć	8		X	ŀ'n	X	4	<b>;</b> ]	····· · <b>†</b> ·	Ņ	J.	$\overline{\times}$
(2)	)	9		Y		<b>ب</b>		Ţ	ļ	11.	1	Ч
(3)	*	# #	J		j	7			11			Ŧ
(4)		# ?	K			ł	7	Ţ			×	Ţ
(5)	2						<b>†</b> ?			ņ	<b>¢</b> .	FI
(6)	••••	•••••	M		Ĩ	}	<u>.</u>	7	•	 •	<b>.</b>	÷
(7)			ŀ·	•••	i"i	÷	==	17	<b></b> 	•••	r"ı	
CG RAM (8)	/	2	Ü		Ö	÷	:1	<b>!</b> ]	7	<b>i.i</b>	Ö	
	$\begin{array}{c} CG \\ RAM \\ (1) \\ (2) \\ (3) \\ (4) \\ (5) \\ (6) \\ (7) \\ CG \\ RAM \\ (2) \\ (3) \\ (4) \\ (5) \\ (6) \\ (7) \\ (7) \\ (6) \\ (7) \\ ($	CG RAM (1) (2) (3) (4) (4) (5) (6) (6) (7) (6) (7) (3) (4) (4) (4) (5) (7) (3) (4) (4) (7) (6) (7) (7) (7) (7) (7) (7) (7) (7) (7) (7	CG       Q         (1)       1       1         (2)       1       1         (3)       1       2         (4)       1       3         (5)       5       4         (6)       5       5         (6)       5       5         (7)       5       5         (7)       5       5         (7)       5       5         (7)       5       5         (7)       5       5         (7)       5       5         (6)       7       7         (3)       3       5         (4)       7       5         (5)       5       5         (6)       7       7         (5)       5       5         (6)       7       7         (5)       5       5         (6)       7       7         (7)       7       7         (6)       7       7         (7)       7       7         (7)       7       7         (6)       7       7         (7)       <	CG       Q       Q         (1) $1$ $1$ (2) $1$ $1$ $1$ (3) $1$ $2$ $3$ (4) $4$ $3$ $C$ (5) $4$ $0$ (6) $3$ $5$ $E$ (7) $8$ $6$ $F$ (6) $3$ $7$ $G$ (7) $8$ $6$ $F$ (7) $8$ $6$ $F$ (7) $8$ $6$ $F$ (3) $4$ $7$ $G$ (3) $4$ $3$ $J$ (4) $+$ $3$ $J$ (4) $+$ $3$ $J$ (5) $3$ $4$ $0$ (6) $$ $$ $M$ (7) $3$ $3$ $3$ (7) $3$ $3$ $3$ (7) $3$ $3$ $3$ $7$ $0$ $1$ $1$	$ \begin{array}{c c} CG \\ RAM \\ (1) \\ (2) \\ (2) \\ (3) \\ (4) \\ (4) \\ (4) \\ (4) \\ (5) \\ (5) \\ (5) \\ (6) \\ (6) \\ (7) \\ (6) \\ (7) \\ (6) \\ (7) \\ (7) \\ (7) \\ (7) \\ (7) \\ (7) \\ (6) \\ (7) \\ (7) \\ (7) \\ (6) \\ (7)$	CG       Q       Q       P $$ (1)       1       1       1       Q       a         (2)       1       1       1       Q       a         (3)       1       2       B       R       b         (4)       1       3       C       S       c         (5)       5       4       D       T       d         (6)       2       5       E       U       e         (7)       8       6       F       V       f         (8)       7       G       W       g       g         (3)       *       I       J       Z       j         (4)       +       5       K       K       k         (5)       9       I       I       m         (6)        =       M       J <td>CG       Ø       Ø       P       P         (1)       1       A       Q       A       A         (2)       1       1       A       Q       A       A         (3)       1       2       B       R       D       r         (4)       #       3       C       S       c       S         (5)       \$       4       D       T       d       t         (6)       \$       5       E       U       e       u         (7)       \$       6       F       U       f       u         (7)       \$       6       F       U       f       u         (7)       \$       6       F       U       f       u         (1)       \$       9       I       Y       I       y         (2)       Y       9       I       Y       I       y         (3)       *       I       J       Z       J       Z       J       Z         (4)       +       I       K       I       K       I       I       I         (6)       -       =&lt;</td> <td><math display="block">\begin{array}{c ccc} CG &amp; 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U &amp; F &amp; U &amp; 9 &amp; 7 &amp; 7 \\ \hline (8) &amp; \$ &amp; C &amp; S &amp; E &amp; U &amp; e &amp; u &amp; s &amp; 7 \\ \hline (7) &amp; \$ &amp; 6 &amp; F &amp; U &amp; F &amp; U &amp; 9 &amp; 7 &amp; 7 \\ \hline (3) &amp; \$ &amp; * &amp; i &amp; J &amp; Z &amp; j &amp; Z &amp; T &amp; 7 \\ \hline (4) &amp; \$ &amp; + &amp; j &amp; K &amp; C &amp; k &amp; ( &amp; f &amp; 7 &amp; 7 \\ \hline (4) &amp; \$ &amp; + &amp; j &amp; K &amp; C &amp; k &amp; ( &amp; f &amp; 7 &amp; 7 \\ \hline (6) &amp; \$ &amp; - &amp; = M &amp; J &amp; m &amp; 3 &amp; n &amp; 7 &amp; 7 \\ \hline (6) &amp; \$ &amp; - &amp; = M &amp; J &amp; m &amp; 3 &amp; n &amp; 7 &amp; 7 \\ \hline (7) &amp; \$ &amp; S &amp; N &amp; \cap n &amp; \$ &amp; 3 &amp; 7 \\ \hline (7) &amp; \$ &amp; S &amp; N &amp; \cap n &amp; \$ &amp; 3 &amp; 7 \\ \hline (7) &amp; \$ &amp; S &amp; N &amp; \cap n &amp; \$ &amp; 3 &amp; 7 \\ \hline (7) &amp; \$ &amp; S &amp; N &amp; \cap n &amp; \$ &amp; 3 &amp; 7 \\ \hline (7) &amp; \$ &amp; S &amp; N &amp; \cap n &amp; \$ &amp; 3 &amp; 7 \\ \hline (7) &amp; \$ &amp; S &amp; N &amp; \cap n &amp; \$ &amp; 3 &amp; 7 \\ \hline (7) &amp; \$ &amp; S &amp; N &amp; \cap n &amp; \$ &amp; 3 &amp; 7 \\ \hline (7) &amp; \$ &amp; S &amp; N &amp; \cap n &amp; \$ &amp; 3 &amp; 7 \\ \hline (7) &amp; \$ &amp; S &amp; N &amp; \cap n &amp; \$ &amp; 3 &amp; 7 \\ \hline (7) &amp; \$ &amp; S &amp; N &amp; \cap n &amp; \$ &amp; 3 &amp; 7 \\ \hline (7) &amp; \$ &amp; S &amp; N &amp; \cap n &amp; \$ &amp; 3 &amp; 7 \\ \hline (7) &amp; \$ &amp; S &amp; N &amp; \cap n &amp; \$ &amp; 3 &amp; 7 \\ \hline (7) &amp; \$ &amp; S &amp; N &amp; \cap n &amp; \$ &amp; 3 &amp; 7 \\ \hline (7) &amp; \$ &amp; S &amp; N &amp; \cap n &amp; \$ &amp; 3 &amp; 7 \\ \hline (7) &amp; \$ &amp; S &amp; N &amp; \cap n &amp; \$ &amp; 3 &amp; 7 \\ \hline (7) &amp; \$ &amp; S &amp; N &amp; \cap n &amp; \$ &amp; 3 &amp; 7 \\ \hline (7) &amp; \$ &amp; S &amp; N &amp; \cap n &amp; \$ &amp; 3 &amp; 7 \\ \hline (7) &amp; \$ &amp; S &amp; N &amp; \cap n &amp; \$ &amp; 3 &amp; 7 \\ \hline (7) &amp; \$ &amp; S &amp; N &amp; \cap n &amp; \$ &amp; 3 &amp; 7 \\ \hline (7) &amp; \$ &amp; S &amp; N &amp; \cap n &amp; \$ &amp; 3 &amp; 7 \\ \hline (7) &amp; \$ &amp; S &amp; N &amp; \cap n &amp; \$ &amp; 3 &amp; 7 \\ \hline (7) &amp; \$ &amp; S &amp; N &amp; \cap n &amp; \$ &amp; 3 &amp; 7 \\ \hline (7) &amp; \$ &amp; S \\ \hline (7) &amp; \$ &amp; S &amp; S</math></td> <td><math display="block">\begin{array}{c c c c c c c c c c c c c c c c c c c </math></td> <td><math display="block">\begin{array}{c ccccccccccccccccccccccccccccccccccc</math></td> <td><math display="block">\begin{array}{c ccccccccccccccccccccccccccccccccccc</math></td>	CG       Ø       Ø       P       P         (1)       1       A       Q       A       A         (2)       1       1       A       Q       A       A         (3)       1       2       B       R       D       r         (4)       #       3       C       S       c       S         (5)       \$       4       D       T       d       t         (6)       \$       5       E       U       e       u         (7)       \$       6       F       U       f       u         (7)       \$       6       F       U       f       u         (7)       \$       6       F       U       f       u         (1)       \$       9       I       Y       I       y         (2)       Y       9       I       Y       I       y         (3)       *       I       J       Z       J       Z       J       Z         (4)       +       I       K       I       K       I       I       I         (6)       -       =<	$\begin{array}{c ccc} CG & Q & Q & Q & P & P \\ \hline (1) & 1 & Q & Q & Q & Q & Q \\ \hline (2) & 1 & 1 & Q & Q & Q & Q & Q \\ \hline (3) & 2 & 2 & 8 & P & P & P & P & P & P & P & P & P$	$\begin{array}{c ccc}     CG & 0 & 0 & 0 & P & P & \\ \hline (1) & 1 & 1 & 0 & 0 & 0 & P & P & \\ \hline (2) & 2 & B & B & F & F & 7 & 7 \\ \hline (3) & 2 & B & B & F & F & 7 & 7 \\ \hline (4) & # & 3 & C & S & c & s & 7 & 7 \\ \hline (5) & $ & 4 & D & T & d & t & s & T \\ \hline (6) & $ & 4 & D & T & d & t & s & T \\ \hline (7) & $ & 6 & F & U & F & U & 9 & 7 & 7 \\ \hline (7) & $ & 6 & F & U & F & U & 9 & 7 & 7 \\ \hline (7) & $ & 6 & F & U & F & U & 9 & 7 & 7 \\ \hline (8) & $ & C & S & E & U & e & u & s & 7 \\ \hline (7) & $ & 6 & F & U & F & U & 9 & 7 & 7 \\ \hline (3) & $ & * & i & J & Z & j & Z & T & 7 \\ \hline (4) & $ & + & j & K & C & k & ( & f & 7 & 7 \\ \hline (4) & $ & + & j & K & C & k & ( & f & 7 & 7 \\ \hline (6) & $ & - & = M & J & m & 3 & n & 7 & 7 \\ \hline (6) & $ & - & = M & J & m & 3 & n & 7 & 7 \\ \hline (7) & $ & S & N & \cap n & $ & 3 & 7 \\ \hline (7) & $ & S & S & S & S & S & S & S \\ \hline (7) & $ & S & S & S & S & S & S & S & S & S$	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$

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### **10.BACK LIGHT CHARACTERISTICS**

LCD Module with bottom LED Backlight **ELECTRICAL RATINGS** 

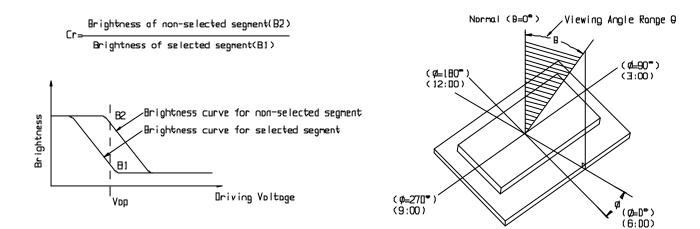
 $Ta = 25^{\circ}C$ 

Item	Symbol	Condition	Min	Тур	Max	Unit
Forward Voltage	VF	IF=100mA	3.8	4.0	4.2	V
Reverse Current	IR	VR=8 V		1000		uA
Luminous Intensity (With LCD dots off)	IV	IF=100mA		101		$Cd/m^2$
Wave length	??	IF=100mA		568		nm
Color	Yellow-Green					

### 11. ELECTRO-OPTICAL CHARACTERISTICS

 $(V_{OP} = 4.8V, Ta = 25^{\circ}C)$ 

Item	Symbo l	Condition	Min	Тур	Max	Unit
Operating Voltage		$Ta = -20^{\circ}C$	4.5	4.7	4.9	
	Vop	$Ta = 25^{\circ}C$	4.0	4.2	4.4	V ms ms
		$Ta = 70^{\circ}C$	3.5	3.7	3.9	
Response time	Tr	$Ta = 25^{\circ}C$		185		
	Tf	1a - 23 C		200		
Contrast	Cr	$Ta = 25^{\circ}C$		4.2		
Viewing angle range	θ	Cr 2	-40		+40	deg
		CI Z	-40		+40	deg



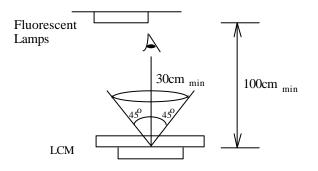
10

### **12.QUALITY SPECIFICATIONS**

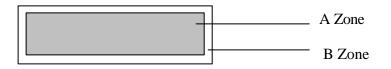
#### 12.1 Standard of the product appearance test

Manner of appearance test: The inspection should be performed in using 20W x 2 fluorescent lamps. Distance between LCM and fluorescent lamps should be 100 cm or more. Distance between LCM and inspector eyes should be 30 cm or more.

Viewing direction for inspection is 45° from vertical against LCM.



Definition of zone:



A Zone: Active display area (minimum viewing area). B Zone: Non-active display area (outside viewing area).

#### 12.2 Specification of quality assurance

AQL inspection standard

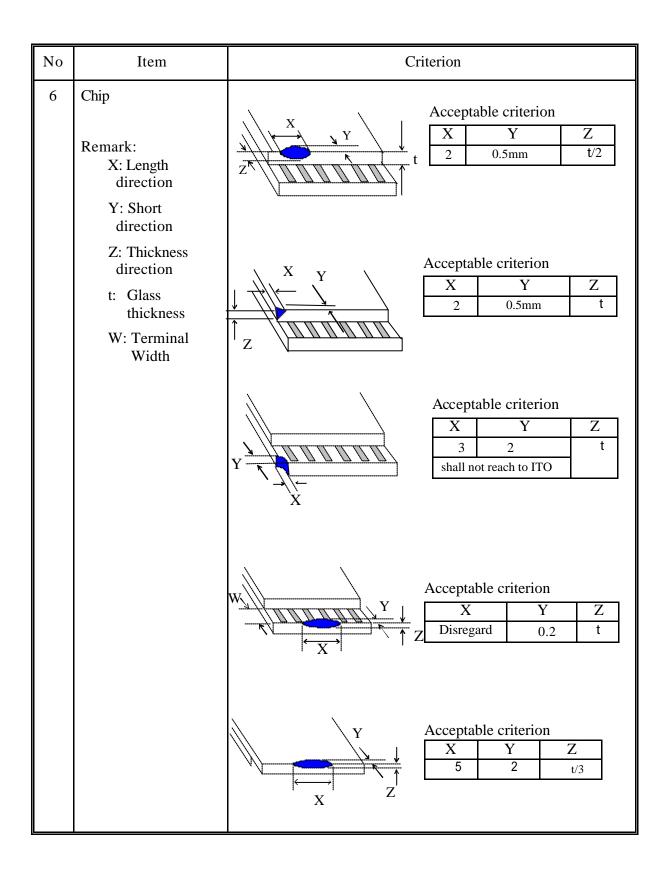
Sampling method: MIL-STD-105E, Level II, single sampling

#### Defect classification (Note: \* is not including)

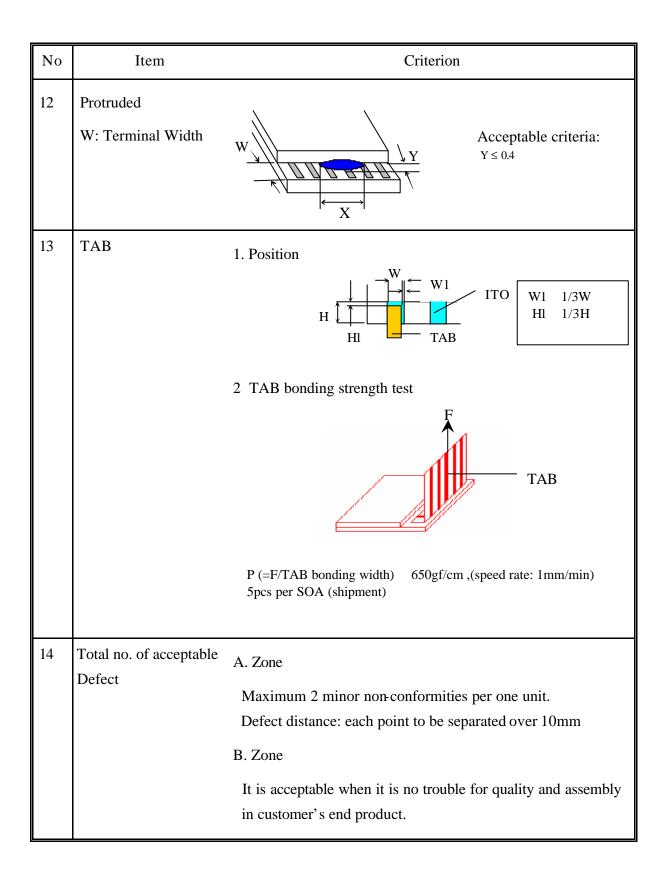
Classify		Item	Note	AQL
Major	Display state	Short or open circuit	1	0.65
		LC leakage		
		Flickering		
		No display		
		Wrong viewing direction		
		Contrast defect (dim, ghost)	2	
		Back-light	1,8	
	Non-display	Flat cable or pin reverse	10	
		Wrong or missing component	11	
Minor	Display	Background color deviation	2	1.0
	state	Black spot and dust	3	
		Line defect, Scratch	4	
		Rainbow	5	
		Chip	6	
		Pin hole	7	
		Protruded	12	
	Polarizer	Bubble and foreign material	3	1
	Soldering	Poor connection	9	1
	Wire	Poor connection	10	1
	TAB	Position, Bonding strength	13	

#### Note on defect classification

No.	Item	Criterion		
1	Short or open circuit	Not allow		
	LC leakage			
	Flickering			
	No display			
	Wrong viewing direction			
	Wrong Back-light			
2	Contrast defect	Refer to approval sample		
	Background color deviation			
3	Point defect, Black spot, dust	Y	Point Size	Acceptable Qty.
	(including Polarizer)	X	φ <u>&lt;</u> 0.10 0.10<φ 0.20	Disregard 3
			0.10<¢ 0.20 0.20<¢ 0.25	2
	$\phi = (X+Y)/2$		0.25<¢ 0.30	1
			φ>0.30	0
		Unit : mm		
4	Line defect,			
	Scratch		Line	Acceptable Qty.
	Scratch			Disregard
			L 0.03 W	2
		2.0 1.0		1
				Applied as point defect
		Unit: mm		
5	Rainbow	Not more than two color changes across the viewing area.		



No.	Item	Criterion		
7	Segment pattern W = Segment width $\phi = (X+Y)/2$	(1) Pin hole $\phi < 0.10$ mm is acceptable.		
8	Back-light	'W' Unit: mm		
Ū	2 weie 2.gew	<ol> <li>The color of backlight should correspond its specification.</li> <li>Not allow flickering</li> </ol>		
9	Soldering	<ul> <li>(1) Not allow heavy dirty and solder ball on PCB.</li> <li>(The size of dirty refer to point and dust defect)</li> <li>(2) Over 50% of lead should be soldered on Land.</li> </ul>		
10	Wire	<ol> <li>(1) Copper wire should not be rusted</li> <li>(2) Not allow crack on copper wire connection.</li> <li>(3) Not allow reversing the position of the flat cable.</li> <li>(4) Not allow exposed copper wire inside the flat cable.</li> </ol>		
11*	РСВ	<ul><li>(1) Not allow screw rust or damage.</li><li>(2) Not allow missing or wrong putting of component.</li></ul>		



### **11.3 Reliability of LCM**

Reliability test condition:

Item	Condition	Time (hrs)	Assessment
High temp. Storage	80°C	48	
High temp. Operating	70°C	48	
Low temp. Storage	-30°C	48	No abnormalities
Low temp. Operating	-20°C	48	in functions
Humidity	40°C/90%RH	48	and appearance
Temp. Cycle	$0^{\circ}C \leftarrow 25^{\circ}C \rightarrow 50^{\circ}C$	10cycles	
	$(30 \min \leftarrow 5 \min \rightarrow 30 \min)$		

Recovery time should be 24 hours minimum. Moreover, functions, performance and appearance shall be free from remarkable deterioration within 50,000 hours under ordinary operating and storage conditions room temperature ( $20\pm8^{\circ}$ C), normal humidity (below 65% RH), and in the area not exposed to direct sun light.

### 12.4 Precaution for using LCD/ICM

LCD/LCM is assembled and adjusted with a high degree of precision. Do not attempt to make any alteration or modification. The followings should be noted.

#### **General Precautions:**

- 1. LCD panel is made of glass. Avoid excessive mechanical shock or applying strong pressure onto the surface of display area.
- 2. The polarizer used on the display surface is easily scratched and damaged. Extreme care should be taken when handling. To clean dust or dirt off the display surface, wipe gently with cotton, or other soft material soaked with isoproply alcohol, ethyl alcohol or trichlorotriflorothane, do not use water, ketone or aromatics and never scrub hard.
- 3. Do not tamper in any way with the tabs on the metal frame.
- 4. Do not made any modification on the PCB without consulting Gemini.
- 5. When mounting a LCM, make sure that the PCB is not under any stress such as bending or twisting. Elastomer contacts are very delicate and missing pixels could result from slight

dislocation of any of the elements.

- 6. Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pixels and also cause rainbow on the display.
- 7. Be careful not to touch or swallow liquid crystal that might leak from a damaged cell. Any liquid crystal adheres to skin or clothes, wash it off immediately with soap and water.

#### **Static Electricity Precautions:**

- 1. CMOS-LSI is used for the module circuit; therefore operators should be grounded whenever he/she comes into contact with the module.
- 2. Do not touch any of the conductive parts such as the LSI pads; the copper leads on the PCB and the interface terminals with any parts of the human body.
- 3. Do not touch the connection terminals of the display with bare hand; it will cause disconnection or defective insulation of terminals.
- 4. The modules should be kept in anti-static bags or other containers resistant to static for storage.
- 5. Only properly grounded soldering irons should be used.
- 6. If an electric screwdriver is used, it should be grounded and shielded to prevent sparks.
- 7. The normal static prevention measures should be observed for work clothes and working benches.
- 8. Since dry air is inductive to static, a relative humidity of 50-60% is recommended.

#### **Soldering Precautions:**

- 1. Soldering should be performed only on the I/O terminals.
- 2. Use soldering irons with proper grounding and no leakage.
- 3. Soldering temperature:  $280^{\circ}C \pm 10^{\circ}C$
- 4. Soldering time: 3 to 4 second.
- 5. Use eutectic solder with resin flux filling.
- 6. If flux is used, the LCD surface should be protected to avoid spattering flux.
- 7. Flux residue should be removed.

#### **Operation Precautions:**

- 1. The viewing angle can be adjusted by varying the LCD driving voltage Vo.
- 2. Since applied DC voltage causes electro-chemical reactions, which deteriorate the display, the applied pulse waveform should be a symmetric waveform such that no DC component remains. Be sure to use the specified operating voltage.
- 3. Driving voltage should be kept within specified range; excess voltage will shorten display life.
- 4. Response time increases with decrease in temperature.
- 5. Display color may be affected at temperatures above its operational range.
- 6. Keep the temperature within the specified range usage and storage. Excessive temperature and humidity could cause polarization degradation, polarizer peel-off or generate bubbles.
- 7. For long-term storage over 40°C is required, the relative humidity should be kept below 60%, and avoid direct sunlight.

#### Limited Warranty

Gemini LCDs and modules are not consumer products, but may be incorporated by Gemini's customers into consumer products or components thereof, Gemini does not warrant that its LCDs and components are fit for any such particular purpose.

- 1. The liability of Gemini is limited to repair or replacement on the terms set forth below. Gemini will not be responsible for any subsequent or consequential events or injury or damage to any personnel or user including third party personnel and/or user. Unless otherwise agreed in writing between Gemini and the customer, Gemini will only replace or repair any of its LCD which is found defective electrically or visually when inspected in accordance with Gemini general LCD inspection standard . (Copies available on request)
- 2. No warranty can be granted if any of the precautions state in handling liquid crystal display above has been disregarded. Broken glass, scratches on polarizer mechanical damages as well as defects that are caused accelerated environment tests are excluded from warranty.
- 3. In returning the LCD/LCM, they must be properly packaged; there should be detailed description of the failures or defect.