

# SPECIFICATION FOR LCM MODULE

**MODULE NO.: ABC020004B02-YIY-R**  
**DOC.REVISION 01**

<b>AVD</b>	<b>PREPARED BY</b>	<b>CHECKED BY</b>	<b>APPROVED BY</b>
<b>SIGNATURE</b>			
<b>DATE</b>			

<b>CUSTOMER APPROVAL</b>	<b>SIGNATURE</b>	<b>DATE</b>

This Specification is subject to change without prior notice. Please contact SHENZHEN AV-DISPLAY CO.,LTD to confirm the latest revision.

If there is no special request from customer, AVD will not reserve the tooling of the product under the following conditions:

1. there is no response from customer in two years after AVD submit the samples;
2. there is no order in two years after the latest mass production and correlated data(include quality record) will be reserved one year more after tooling was discarded.

## **DOCUMENT REVISION HISTORY**

<b>Version</b>	<b>DATE</b>	<b>DESCRIPTION</b>	<b>CHANGED BY</b>
00	Jul-07-2006	First issue	---
01	Aug-17-2012	Update	JYL

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# 1. FUNCTIONS & FEATURES

- 1.1. Format : 20x4characters
- 1.2. LCD mode : STN / Positive/ Transmissive/ Y-G
- 1.3. Viewing direction : 6 O'clock
- 1.4. Driving scheme : 1/16 Duty , 1/5 Bias
- 1.5. Power supply voltage (V<sub>DD</sub>) : 5.0V
- 1.6. LCD driving voltage(V<sub>op</sub>) : 4.5V
- 1.7. Operation temp : -20~70°C
- 1.8. Storage temp : -30~80°C
- 1.9. Backlight color : Yellow-Green
- 1.10. RoHS standard

# 2. MECHANICAL SPECIFICATIONS

- 2.1. Module size : 98.0mm(L)\*60.0mm(W)\* Max14.0 mm(H)
- 2.2. Viewing area : 77.0mm(L)\*26.5mm(W)
- 2.3. Character pitch : 3.55mm(L)\*5.35mm(W)
- 2.4. Character size : 2.95mm(L)\*4.75mm(W)
- 2.5. Dot pitch : 0.60mm(L)\*0.60mm(W)
- 2.6. Dot size : 0.55mm(L)\*0.55mm(W)
- 2.7. Weight : Approx.

# 3. BLOCK DIAGRAM

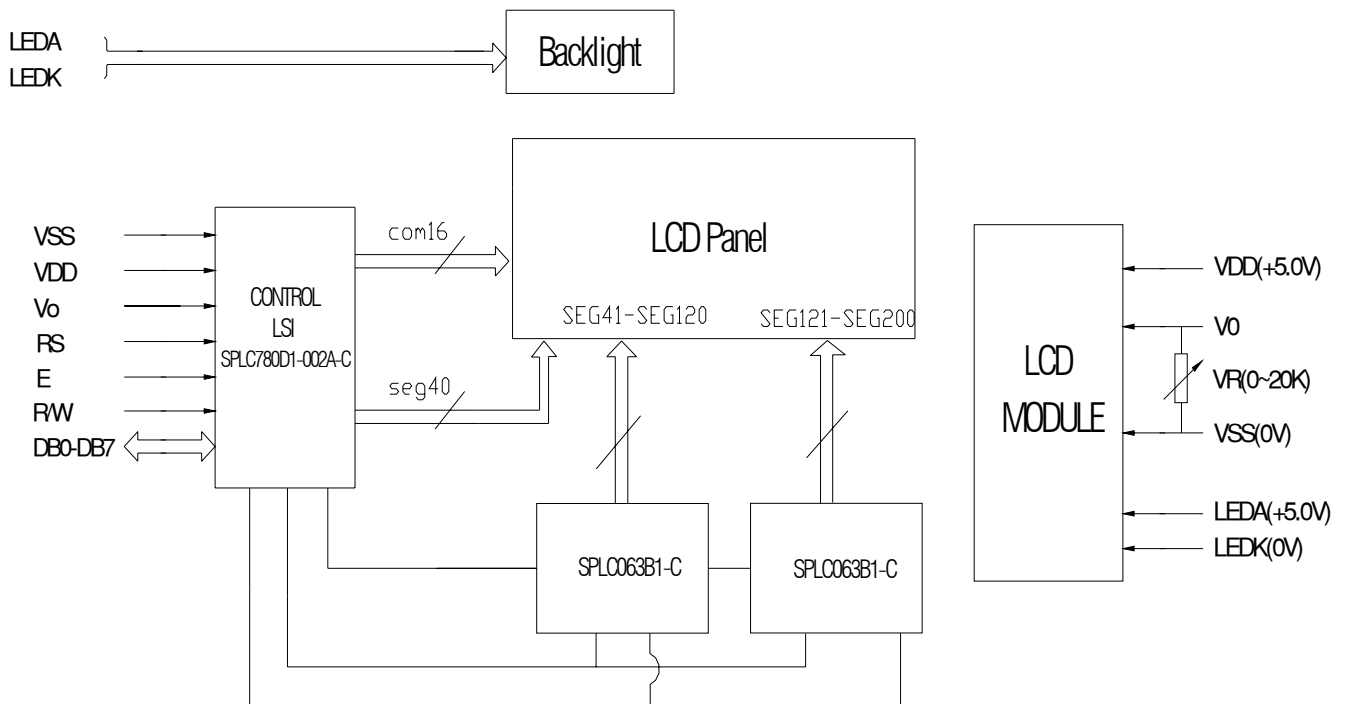


Figure 2. Block diagram

# 4. DIMENSIONAL OUTLINE

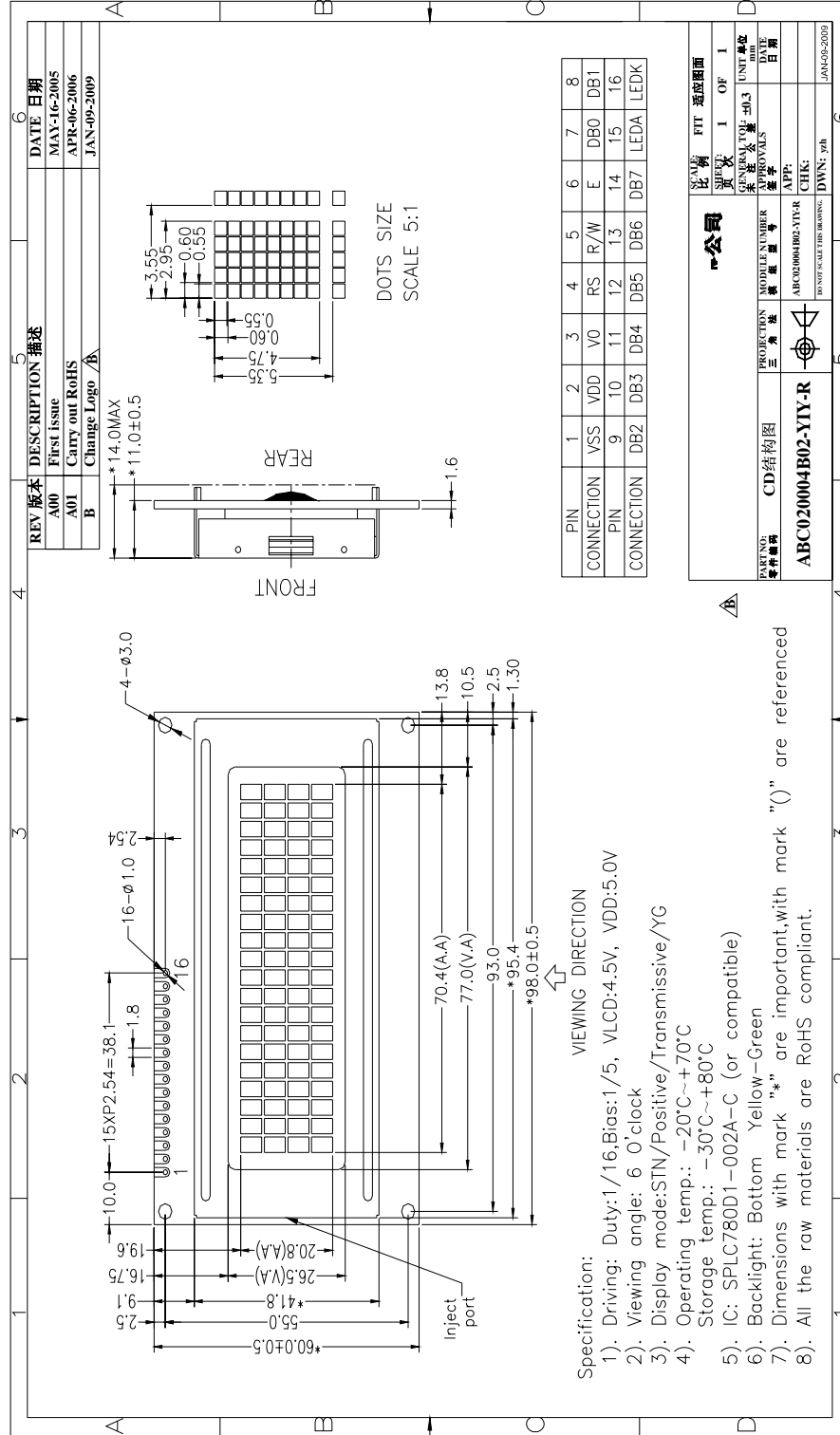


Figure 1. Dimensional outline

## **5. PIN DESCRIPTION**

No.	Symbol	Function
1	VSS	Power ground (0V)
2	VDD	Power supply for Logic(+5V)
3	V0	Power supply for LCD drive
4	RS	Register selection (H: Data register , L :Instruction register)
5	R/W	Read/write selection (H: Read , L: Write)
6	E	Enable signal.
7-14	DB0~DB7	Data Bus line
15	LEDA	Power supply for backlight(+5V)
16	LEDK	Power supply for backlight(0V)

## **6. MAXIMUM ABSOLUTE LIMIT**

Item	Symbol	MIN	MAX	Unit
Supply Voltage for Logic	V <sub>DD</sub>	-0.3	7.0	V
Supply Voltage for LCD	V <sub>0</sub>	V <sub>DD</sub> -10.0	V <sub>DD</sub> +0.3	V
Input Voltage	V <sub>in</sub>	-0.3	V <sub>DD</sub> +0.3	V
Supply Current for Backlight	I <sub>F</sub> (T <sub>a</sub> = 25°C)	---	216	mA
Reverse Voltage for Backlight	V <sub>R</sub> (T <sub>a</sub> = 25°C)	---	10	V
Operating Temperature	T <sub>op</sub>	-20	70	°C
Storage Temperature	T <sub>st</sub>	-30	80	°C

## 7. ELECTRICAL CHARACTERISTICS

### 7.1 DC characteristics (VDD=4.5V-5.5V, TA=25°C)

Characteristics	Symbol	Limit			Unit	Test Condition
		Min.	Typ.	Max.		
Operating Current	$I_{DD}$	-	0.55	0.8	mA	External clock (Note)
Input High Voltage	$V_{IH1}$	2.5	-	VDD	V	Pins:(E, RS, R/W, DB0 - DB7) VDD=5V
Input Low Voltage	$V_{L1}$	-0.3	-	0.6	V	
Input High Voltage	$V_{IH2}$	VDD-1	-	VDD	V	Pin OSC1
Input Low Voltage	$V_{L2}$	-0.2	-	1.0	V	Pin OSC1
Input High Current	$I_{IH}$	-2.0	-	2.0	$\mu$ A	Pins: (RS, R/W, DB0 - DB7) VDD = 5.0V
Input Low Current	$I_{IL}$	-20	-125	-250	$\mu$ A	
Output High Voltage (TTL)	$V_{OH1}$	2.4	-	VDD	V	$I_{OH} = -0.1$ mA Pins: DB0 - DB7
Output Low Voltage (TTL)	$V_{OL1}$	-	-	0.4	V	$I_{OL} = 0.1$ mA Pins: DB0 - DB7
Output High Voltage (CMOS)	$V_{OH2}$	0.9VDD	-	VDD	V	$I_{OH} = -40$ $\mu$ A, Pins: CL1, CL2, M, D
Output Low Voltage (CMOS)	$V_{OL2}$	-	-	0.1VDD	V	$I_{OL} = 40$ $\mu$ A, Pins: CL1, CL2, M, D
Driver ON Resistance (COM)	$R_{COM}$	-	-	20	K $\Omega$	$I_o = \pm 50$ $\mu$ A, $V_{LCD} = 4.0$ V Pins: COM1 - COM16
Driver ON Resistance (SEG)	$R_{SEG}$	-	-	30	K $\Omega$	$I_o = \pm 50$ $\mu$ A, $V_{LCD} = 4.0$ V Pins: SEG1 - SEG40
LCD Voltage	$V_{LCD}$	3.0	-	8	V	VDD-V5, 1/4 bias or 1/5 bias

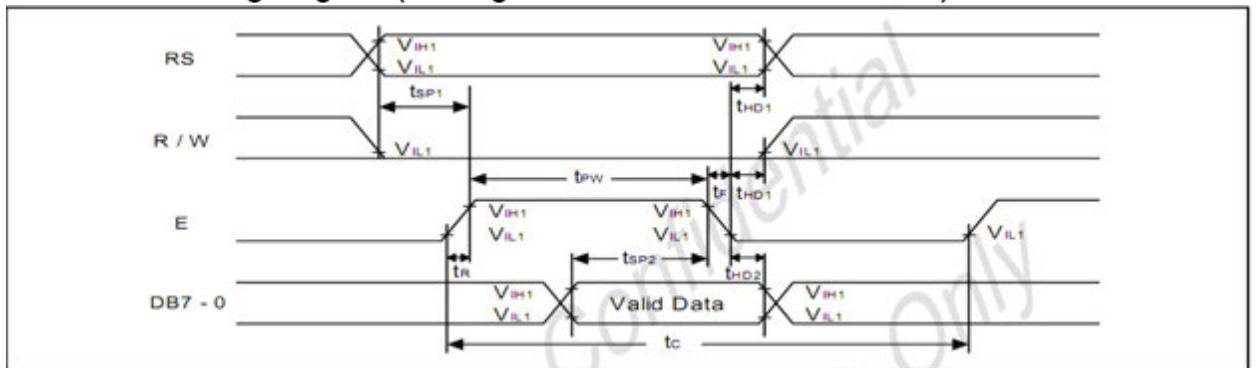
Note:  $F_{osc} = 250$ KHz, VDD = 5.0V, pin E = "L", RS, R/W, DB0 - DB7 are open, all outputs are no loads.

## 7.2 AC characteristics(VDD=4.5V-5.5V,TA=25°C)

### Write mode (Writing Data from MPU to SPLC780D1)

Characteristics	Symbol	Limit			Unit	Test Condition
		Min.	Typ.	Max.		
E Cycle Time	$t_c$	400	-	-	ns	Pin E
E Pulse Width	$t_{pw}$	150	-	-	ns	Pin E
E Rise/Fall Time	$t_{r, t_f}$	-	-	25	ns	Pin E
Address Setup Time	$t_{sp1}$	30	-	-	ns	Pins: RS, R/W, E
Address Hold Time	$t_{hd1}$	10	-	-	ns	Pins: RS, R/W, E
Data Setup Time	$t_{sp2}$	40	-	-	ns	Pins: DB0 - DB7
Data Hold Time	$t_{hd2}$	10	-	-	ns	Pins: DB0 - DB7

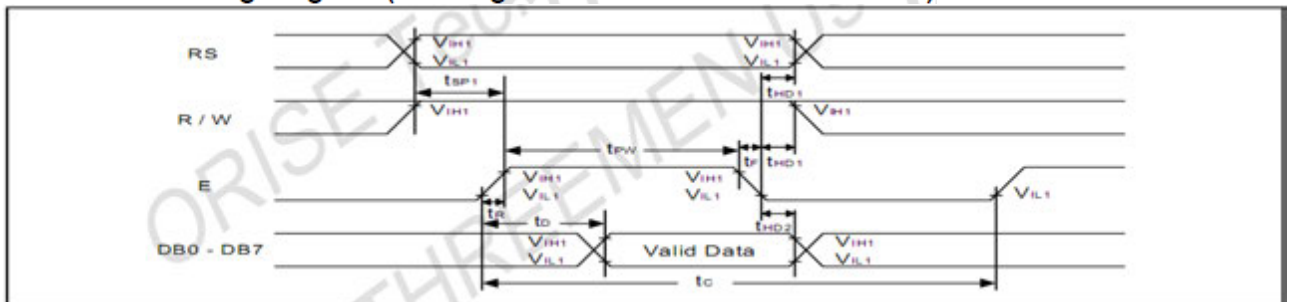
### Write mode timing diagram (Writing Data from MPU to SPLC780D1)



### Read mode (Reading Data from SPLC780D1 to MPU)

Characteristics	Symbol	Limit			Unit	Test Condition
		Min.	Typ.	Max.		
E Cycle Time	$t_c$	400	-	-	ns	Pin E
E Pulse Width	$t_w$	150	-	-	ns	Pin E
E Rise/Fall Time	$t_r, t_f$	-	-	25	ns	Pin E
Address Setup Time	$t_{sp1}$	30	-	-	ns	Pins: RS, R/W, E
Address Hold Time	$t_{hd1}$	10	-	-	ns	Pins: RS, R/W, E
Data Output Delay Time	$t_o$	-	-	100	ns	Pins: DB0 - DB7
Data hold time	$t_{hd2}$	5.0	-	-	ns	Pin DB0 - DB7

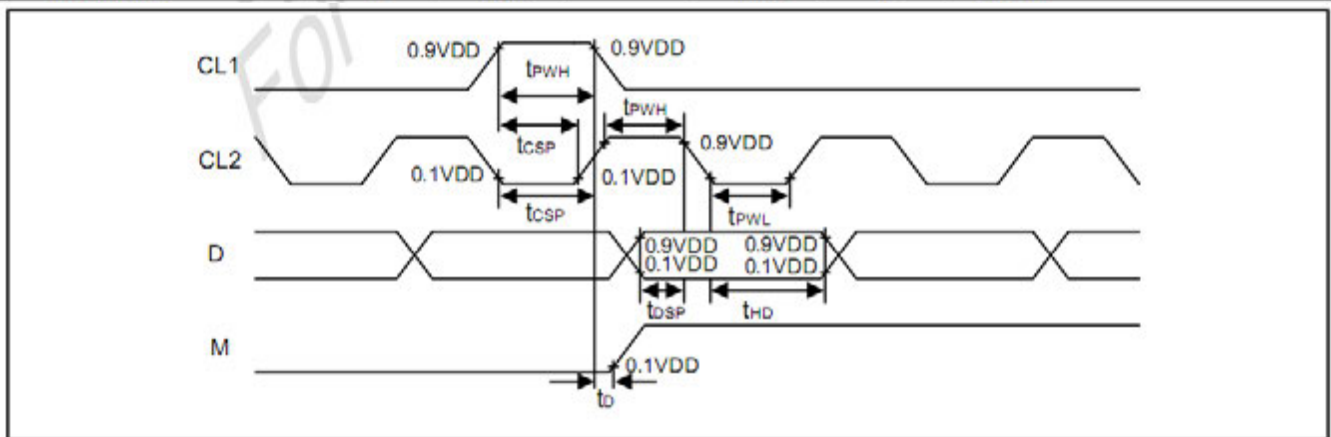
### Read mode timing diagram (Reading Data from SPLC780D1 to MPU)





## Interface mode with LCD Driver

Characteristics	Symbol	Limit			Unit	Test Condition
		Min.	Typ.	Max.		
Clock pulse width high	$t_{PWH}$	800	-	-	ns	Pins: CL1, CL2
Clock pulse width low	$t_{PWL}$	800	-	-	ns	Pins: CL1, CL2
Clock setup time	$t_{CSP}$	500	-	-	ns	Pins: CL1, CL2
Data setup time	$t_{DSP}$	300	-	-	ns	Pins: D
Data hold time	$t_{HD}$	300	-	-	ns	Pins: D
M delay time	$t_D$	-1000	-	1000	ns	Pins: M

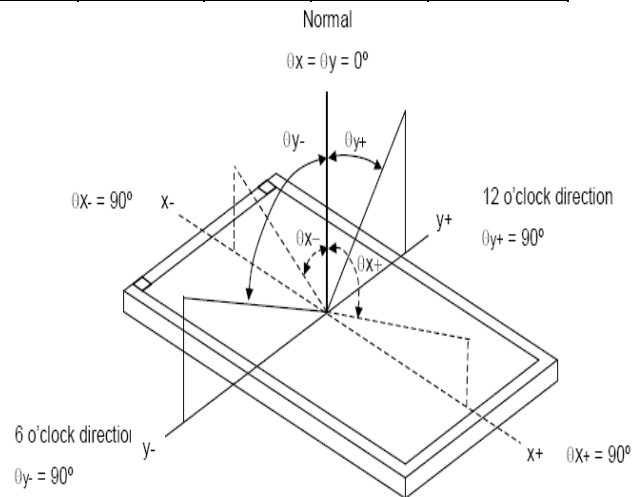
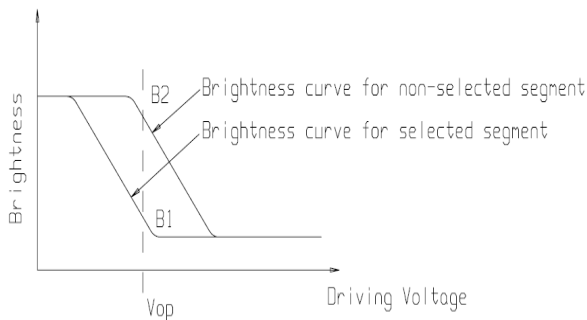


## 8. ELECTRO-OPTICAL CHARACTERISTICS

(  $V_{OP} = 4.5V$ ,  $T_a = 25^{\circ}C$  )

Item	Symbol	Condition	Min	Typ	Max	Unit
Operating Voltage	$V_{op}$	$T_a = -20^{\circ}C$	4.9	5.1	5.3	V
		$T_a = 25^{\circ}C$	4.2	4.5	4.8	
		$T_a = 70^{\circ}C$	3.8	4.0	4.2	
Response time	$T_r$	$T_a = 25^{\circ}C$	---	185	---	ms
	$T_f$		---	200	---	ms
Contrast	$Cr$	$T_a = 25^{\circ}C$	---	4	---	---
Viewing angle range	$\theta_x$	$Cr \geq 2$	-40	---	+40	deg
	$\theta_y$		-30	---	+45	deg

$$Cr = \frac{\text{Brightness of non-selected segment}(B2)}{\text{Brightness of selected segment}(B1)}$$



## 9. BACKLIGHT CHARACTERISTICS

LCD Module with Edge White LED Backlight  
ELECTRICAL RATINGS

$T_a = 25^{\circ}C$

Item	Symbol	Condition	Min	Typ	Max	Unit
Forward Current	$V_f$	$I_f = 180mA$	4.0	4.2	4.4	V
Reverse Current	$I_r$	$V_r = 10V$	---	---	180	$\mu A$
Luminous Intensity (Without LCD)	$L_v$	$I_f = 180mA$	120	160	230	$cd/m^2$
Colour Coordinates(Without LCD)	$\lambda_p$	$I_f = 180mA$	569	---	576	nm
Color	Yellow-Green					

Note:

when the temperature exceed  $25^{\circ}C$ , the approved current decrease rate for Backlight change as the temperature increase is:  $-0.36 \times 18mA/^{\circ}C$  (below  $25^{\circ}C$ , the current refer to constant, which would not change with temperature ).

## 10. CONTROL AND DISPLAY COMMAND

Instruction	Instruction Code										Description	Execution time (Temp = 25°C)		
	RS	RW	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0		Fosc= 190KHz	Fosc= 270KHz	Fosc= 350KHz
Clear Display	0	0	0	0	0	0	0	0	0	1	Write "20H" to DDRAM and set DDRAM address to "00H" from AC	2.16ms	1.52ms	1.18ms
Return Home	0	0	0	0	0	0	0	0	0	1	Set DDRAM address to "00H" from AC and return cursor to its original position if shifted. The contents of DDRAM are not changed.	2.16ms	1.52ms	1.18ms
Entry Mode Set	0	0	0	0	0	0	0	1	I/D	S	Assign cursor moving direction and enable the shift of entire display	53µs	38µs	29µs
Display ON/OFF Control	0	0	0	0	0	0	1	D	C	B	Set display (D), cursor(C), and blinking of cursor(B) on/off control bit.	53µs	38µs	29µs
Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	-	-	Set cursor moving and display shift control bit, and the direction, without changing of DDRAM data.	53µs	38µs	29µs
Function Set	0	0	0	0	1	DL	N	F	-	-	Set interface data length (DL: 8-bit/4-bit), numbers of display line (N: 2-line/1-line) and, display font type (F:5x10 dots/5x8 dots)	53µs	38µs	29µs
Set CGRAM Address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	Set CGRAM address in address counter.	53µs	38µs	29µs
Set DDRAM Address	0	0	1	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Set DDRAM address in address counter	53µs	38µs	29µs
Read Busy Flag and Address Counter	0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.			
Write Data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM/CGRAM).	53µs	38µs	29µs
Read Data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM/CGRAM).	53µs	38µs	29µs

Note1: "-": don't care

Note2: In the operation condition under -20°C ~ 75°C, the maximum execution time for majority of instruction sets is 100µs, except two instructions, "Clear Display" and "Return Home", in which maximum execution time can take up to 4.1ms.

# 11. Font Characteristic

Upper 4 bit Lower 4 bit	LLLL	LLLH	LLHL	LLHH	LHLL	LHLH	LHHL	LHHH	HLLL	HLLH	HLHL	HLHH	HHLL	HHLH	HHHL	HHHH
LLLL	☐	☐	☐	⊗	⊗	⊗	⊗	⊗	☐	☐	⊗	⊗	⊗	⊗	⊗	⊗
LLLH	☐	☐	!	!	!	!	!	!	☐	☐	!	!	!	!	!	!
LLHL	☐	☐	"	"	"	"	"	"	☐	☐	"	"	"	"	"	"
LLHH	☐	☐	#	#	#	#	#	#	☐	☐	#	#	#	#	#	#
LHLL	☐	☐	\$	\$	\$	\$	\$	\$	☐	☐	\$	\$	\$	\$	\$	\$
LHLH	☐	☐	%	%	%	%	%	%	☐	☐	%	%	%	%	%	%
LHHL	☐	☐	&	&	&	&	&	&	☐	☐	&	&	&	&	&	&
LHHH	☐	☐	'	'	'	'	'	'	☐	☐	'	'	'	'	'	'
HLLL	☐	☐	(	(	(	(	(	(	☐	☐	(	(	(	(	(	(
HLLH	☐	☐	)	)	)	)	)	)	☐	☐	)	)	)	)	)	)
HLHL	☐	☐	*	*	*	*	*	*	☐	☐	*	*	*	*	*	*
HLHH	☐	☐	+	+	+	+	+	+	☐	☐	+	+	+	+	+	+
HHLL	☐	☐	,	,	,	,	,	,	☐	☐	,	,	,	,	,	,
HHLH	☐	☐	=	=	=	=	=	=	☐	☐	=	=	=	=	=	=
HHHL	☐	☐	.	.	.	.	.	.	☐	☐	.	.	.	.	.	.
HHHH	☐	☐	/	/	/	/	/	/	☐	☐	/	/	/	/	/	■

## **12. PRECAUTION FOR USING LCD/LCM**

LCD/LCM is assembled and adjusted with a high degree of precision. Do not attempt to make any alteration or modification. The followings should be noted.

### **General Precautions:**

1. LCD panel is made of glass. Avoid excessive mechanical shock or applying strong pressure onto the surface of display area.
2. The polarizer used on the display surface is easily scratched and damaged. Extreme care should be taken when handling. To clean dust or dirt off the display surface, wipe gently with cotton, or other soft material soaked with isopropyl alcohol, ethyl alcohol or trichlorotrifluoroethane, do not use water, ketone or aromatics and never scrub hard.
3. Do not tamper in any way with the tabs on the metal frame.
4. Do not make any modification on the PCB without consulting AVD.
5. When mounting a LCM, make sure that the PCB is not under any stress such as bending or twisting. Elastomer contacts are very delicate and missing pixels could result from slight dislocation of any of the elements.
6. Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pixels and also cause rainbow on the display.
7. Be careful not to touch or swallow liquid crystal that might leak from a damaged cell. Any liquid crystal adheres to skin or clothes, wash it off immediately with soap and water.

### **Static Electricity Precautions:**

1. CMOS-LSI is used for the module circuit; therefore operators should be grounded whenever he/she comes into contact with the module.
2. Do not touch any of the conductive parts such as the LSI pads; the copper leads on the PCB and the interface terminals with any parts of the human body.
3. Do not touch the connection terminals of the display with bare hand; it will cause disconnection or defective insulation of terminals.
4. The modules should be kept in anti-static bags or other containers resistant to static for storage.
5. Only properly grounded soldering irons should be used.
6. If an electric screwdriver is used, it should be grounded and shielded to prevent sparks.
7. The normal static prevention measures should be observed for work clothes and working benches.
8. Since dry air is inductive to static, a relative humidity of 50-60% is recommended.

### **Soldering Precautions:**

1. Soldering should be performed only on the I/O terminals.
2. Use soldering irons with proper grounding and no leakage.
3. Soldering temperature: 270~300°C
4. Soldering time: 2 to 3 second.
5. Use eutectic solder with resin flux filling.



## **13. LCM test criteria**

### **1. Objective**

The LCM test criteria are set to formalize AVD's LCM quality standards with reference to those of the customer for inspection, release and acceptance of finished LCM products in order to guarantee the quality required by the customer.

### **2. Scope**

The criteria are applicable to all the LCM products manufactured by AVD, either supplied alone or embedded in or integrated with other components.

### **3. Inspection Apparatuses**

Function testers, vernier calipers, microscopes, magnifiers, ESD wrist straps, finger cots, labels, ovens for high-low temperature tests, refrigerators, constant voltage power supply (DC) , desk lamps, etc.

### **4. Sampling Plan and Reference Standards**

4.1.1 According to GB/T 2828.1---2003/ISO2859-1:1999, single sampling under formal inspection and the general inspection level II applies.

Item of Inspection	Sampling Rate	AQL Assessment
Cosmetic	II Single Sampling	MA=0.4 MI=1.5
Mechanical	N=3	C=0
Functional	II Single Sampling	MA=0.4 MI=1.5

4.1.2 GB/T 2828.1---2003/ISO2859-1:1999, sampling procedures and tables for batch inspection.

4.1.3 GB/T 1619.96 Test Methods for TN LCD.

4.1.4 GB/T 12848.91 General Specifications for STN LCD.

4.1.5 GB2421-89 Basic Environmental Test Procedures for Electrical and Electronic Products

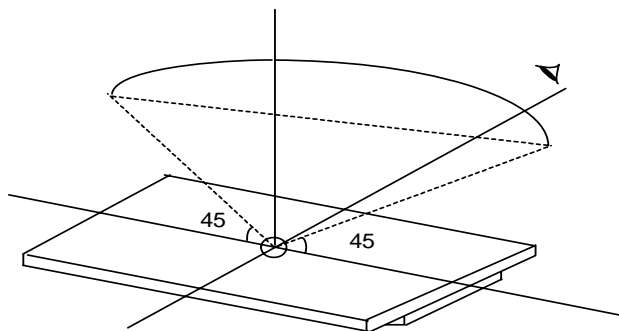
4.1.6 IPC-A-610C Acceptance Condition for Electrical Assemblies.

### **5. Inspection Conditions and Inspection Reference**

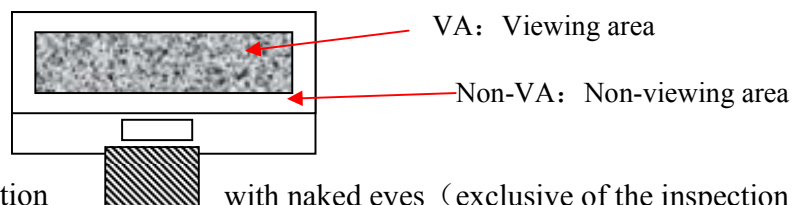
5.1 Cosmetic inspection: shall be done normally at  $25\pm 5^{\circ}\text{C}$  of the ambient temperature and  $45\pm 20\%\text{RH}$  of relative humidity, under the ambient luminance greater than  $300\text{cd}/\text{cm}^2$  and at the distance of 30cm apart between the inspector's eyes and the LCD panel and normally in reflected light. For back-lit LCMs, cosmetic

inspection shall be done under the ambient luminance less than 100cd/cm<sup>2</sup> with the backlight on.

- 5.2 The LCM shall be tested at the angle of 45°, left and right, and 0-45°, top and bottom (for STN LCM, at 20° -55°) .



- 5.3 Definition of VA



- 5.4 Inspection with naked eyes (exclusive of the inspection of the physical dimensions of defects carried out with magnifiers) .

- 5.5 Electrical properties

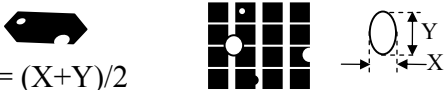
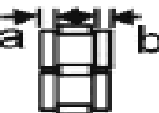
Inspection with the test jigs against the product specifications or drawings; display contents and parameters shall conform to those of the product specifications and the display effect to the sample.

- 5.5.1 Test voltage (V) :

- 5.5.1.1 (Determined) according to the operating instruction of test jigs assuming the external circuit can be adjusted unless the customer otherwise specifies driving voltage(s). (Display) effects are controlled within the specified range of voltage variation (If no specific requirements, display effects are controlled at  $V_{op} = 9V$  or  $V_{op} \pm 0.3V$  when  $V_{op}$  is below 9V; if  $V_{op}$  is above 9V, display effects are controlled at  $V_{op} \pm 0.3\%$  at least). For display products with the customer-specified fixed  $V_{op}$ , display effects are





6.2.10	PI black/white spots	Partial black and white spots visible when changing display contents due to defective PI layer.		√	refer to the spot/line criteria for the visible spots when display image remains still; others OK.
6.2.11	pinhole/white spots	Deformed patterns appearing when LCD is turned on caused by missing ITO.  $d = (X+Y)/2$		√	refer to spot/line standard
6.2.12	Pattern distortion	Segment is either wider, narrower or deformed than the specified, caused by panel misalignment, resulting in unwanted heave(s) or missing: $ Ia-Ib  \leq 1/4W$ (W is the normal width) 		√	Acceptable $ Ia-Ib  > 1/4W$ , rejected
6.2.13	High current	LCM current is larger than the designed value.		√	Rejected

### 6.3 LCD Visual Defects

#### 6.3.1 Spot defect (defined within VA, spots out of VA do not count.)

Defect	Average diameter (d)	Acceptable quantity	MAJ	MIN
Spot defect (black spot, foreign matter, nick, scratches, including LC mis-orientation.)	$d \leq 0.2$	3		√
	$0.2 < d \leq 0.25$	2		
	$0.25 < d \leq 0.30$	1		

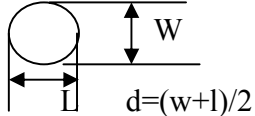
#### 6.3.2 Line defects (defined within VA; those out of VA do not count.)

Defect	Length(L)	Width(W)	Acceptable quantity	MAJ	MIN
line defects (scratches, linear foreign matter) 	$\leq 5.0$	$\leq 0.02$	3		√
	$\leq 3.0$	$\leq 0.03$	3		
	$\leq 3.0$	$\leq 0.05$	1		

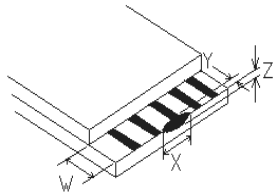
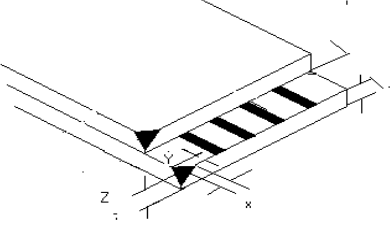
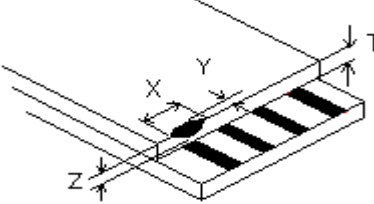
note: 1.If the width is bigger than 0.1mm, it shall be treated as spot defect.

#### 6.3.3 Polarizer air bubble (defined within VA; those out of VA do not count.)

Defect	Average diameter (d)	Acceptable quantity	MAJ	MIN
Polarizer air bubble, Concave-Convex dot.	$d \leq 0.3$	3		√
	$0.3 < d \leq 0.5$	2		
	$0.5 < d \leq 0.8$	1		



6.3.4 Glass Damages (for LCMs without bezels and whose LCD edges exposed and for LCMs with bezels, including COG and H/S LCMs)

No.	Item	Acceptance Standard	MAJ	MIN	
6.3.4.1		(mm)		√	
		X			$\leq 1/8L$
		Y			$\leq 1/3W$
		Z			$\leq 1/2t$
		Acceptable quantity			2
		When $Y \leq 0.2\text{mm}$ , the length of X doesn't count; for chip neither on lead nor through, when $X \leq 1/10L$ , $Y \leq 1/2W$ max, it doesn't count.			
6.3.4.2		(mm)	MAJ	MIN	
		X	Not entering into frame and not contacting with conductive position	√	
		Y			
		Z			$\leq t$
		Acceptable Quantity	2		
		For chips on corners, refer to 6.3.4.3 and they must be out of the frame epoxy. For chips on lead, refer to 6.3.4.1			
6.3.4.3		(mm)	MAJ		MIN
		X	$\leq 1/8 L$	√	
		Y	$\leq 1/2H$		
		z	$\leq 1/2t$		
		Accept numbers	2		
		The standard for inner chip on sealed area is the same as the standard for outer. For chip on the reverse of ITO contact pad ledge, refer to 6.3.4.1 for chip on the reverse of ITO contact pad ledge for the value of Y.			

Notes: T=Glass Thickness, L=Length, H=Distance between LCD Edge to Inside Edge of LCD Frame Epoxy and W=Width of Contact Pad Ledge.

#### 6.3.5 Others

No.	Item	Description	MAJ	MIN	Acceptance standard
6.3.5.1	Newton Ring /Background color	More than one color on the same LCD panel or different colors on different LCDs from the same batch.		√	Reject or refer to limit sample
6.3.5.2	LC Leakage	/	√		Rejected
6.3.5.3	Missing protective film	/		√	Rejected

#### 6.4 Backlight

No.	Item	Description	MAJ	MIN	Accept standard
6.4.1	Backlight not working, wrong color	/	√		Rejected
6.4.2	Color deviation	When powered on, the LCD color differs from that of the sample and is found after testing not conforming to the drawing.		√	Refer to sample and drawing
6.4.3	Brightness deviation	When powered on, the LCD brightness differs from that of the sample and is found after testing not conforming to the drawing; or if conforming to the drawing but over±30%.		√	Refer to sample and drawing
6.4.4	Uneven brightness	When powered on, the LCD brightness is uneven on the same LCD and out of the specification of the drawing.		√	Refer to sample and drawing
6.4.5	Spot/line scratch	Appearance of spot or line scratches on the LCD when turned on.		√	Refer to 6.3.1/6.3.2

#### 6.5 Metal frame (Metal Bezel)

No.	Item	Description	MAJ	MIN	Accept standard
6.5.1	Material/surface treatment	Metal frame/surface treatment do not conform to the specifications.	√		Rejected
6.5.2	Tab twist inconformity/ Tab not twisted	Wrong twist method or direction and twist tabs are not twisted as required.	√		Rejected

6.5.3	Oxidization, chapped paint, discoloration, dents, and scratches	Oxidation on the surface of the metal bezel; the quantity of spot defect (chapped front surface paint and substrate-exposing scratches) $\leq 0.8\text{mm}$ exceeds 3; the quantity of linear defects with the length $\leq 5.0\text{mm}$ and width $\leq 0.05\text{mm}$ exceeds 2; the quantity of spot defect (front dent, bubble, side surface chapped paint and substrate-exposing scratches) $\leq 1.0\text{mm}$ exceeds 3; the quantity of linear defects with the width $\leq 0.05\text{mm}$ exceeds 3.		√	Rejected
6.5.4	Burr	Burr(s) on metal bezel is so long as to get into viewing area.		√	Rejected

#### 6.6 PCB/COB

No.	Item	Description	MAJ	MIN	Accept standard
6.6.1	Improper Epoxy Cover	<ol style="list-style-type: none"> <li>1. Contacts exposure within the white circle for COB chip bonding.</li> <li>2. The height of epoxy cover is out of the product specifications and drawing.</li> <li>3. The epoxy cover over the COB chip exceeds the circle by more than 2mm in diameter, which is the maximum distance the epoxy cover is allowed to exceed the circle.</li> <li>4. Existence of obvious linear mark(s) or chip-exposing pinhole on the epoxy cover.</li> <li>5. The pinhole diameter on the epoxy over exceeds 0.25mm and there is foreign matter in the pinhole.</li> </ol>		√	Rejected
6.6.2	PCB cosmetic defect	<ol style="list-style-type: none"> <li>1. Oxidized or contaminated gold fingers on PCB.</li> <li>2. Bubbles on PCB after reflow-soldering.</li> <li>3. Exposure of conductive copper foil caused by chapped or scratched solder-resist coating.</li> </ol> <p>For the conductive area of PCB repaired with the solder resist coating material, the diameter <math>\psi</math> of the repaired area on the circuit must not exceed 1.3mm while for the non-conductive area of PCB repaired with the solder resist coating material, the diameter <math>\psi</math> must not exceed</p>		√	Rejected



No.	Item	Description	MAJ	MIN	Acceptance standard
6.8.1	H/S or FPC out of specification		√		Rejected
6.8.2	Dimensions /position	Dimensions of heat compression material should be within the specification of the drawing and the contact of conductive material should cover more than 1/2 of ITO, PDA, etc and the dimensions of dislocation shall be within the tolerance.		√	Acceptable
6.8.3	Foreign Matter in Heat pressure area	If foreign matter in conductive heat compression area shall not cause short, it is OK. If foreign matter in non-conductive heat compression area does not exceed 50% of the heat pressure area, it is OK.		√	Acceptable
6.8.4	Folds			√	Refer to limited sample

#### 6.9 Connectors and other components

No.	Item	Description	MAJ	MIN	Accept standard
6.9.1	Out of Specification	The specification of connector and other components do not conform to the drawing.	√		Rejected
6.9.2	Position and order	Solder position and Pin# 1 should be in the positions specified by the drawing.		√	Rejected
6.9.3	Cosmetic	1. Flux on PCB components and pins. 2. The pin width of a PIN connector exceeds 1/2 of the specified pin width.		√	Rejected

#### 6.10 General Appearance

No.	Item	Description	MAJ	MIN	Accept standard
6.10.1	Connection material	1. Damaged or contaminated FPC or H/S gold fingers or FFC contacts. 2. Sharp folds on FPC, FFC, COF, H/S (unless designed for). 3. Solder paste larger than 2/3 of pin width on the gold finger of FPC and PCB. 4. Pierced or folded FPC/FFC exceeding limit sample.		√	Rejected

6.10.2	Defective stiffener	Stiffener not fully covering H/S, FFC, or FPC and or not pasted on or glued on output pins.		√	Rejected
6.10.3	Surface dirt	Smears, residual glue, finger prints, etc. on the surface of finished LCMs; solder spatters or solder balls on non-soldered area of PCB/COB. Non-removed defect mark or label on LCMs.		√	Rejected
6.10.4	Assembly black spot	Smears or black spots found on LCMs after backlight or diffuser are assembled.		√	Refer to 6.3.1
6.10.5	Product mark	Missing, unclear, incorrect, or misplaced part numbers and/or batch marks.		√	Rejected
6.10.6	Inner packing	Packing being inconsistent with quantity and part number on packing label, specifications or the customer order - either short-packed or over-packed.		√	Rejected

## 7. Reliability test

Notes: ①Reliability tests shall be done as required by the customer if they inform ADV of their special requirements when starting a project.

Test item	Condition	Time(hrs)	Acceptance standard
High Storage Temp.	80°C	120	Functions and appearance are qualified before and after test
High Operating Temp.	70°C	120	
Low Storage Temp.	-30°C	120	
Low Operating Temp.	-20°C	120	
Temp& Humidity Test	40°C/ 90%RH	120	
Thermal Shock	-20°C ← 25°C →70°C (30 min ← 5 min → 30min)	10 cycles	

②Storage test at high-low temperature and functionality test shall be done with reference to the specified temperature range.

③Test conditions shall be controlled at the permissible tolerance of  $\pm 5^{\circ}\text{C}$ .

## 8. Packing

8.1 Product packing shall be checked and qualified in accordance with design requirements for packing. The product packaging label shall bear not only product name, part number,



quantity, product date code but also QA's qualifying stamp for each production stage.  
Incomplete or wrong label shall be unacceptable.

8.2 When there are problems with packing safety conformity such as shock resistance, moisture resistance, anti ESD and pressure resistance, packing shall be disqualified.

8.3 When confirmed and accepted by AVD, packing shall be inspected and released according to the customer's special requirements for packing.

8.4 RoHS and non-RoHS compliant products shall be labeled clearly and separately. Unless otherwise specified by the customer, "RoHS" labels shall be used for all RoHS compliant products.

## **9. Others**

9.1 Items not specified in this document or released on compromise should be inspected with reference to the mutual agreement and limit samples.