



GEMINI Technology Co., Ltd.
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SPECIFICATION FOR LCM MODULE

MODULE NO.: ABG122032A02- YHY
DOC.REVISION : 00

Customer Approval:

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	SIGNATURE	DATE
PREPARED BY (RD ENGINEER)		May-25-2006
PREPARED BY (QA ENGINEER)		
CHECKED BY		
APPROVED BY		



DOCUMENT REVISION HISTORY

Version	DATE	DESCRIPTION	CHANGED BY
00	Apr-6-2006	First issue	
01	Apr-12-2006	Change pin definiens	
02	May-25-2006	Replace the IC	



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1. FUNCTIONS & FEATURES

- 1.1. Format : 122x32dots
- 1.2. LCD mode : STN / Positive transfective mode / Yellow-green
- 1.3. Viewing direction : 6 o'clock
- 1.4. Driving scheme : 1/32 Duty cycle, 1/6 Bias
- 1.5. Power supply voltage(V_{DD}) : 5.0V
- 1.6. LCD driving voltage : 4.5V
- 1.7. Operation temp : -20~70
- 1.8. Storage temp : -30~80
- 1.9. Backlight color : Yellow-green

2. MECHANICAL SPECIFICATIONS

- 2.1. Module size : 80.0mm(L)*36.0mm(W)*13.5MAX mm(H)
- 2.2. Viewing area : 60.5mm(L)*18.5mm(W)
- 2.3. Dot pitch : 0.44mm(L)*0.49mm(W)
- 2.4. Dot size : 0.40mm(L)*0.45mm(W)
- 2.5. Weight : Approx.

3. BLOCK DIAGRAM

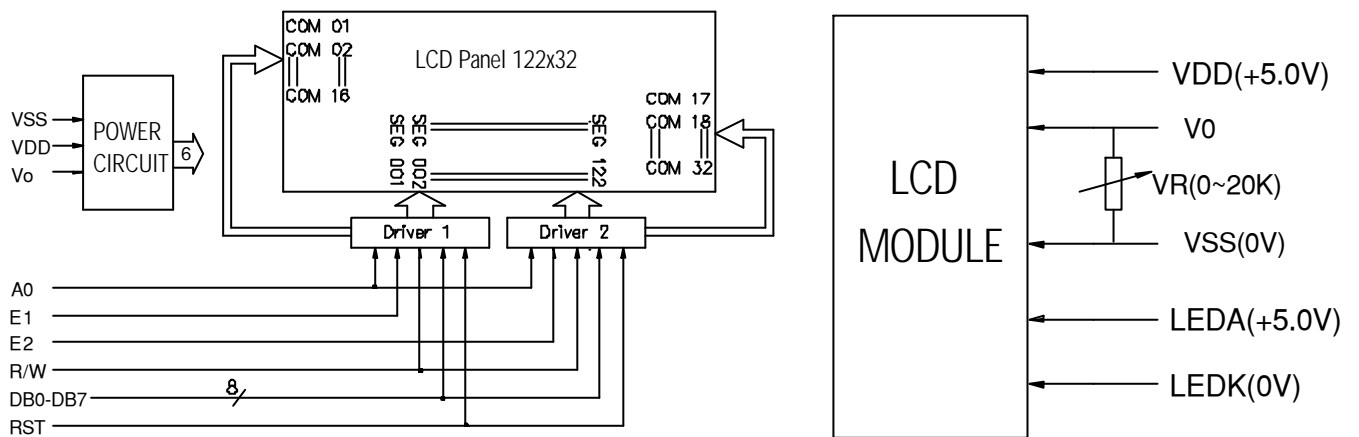


Figure 1. Block diagram

4. DIMENSIONAL OUTLINE

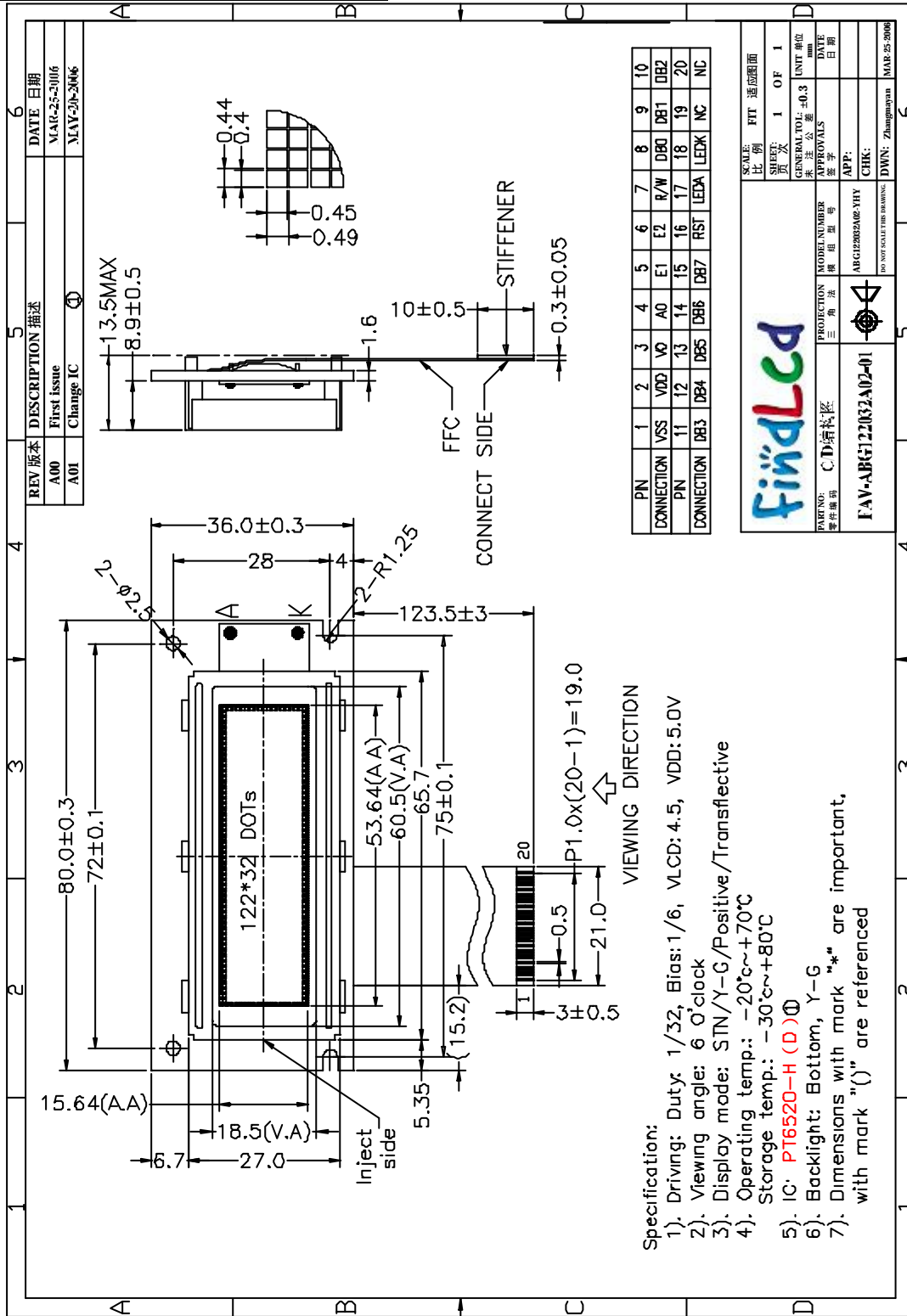


Figure 2. Dimensional outline

5. PIN DESCRIPTION

No.	Symbol	Function
1	VSS	GND(0V)
2	VDD	Power supply(+5.0V)
3	VO	Supply voltage for LCD drive
4	A0	Register selection. (H: Data register L: Instruction register)
5	E1	Enable signal for IC1(left half of the panel)
6	E2	Enable signal for IC2(right half of the panel)
7	R/W	Read /write selection. (H: Read L: write)
8~15	DB0~DB7	Data bus lines
16	RST	Reset signal(The rise of the signal is for active and keep RET='h')
17	LEDA	Power supply for backlight(+5.0V)
18	LEDK	Power supply for backlight(0V)
19/20	NC	No connection

6. MAXIMUM ABSOLUTE LIMIT

(For IC)

Parameter	Symbol	Condition	Unit
Supply voltage (1)	V _{SS}	-8.0 ~ +0.3	V
Supply voltage (2)	V ₅	-16.5 ~ +0.3	V
Supply voltage (3)	V ₁ , V ₄ , V ₂ , V ₃	V ₅ ~ +0.3	V
Input voltage	V _{IN}	V _{SS} - 0.3 ~ +0.3	V
Output voltage	V _O	V _{SS} - 0.3 ~ +0.3	V
Allowable loss	PD	250	mW
Operating temperature	T _{opr}	-30 ~ +85	°C
Storage temperature	T _{stg}	-65 ~ +150	°C
Soldering temperature/time	T _{solder}	260 / 10 (at lead)	°C / Sec

Notes:

1. All voltages are based on VDD = 0V.

2. The following condition must always hold true with voltages V1, V2, V3, V4 and V5:

VDD = V1= V2= V3= V4= V5

3. The LSI may be permanently damaged if used with any value in excess of the absolute maximum ratings. During normal operation, the LSI should preferably be used within the specified electrical characteristics. Failure to meet them can cause the LSI to malfunction or lose its reliability.

4. Generally, flat package LSIs may have moisture resistance lowered when solder dipped. In mounting LSIs on a board, it is recommended to use a method which is least unlikely to give thermal stress on the package resin.

7. ELECTRICAL CHARACTERISTICS

(VDD=0V, VSS=-5V)

Parameter	Symbol	Condition	Min	Typ	Max	Unit	Applicable Pin		
Operating voltage (1)*1	Recommended	VSS	-5.5	-5.0	-4.5	V	VSS		
	Potential		-7.0	-	-2.4	V			
Operating voltage (2)	Recommended	V5	-13	-	-3.5	V	V5		
	Potential		-13	-	-	V			
	Potential		V1, V2	0.6xV5	-	VDD		V	V1, V2
	Potential	V3, V4	V5	-	0.4xV5	V	V3, V4		
High input voltage	VIHT		VSS+2.0	-	VDD	V	*2, *3		
	VIHC		0.2xVSS	-	VDD				
	VIHT	VSS=-3v	0.2xVSS	-	VDD				
	VIHC	VSS=-3v	0.2xVSS	-	VDD				
Low input voltage	VILT		VSS	-	VSS+0.8	V	*2, *3		
	VILC		VSS	-	0.8xVSS				
	VILT	VSS=-3v	VSS	-	0.85xVSS				
	VILC	VSS=-3v	VSS	-	0.8xVSS				
High output voltage	VOHT	IOH=-3.0mA	VSS+2.4	-	-	V	OSC2 *4, *5		
	VOHC1	IOH=-2.0mA	VSS+2.4	-	-				
	VOHC2	IOH=-120Ma	0.2xVSS	-	-				
	VOHT	VSS=-3v IOH=-2mA	0.2xVSS			V	*4, *5 OSC2		
	VOHC1	VSS=-3v IOH=-2mA	0.2xVSS						
	VOHC2	VSS=-3v IOH=-50μA	0.2xVSS						
Low output voltage	VOLT	IOL=3.0mA	-	-	VSS+0.4	V	OSC2 *4, *5		
	VOLT1	IOL=2.0mA	-	-	VSS+0.4				
	VOLT2	IOL=120μA	-	-	0.8xVSS				
	VOLT	VSS=-3v IOL=2mA			0.8xVSS	V	*4, *5 OSC2		
	VOLC1	VSS=-3v IOL=2mA			0.8xVSS				
	VOLC2	VSS=-3v IOL=50μA			0.8xVSS				
Input leak current	ILI		-1	-	1	μA	*6		
Output leak current	ILO		-3	-	3	μA	*7		
LCD driver ON resistance	RON	Ta=25°C	V5=-5.0V	-	5	7.5	KΩ	SEG0~60 COM0~15	
			V5=-3.5V	-	10	50			
Static current consumption	IDDQ	CS=CL=VDD	-	0.05	1	μA	VDD		
Dynamic current dissipation	External CLK	During IDD (1)	During display Vs=-5.0V	Rf=1MΩ	-	9.5	15	μA	VDD
			During display V5=-5V VSS=-3V	Rf=1 MΩ	-	6.0	12		
	Oscillator	IDD (2)	During access Tcyc = 200kHz		-	300	500	μA	VDD
			VSS=-3V, During access Tcyc = 200 kHz			150	300		
Input terminal capacity	CIN	Ta=25°C, f=1MHz	-	5.0	8	pF	All input terminals		
Oscillation frequency	Fosc	Rf=1MΩ+2% VSS=-5.0V	15	18	21	kHz	*4, *5 OSC2		
		Rf=1MΩ +2% VSS=-3.0V	11	16	21				
Reset time	tR		1.0	-		μs	RES		
Hysteresis	VH		0.05VSS	0.1VSS	-	V			

Notes: 1. A wide range of operating voltages is guaranteed, except in case of abrupt voltage fluctuations during MPU access.

2. A0, D0~D7, E, R/W and CS pins

3. CL, FR, M/S and RES pins

4. A0, E, R/W, CS, CL, RES, M/S pins

8. TIMING CHARACTERISTICS

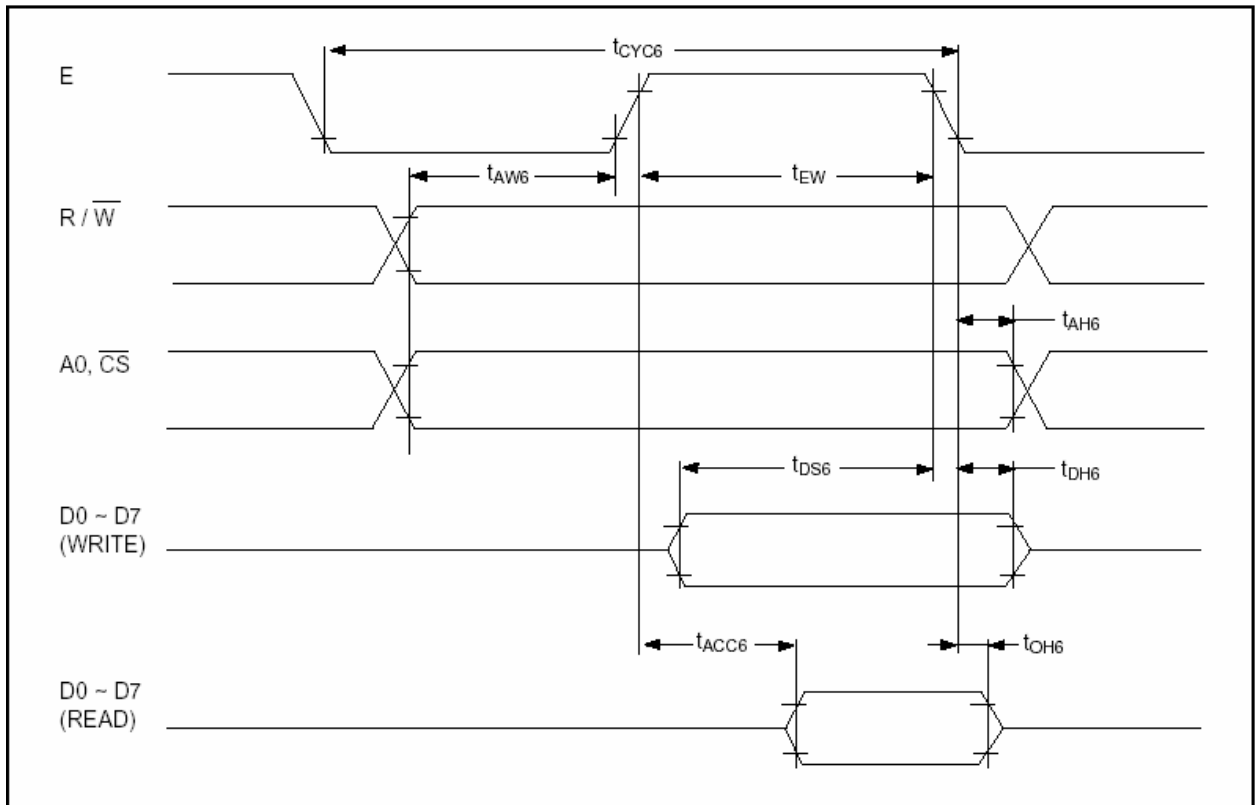
Ta = -210 to 75°C, VSS = -5.0V ± 10%

Parameter	Symbol	Signal	Condition	Min	Typ	Max	Unit
System cycle time	tCYC6 *1	A0, \overline{CS}	CL = 100 pF	1000	—	—	ns
Address setup time	tAW6	R/ \overline{W}		20	—	—	ns
Address hold time	tAH6			10	—	—	ns
Data setup time	tDS6			D0 – D7	80	—	—
Data hold time	tDH6	10			—	—	ns
Output disable time	tOH6	10			—	60	ns
Access time	tACC6	—			—	90	ns
Enable pulse width: Read	tEW	E	100	—	—	ns	
Enable pulse width: Write			80	—	—	ns	

*1 tCYC6 indicates the cycle time during which $\overline{CS} \cdot E = "H"$. It does not mean the cycle time of signal E.

*2 Each of the values where Vss = -3.0V is about 200% of that where Vss = -5.0V (i.e., the listed value).

*3 The rise or fall time of input signals should be less than 15 ns.



System bus read/write II (68-family MPU)

9. CONTROL AND DISPLAY INSTRUCTION

	Command	Code											Function	
		A0	RD	WR	D7	D6	D5	D4	D3	D2	D1	D0		
(1)	Display ON/OFF	0	1	0	1	0	1	0	1	1	1	0/1	Turns all display on or off, independently of display RAM data or internal status. 1: ON 0: OFF (Power-saving mode with static drive on)*	
(2)	Display Start Line	0	1	0	1	1	0	Display Start Address (0 – 31)				0	Specifies RAM line corresponding to uppermost line (COM0) of display.	
(3)	Set Page Address	0	1	0	1	0	1	1	1	0	Page (0-3)		Sets display RAM page in page address register.	
(4)	Set Column (Segment) Address	0	1	0	0	Column Address (0-79)							0	Sets display RAM column address in column address register.
(5)	Read Status	0	0	1	Busy	ADC	ON/OFF	RESET	0	0	0	0	Reads the following status: BUSY 1: Internal operation 0: Ready ADC 1: CW output (forward) 0: CCW output (reverse) ON/OFF 1: Display off 0: Display on RESET 1: Being reset 0: Normal	
(6)	Write Display Data	1	1	0					Write Data					
(7)	Read Display Data	1	0	1	Read Data							0		
(8)	Select ADC	0	1	0	1	0	1	0	0	0	0	0/1	Used to invert relationship of assignment between display RAM column addresses and segment driver outputs. 0: CW output (forward) 1: CCW output (reverse)	
(9)	Static Drive ON/OFF	0	1	0	1	0	1	0	0	1	0	0/1	Selects normal display or static driving operation. 1: Static drive (power-saving mode) 0: Normal driving	
(10)	Select Duty	0	1	0	1	0	1	0	1	0	0	0/1	Selects LCD cell driving duty 1: 1/32 0: 1/16	
(11)	Read Modify Write	0	1	0	1	1	1	0	0	0	0	0	Increments column address counter by 1 when display data is written. (This is not done when data is read.)	
(12)	End	0	1	0	1	1	1	0	1	1	1	0	Clears read modify write mode.	
(13)	Reset	0	1	0	1	1	1	0	0	0	1	0	Sets display start line register on the first line. Also sets column address counter and page address counter to 0.	

* With display off (command (1)), static drive going on (9) invokes power-saving mode.

10.? BACK LIGHT CHARACTERISTICS

LCD Module with bottom backlight
ELECTRICAL RATINGS

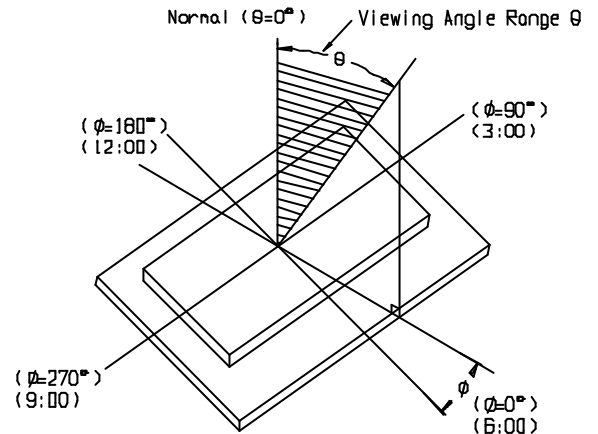
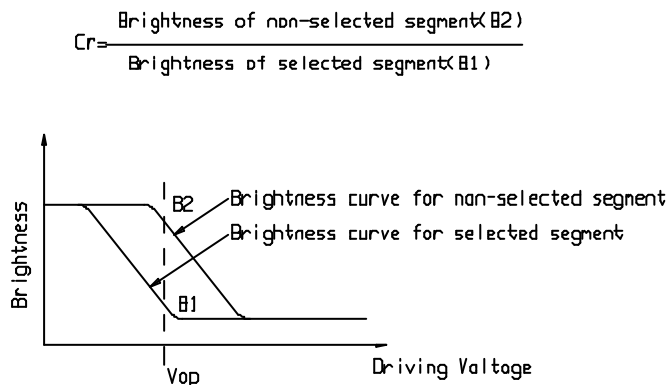
Ta = 25°C

Item	Symbol	Condition	Min	Typ	Max	Unit
Forward Voltage	VF	IF=100mA	4.0	4.2	4.4	V
Reverse Current	IR	VR=10V	---	0.1	---	mA
Luminous Intensity (With LCD dots off)	IV	IF=100mA	--	---	---	Cd/m ²
Wave length	??	IF=100mA	568	570	575	nm
Color	Yellow-green					

11. ELECTRO-OPTICAL CHARACTERISTICS?

(V_{OP} = 4.5V, Ta = 25°C)

Item	Symbol	Condition	Min	Typ	Max	Unit
Operating Voltage	Vop	Ta = -20°C	4.9	5.0	5.2	V
		Ta = 25°C	4.3	4.5	4.7	
		Ta = 70°C	3.9	4.0	4.1	
Response time	Tr	Ta = 25°C	---	185	---	ms
	Tf		---	200	---	ms
Contrast	Cr	Ta = 25°C	---	4	---	---
Viewing angle range	θ	Cr 2	-40	---	+40	deg
			-40	---	+40	deg

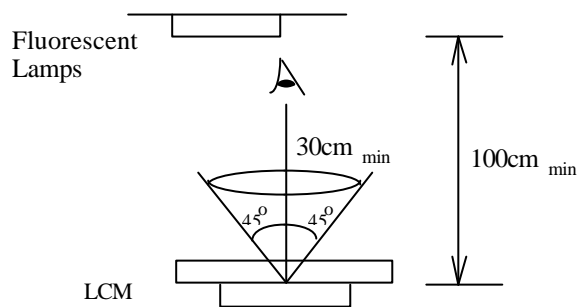


12.QUALITY SPECIFICATIONS

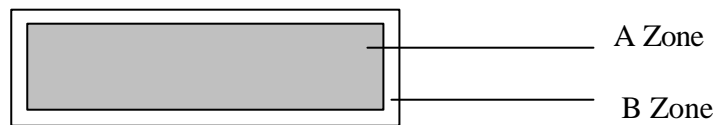
12.1 Standard of the product appearance test

Manner of appearance test: The inspection should be performed in using 20W x 2 fluorescent lamps. Distance between LCM and fluorescent lamps should be 100 cm or more. Distance between LCM and inspector eyes should be 30 cm or more.

Viewing direction for inspection is 45° from vertical against LCM.



Definition of zone:



A Zone: Active display area (minimum viewing area).

B Zone: Non-active display area (outside viewing area).

12.2 Specification of quality assurance

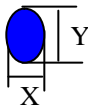
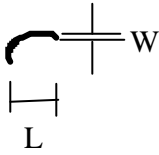
AQL inspection standard

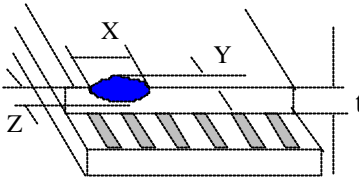
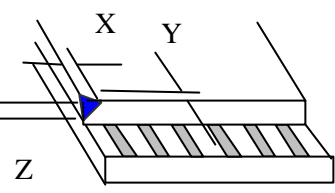
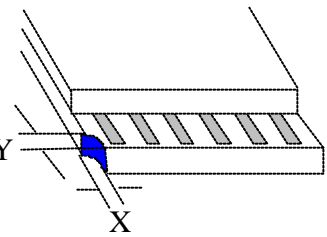
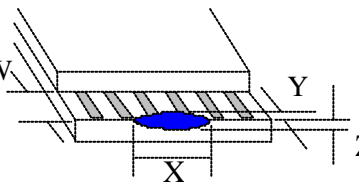
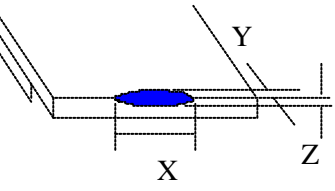
Sampling method: MIL-STD-105E, Level II, single sampling

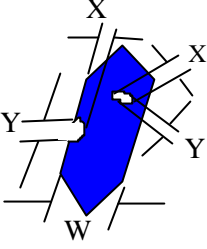
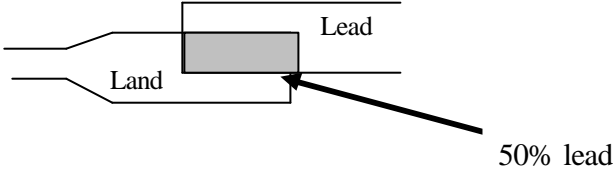
Defect classification (Note: * is not including)

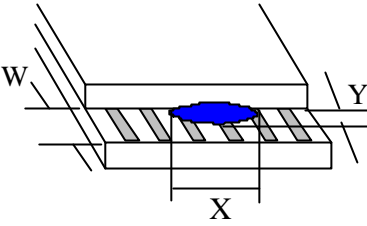
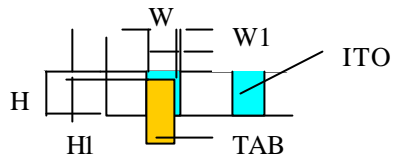
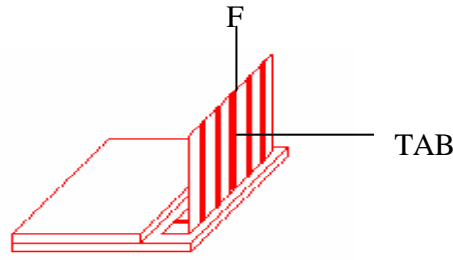
Classify	Item		Note	AQL
Major	Display state	Short or open circuit	1	0.65
		LC leakage		
		Flickering		
		No display		
		Wrong viewing direction		
		Contrast defect (dim, ghost)	2	
		Back-light	1,8	
	Non-display	Flat cable or pin reverse	10	
Wrong or missing component		11		
Minor	Display state	Background color deviation	2	1.0
		Black spot and dust	3	
		Line defect, Scratch	4	
		Rainbow	5	
		Chip	6	
		Pin hole	7	
	Polarizer	Protruded	12	
		Bubble and foreign material	3	
	Soldering	Poor connection	9	
	Wire	Poor connection	10	
	TAB	Position, Bonding strength	13	

Note on defect classification

No.	Item	Criterion																				
1	Short or open circuit	Not allow																				
	LC leakage																					
	Flickering																					
	No display																					
	Wrong viewing direction																					
	Wrong Back-light																					
2	Contrast defect	Refer to approval sample																				
	Background color deviation																					
3	Point defect, Black spot, dust (including Polarizer) $\phi = (X+Y)/2$	 <table border="1" data-bbox="917 996 1337 1288"> <thead> <tr> <th>Point Size</th> <th>Acceptable Qty.</th> </tr> </thead> <tbody> <tr> <td>$\phi \leq 0.10$</td> <td>Disregard</td> </tr> <tr> <td>$0.10 < \phi \leq 0.20$</td> <td>3</td> </tr> <tr> <td>$0.20 < \phi \leq 0.25$</td> <td>2</td> </tr> <tr> <td>$0.25 < \phi \leq 0.30$</td> <td>1</td> </tr> <tr> <td>$\phi > 0.30$</td> <td>0</td> </tr> </tbody> </table> <p>Unit : mm</p>	Point Size	Acceptable Qty.	$\phi \leq 0.10$	Disregard	$0.10 < \phi \leq 0.20$	3	$0.20 < \phi \leq 0.25$	2	$0.25 < \phi \leq 0.30$	1	$\phi > 0.30$	0								
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4	Line defect, Scratch	 <table border="1" data-bbox="845 1451 1377 1709"> <thead> <tr> <th colspan="2">Line</th> <th>Acceptable Qty.</th> </tr> <tr> <th>L</th> <th>W</th> <th></th> </tr> </thead> <tbody> <tr> <td>---</td> <td>$0.015 W$</td> <td>Disregard</td> </tr> <tr> <td>$3.0 L$</td> <td>$0.03 W$</td> <td rowspan="2">2</td> </tr> <tr> <td>$2.0 L$</td> <td>$0.05 W$</td> </tr> <tr> <td>$1.0 L$</td> <td>$0.1 > W$</td> <td>1</td> </tr> <tr> <td>---</td> <td>$0.05 < W$</td> <td>Applied as point defect</td> </tr> </tbody> </table> <p>Unit: mm</p>	Line		Acceptable Qty.	L	W		---	$0.015 W$	Disregard	$3.0 L$	$0.03 W$	2	$2.0 L$	$0.05 W$	$1.0 L$	$0.1 > W$	1	---	$0.05 < W$	Applied as point defect
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$1.0 L$	$0.1 > W$	1																				
---	$0.05 < W$	Applied as point defect																				
5	Rainbow	Not more than two color changes across the viewing area.																				

No	Item	Criterion																																	
6	<p>Chip</p> <p>Remark: X: Length direction Y: Short direction Z: Thickness direction t: Glass thickness W: Terminal Width</p>	 <p>Acceptable criterion</p> <table border="1" data-bbox="981 504 1364 582"> <thead> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td>2</td> <td>0.5mm</td> <td>t/2</td> </tr> </tbody> </table>  <p>Acceptable criterion</p> <table border="1" data-bbox="973 795 1364 884"> <thead> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td>2</td> <td>0.5mm</td> <td>t</td> </tr> </tbody> </table>  <p>Acceptable criterion</p> <table border="1" data-bbox="989 1086 1364 1209"> <thead> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td>3</td> <td>2</td> <td>t</td> </tr> <tr> <td colspan="2">shall not reach to ITO</td> <td></td> </tr> </tbody> </table>  <p>Acceptable criterion</p> <table border="1" data-bbox="973 1444 1364 1534"> <thead> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td>Disregard</td> <td>0.2</td> <td>t</td> </tr> </tbody> </table>  <p>Acceptable criterion</p> <table border="1" data-bbox="973 1724 1332 1803"> <thead> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td>5</td> <td>2</td> <td>t/3</td> </tr> </tbody> </table>	X	Y	Z	2	0.5mm	t/2	X	Y	Z	2	0.5mm	t	X	Y	Z	3	2	t	shall not reach to ITO			X	Y	Z	Disregard	0.2	t	X	Y	Z	5	2	t/3
X	Y	Z																																	
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5	2	t/3																																	

No.	Item	Criterion								
7	Segment pattern $W = \text{Segment width}$ $\phi = (X+Y)/2$	(1) Pin hole $\phi < 0.10\text{mm}$ is acceptable.  <table border="1" data-bbox="906 629 1353 808"> <thead> <tr> <th>Point Size</th> <th>Acceptable Qty</th> </tr> </thead> <tbody> <tr> <td>$\phi < 1/4W$</td> <td>Disregard</td> </tr> <tr> <td>$1/4W < \phi < 1/2W$</td> <td>1</td> </tr> <tr> <td>$\phi > 1/2W$</td> <td>0</td> </tr> </tbody> </table> <p style="text-align: center;">Unit: mm</p>	Point Size	Acceptable Qty	$\phi < 1/4W$	Disregard	$1/4W < \phi < 1/2W$	1	$\phi > 1/2W$	0
Point Size	Acceptable Qty									
$\phi < 1/4W$	Disregard									
$1/4W < \phi < 1/2W$	1									
$\phi > 1/2W$	0									
8	Back-light	(1) The color of backlight should correspond its specification. (2) Not allow flickering								
9	Soldering	(1) Not allow heavy dirty and solder ball on PCB. (The size of dirty refer to point and dust defect) (2) Over 50% of lead should be soldered on Land. 								
10	Wire	(1) Copper wire should not be rusted (2) Not allow crack on copper wire connection. (3) Not allow reversing the position of the flat cable. (4) Not allow exposed copper wire inside the flat cable.								
11*	PCB	(1) Not allow screw rust or damage. (2) Not allow missing or wrong putting of component.								

No	Item	Criterion				
12	Protruded W: Terminal Width	 <p>Acceptable criteria: $Y \leq 0.4$</p>				
13	TAB	<p>1. Position</p>  <table border="1" data-bbox="1161 779 1385 907"> <tr> <td>W1</td> <td>1/3W</td> </tr> <tr> <td>H1</td> <td>1/3H</td> </tr> </table> <p>2. TAB bonding strength test</p>  <p>P (=F/TAB bonding width) 650gf/cm ,(speed rate: 1mm/min) 5pcs per SOA (shipment)</p>	W1	1/3W	H1	1/3H
W1	1/3W					
H1	1/3H					
14	Total no. of acceptable Defect	<p>A. Zone</p> <p>Maximum 2 minor non-conformities per one unit. Defect distance: each point to be separated over 10mm</p> <p>B. Zone</p> <p>It is acceptable when it is no trouble for quality and assembly in customer's end product.</p>				

12.3 Reliability of LCM

Reliability test condition:

Item	Condition	Time (hrs)	Assessment
High temp. Storage	80°C	48	No abnormalities in functions and appearance
High temp. Operating	70°C	48	
Low temp. Storage	-30°C	48	
Low temp. Operating	-20°C	48	
Humidity	40°C/ 90%RH	48	
Temp. Cycle	0°C ← 25°C → 50°C (30 min ← 5 min → 30min)	10cycles	

Recovery time should be 24 hours minimum. Moreover, functions, performance and appearance shall be free from remarkable deterioration within 50,000 hours under ordinary operating and storage conditions room temperature (20±8°C), normal humidity (below 65% RH), and in the area not exposed to direct sun light.

12.4 Precaution for using LCD/LCM

LCD/LCM is assembled and adjusted with a high degree of precision. Do not attempt to make any alteration or modification. The followings should be noted.

General Precautions:

1. LCD panel is made of glass. Avoid excessive mechanical shock or applying strong pressure onto the surface of display area.
2. The polarizer used on the display surface is easily scratched and damaged. Extreme care should be taken when handling. To clean dust or dirt off the display surface, wipe gently with cotton, or other soft material soaked with isopropyl alcohol, ethyl alcohol or trichlorotrifluoroethane, do not use water, ketone or aromatics and never scrub hard.
3. Do not tamper in any way with the tabs on the metal frame.
4. Do not make any modification on the PCB without consulting Gemini.
5. When mounting a LCM, make sure that the PCB is not under any stress such as bending or twisting. Elastomer contacts are very delicate and missing pixels could result from slight dislocation of any of the elements.
6. Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pixels and also cause rainbow on the display.
7. Be careful not to touch or swallow liquid crystal that might leak from a damaged cell. Any liquid crystal adheres to skin or clothes, wash it off immediately with soap and water.

Static Electricity Precautions:

1. CMOS-LSI is used for the module circuit; therefore operators should be grounded when ever he/she comes into contact with the module.
2. Do not touch any of the conductive parts such as the LSI pads; the copper leads on the PCB and the interface terminals with any parts of the human body.
3. Do not touch the connection terminals of the display with bare hand; it will cause disconnection or defective insulation of terminals.
4. The modules should be kept in anti-static bags or other containers resistant to static for storage.
5. Only properly grounded soldering irons should be used.
6. If an electric screwdriver is used, it should be grounded and shielded to prevent sparks.
7. The normal static prevention measures should be observed for work clothes and working enches.
8. Since dry air is inductive to static, a relative humidity of 50-60% is recommended.

Soldering Precautions:

1. Soldering should be performed only on the I/O terminals.
2. Use soldering irons with proper grounding and no leakage.
3. Soldering temperature: $280^{\circ}\text{C}\pm 10^{\circ}\text{C}$
4. Soldering time: 3 to 4 second.
5. Use eutectic solder with resin flux filling.
6. If flux is used, the LCD surface should be protected to avoid spattering flux.
7. Flux residue should be removed.

Operation Precautions:

1. The viewing angle can be adjusted by varying the LCD driving voltage V_o .
2. Since applied DC voltage causes electro-chemical reactions, which deteriorate the display, the applied pulse waveform should be a symmetric waveform such that no DC component remains. Be sure to use the specified operating voltage.
3. Driving voltage should be kept within specified range; excess voltage will shorten display life.
4. Response time increases with decrease in temperature.
5. Display color may be affected at temperatures above its operational range.
6. Keep the temperature within the specified range usage and storage. Excessive temperature and humidity could cause polarization degradation, polarizer peel-off or generate bubbles.
7. For long-term storage over 40°C is required, the relative humidity should be kept below 60%, and avoid direct sunlight.

Limited Warranty

Gemini LCDs and modules are not consumer products, but may be incorporated by Gemini's customers into consumer products or components thereof, Gemini does not warrant that its LCDs and components are fit for any such particular purpose.

1. The liability of Gemini is limited to repair or replacement on the terms set forth below. Gemini will not be responsible for any subsequent or consequential events or injury or damage to any personnel or user including third party personnel and/or user. Unless otherwise agreed in writing between Gemini and the customer, Gemini will only replace or repair any of its LCD which is found defective electrically or visually when inspected in accordance with Gemini general LCD inspection standard . (Copies available on request)
2. No warranty can be granted if any of the precautions state in handling liquid crystal display above has been disregarded. Broken glass, scratches on polarizer mechanical damages as well as defects that are caused accelerated environment tests are excluded from warranty.
3. In returning the LCD/LCM, they must be properly packaged; there should be detailed description of the ailures or defect