



ABS205 THRU ABS210

MINI SILICON SURFACE MOUNT BRIDGE RECTIFIER

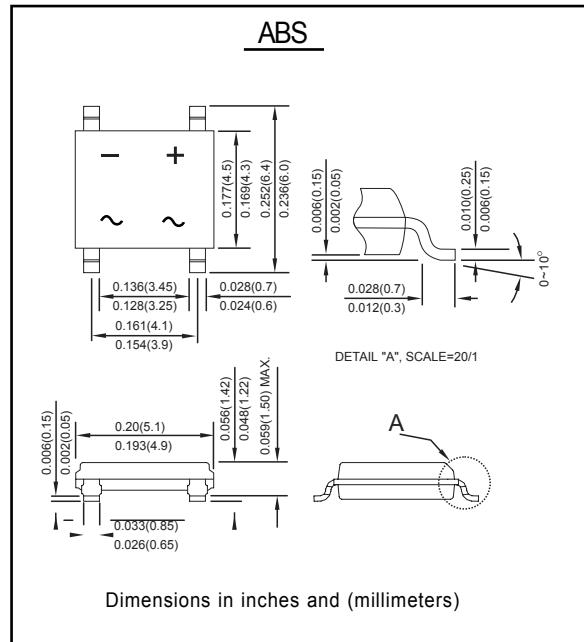
Reverse Voltage - 50 to 1000 Volts Forward Current - 2.0 Ampere

FEATURES

- Ideal for printed circuit board
- Reliable low cost construction technique results in inexpensive product
- High temperature soldering guaranteed : 260°C / 10 seconds / 0.375" (9.5mm) lead length at 5 lbs., (2.3 kg) tension

MECHANICAL DATA

- Case : Molded Plastic
- Epoxy : Device has UL flammability classification 94V-0
- Mounting Position : Any
- Marking : Type Number



MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified.
Single phase half-wave 60Hz, resistive or inductive load, for capacitive load current derate by 20%.

Characteristic	Symbol	ABS 205	ABS 21	ABS 22	ABS 24	ABS 26	ABS 28	ABS 210	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	50	100	200	400	600	800	1000	V
RMS Reverse Voltage	V _R (RMS)	35	70	140	280	420	560	700	V
Average Rectified Output Current (Note 1) @T _A = 50°C Average Rectified Output Current (Note 2) @T _A = 50°C	I _O				2.0				A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I _{FSM}				60				A
I ² t Rating for Fusing (t < 8.3ms)	I ² t				5.0				A ² s
Forward Voltage per element @I _F = 2.0A	V _{FM}				1.1				V
Peak Reverse Current @T _A = 25°C At Rated DC Blocking Voltage @T _A = 125°C	I _{RM}				5.0	500			µA
Typical Junction Capacitance per leg (Note 3)	C _J				13				pF
Typical Thermal Resistance per leg (Note 1)	R _{θJA} R _{θJL}				62.5	25			°C/W
Operating and Storage Temperature Range	T _j , T _{STG}				-55 to +150				°C

- Note: 1. Mounted on glass epoxy PC board with 1.3mm² solder pad.
 2. Mounted on aluminum substrate PC board with 1.3mm² solder pad.
 3. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.



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RATINGS AND CHARACTERISTIC CURVES

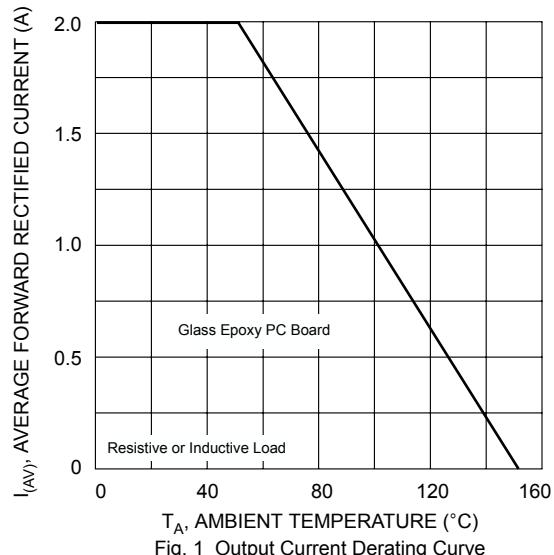


Fig. 1 Output Current Derating Curve

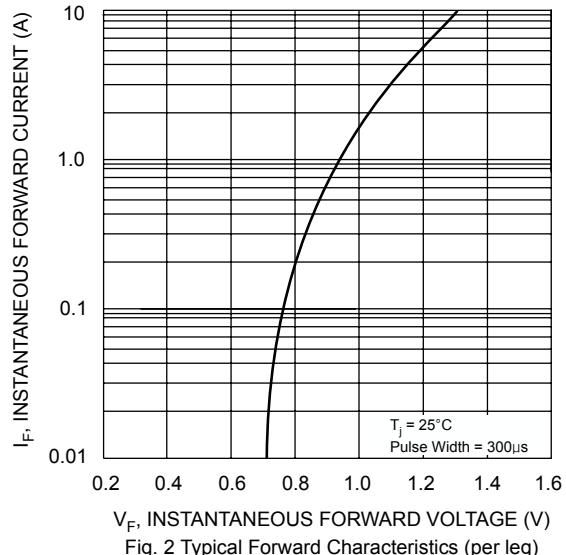


Fig. 2 Typical Forward Characteristics (per leg)

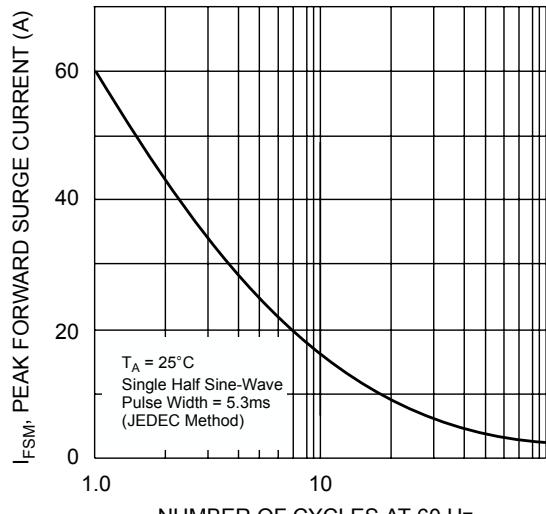


Fig. 3 Maximum Peak Forward Surge Current (per leg)

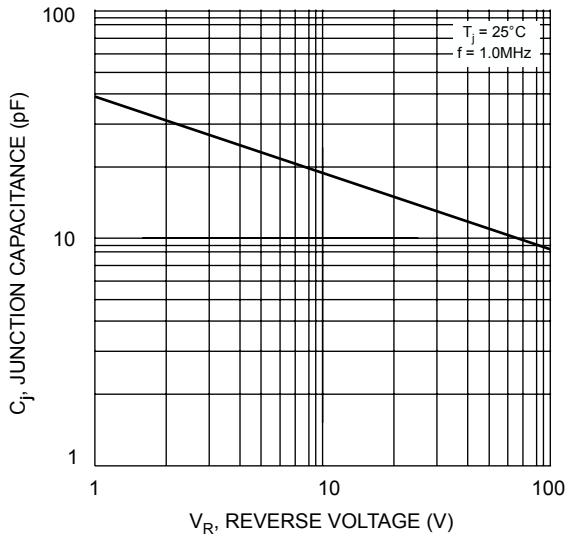


Fig. 4 Typical Junction Capacitance

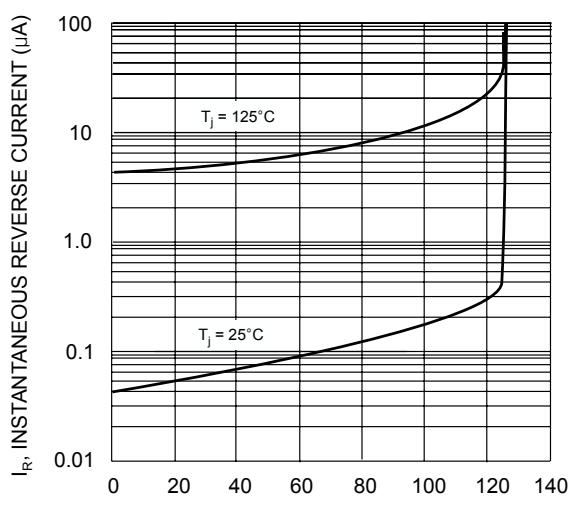


Fig. 5 Typical Reverse Characteristics (per element)