

### Description

The ACE7331M utilize a high cell density trench process to provide low r<sub>DS</sub>(on) and to ensure minimal power loss and heat dissipation. Typical applications are DC-DC converters and power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

#### Features

- Low r<sub>DS(on)</sub> provides higher efficiency and extends battery life
- Low thermal impedance copper lead frame DFN3x3-8L saves board space
- Fast switching speed
- High performance trench technology

#### **Absolute Maximum Ratings**

Parameter		Symbol	Limit	Units	
Drain-Source Voltage		V <sub>DS</sub>	-30	V	
Gate-Source Voltage		V <sub>GS</sub>	±20	V	
Continuous Drain Current <sup>a</sup>	<b>T</b> <sub>A</sub> =25°C	- I <sub>D</sub>	-13.4	A	
	T <sub>A</sub> =70°C		-11.0		
Pulsed Drain Current <sup>b</sup>		I <sub>DM</sub>	±50	А	
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	-2.1	А	
Power Dissipation <sup>a</sup>	<b>T</b> <sub>A</sub> =25°C	Б	3.5	W	
Fower Dissipation	<b>T</b> <sub>A</sub> <b>=70</b> °C	- P <sub>D</sub>	2.0		
Operating temperature / storage temperature		T <sub>J</sub> /T <sub>STG</sub>	-55~150	°C	

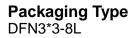
THERMAL RESISTANCE RATINGS								
Parameter		Symbol	Maximum	Units				
Maximum Junction-to-Ambient <sup>a</sup>	t <= 10 sec	Р	35	°C 11/				
	Steady State	R <sub>θJA</sub>	81	°C/W				

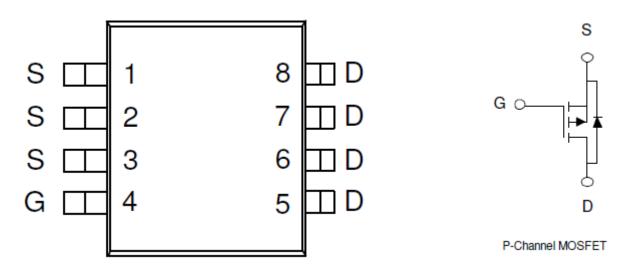
Notes

a. Surface Mounted on 1" x 1" FR4 Board.

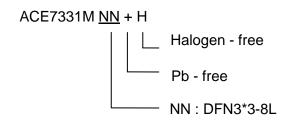
b. Pulse width limited by maximum junction temperature







## **Ordering information**





## **Electrical Characteristics**

 $T_A=25^{\circ}C$ , unless otherwise specified.

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit			
Static									
Gate-Source Threshold Voltage	$V_{\text{GS(th)}}$	$V_{DS} = V_{GS}$ , $I_D = -250 \text{ uA}$	-1			V			
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 25 V$			±100	nA			
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = -24 \text{ V}, V_{GS} = 0 \text{ V}$			-1				
		$V_{\text{DS}}$ = -24 V, $V_{\text{GS}}$ = 0 V, $T_{\text{J}}$ = 55°C			-5	uA			
On-State Drain Current <sup>A</sup>	I <sub>D(on)</sub>	$V_{DS}$ = -5 V, $V_{GS}$ = -10 V	-50			А			
Drain-Source On-Resistance <sup>A</sup>	R <sub>DS(ON)</sub>	$V_{GS} = -10 \text{ V}, \text{ I}_{D} = -11.5 \text{ A}$			19	mΩ			
		$V_{GS}$ = -4.5 V, I <sub>D</sub> =-9.3 A			30				
Forward Transconductance <sup>A</sup>	<b>g</b> fs	$V_{DS}$ =-15 V, $I_{D}$ = -11.5 A		29		S			
Diode Forward Voltage	$V_{SD}$	$I_{\rm S} = 2.5  {\rm A},  {\rm V}_{\rm GS} = 0  {\rm V}$		-0.8		V			
Dynamic <sup>b</sup>									
Total Gate Charge	$Q_g$	V <sub>DS</sub> = -15 V, V <sub>GS</sub> = -5 V, I <sub>D</sub> =- 11.5 A		25		nC			
Gate-Source Charge	$Q_gs$			11					
Gate-Drain Charge	$Q_gd$			17					
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DS</sub> = -15 V, R <sub>L</sub> = 6 Ω, I <sub>D</sub> = -1 A, V <sub>GEN</sub> = -10 V		15		ns			
Rise Time	t <sub>r</sub>			13					
Turn-Off Delay Time	t <sub>d(off)</sub>			100					
Fall Time	t <sub>f</sub>			54					

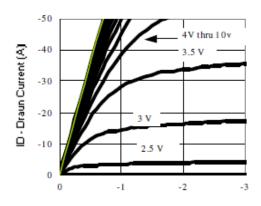
Note :

a. Pulse test: PW <= 300us duty cycle <= 2%.

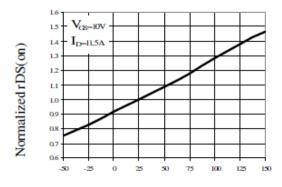
b. Guaranteed by design, not subject to production testing



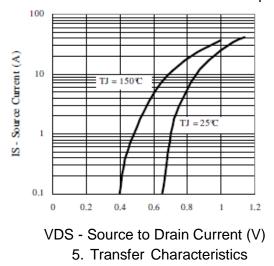
### **Typical Performance Characteristics**

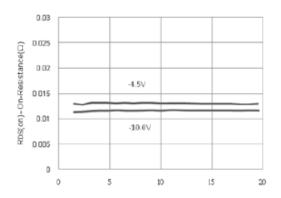


VDS - Gate-to-Source Voltage (V) 1.On-Resistance Characteristics

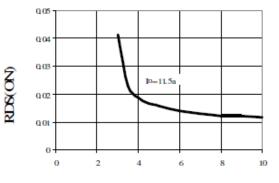


TJ –Junction Temperature(°C) 3. On-Resistance vs. Variation with Temperature

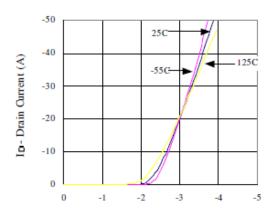




ID-Drain Current (A)ace 2. On-Resistance Variation with Drain Current and Gate Voltage



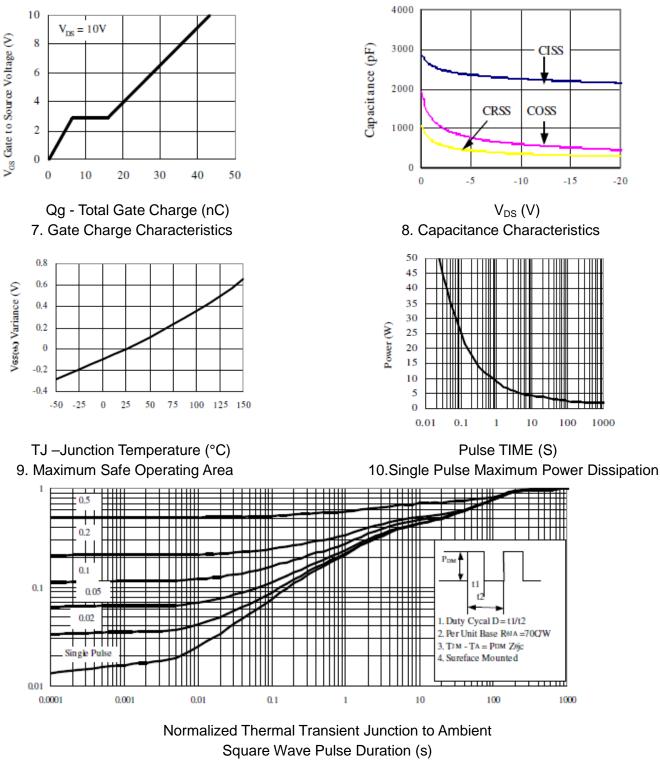
VGS - Gate-to-Source Voltage (V) 4. On-Resistance vs with Gate to Source Voltage



VGS- Gate to Source Voltage (V) 6.Body Diode Forward Voltage Variation with source Current and Temperature

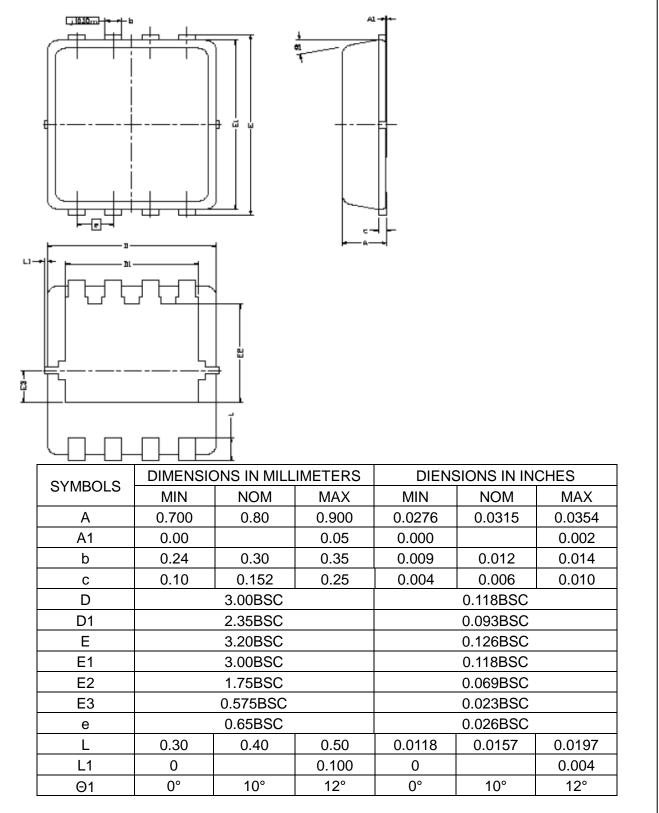


### **Typical Performance Characteristics**



11. Transient Thermal Response Curve





Unit: mm



#### Notes

ACE does not assume any responsibility for use as critical components in life support devices or systems without the express written approval of the president and general counsel of ACE Electronics Co., LTD. As sued herein:

- 1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and shoes failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
- 2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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