ACNT-H313

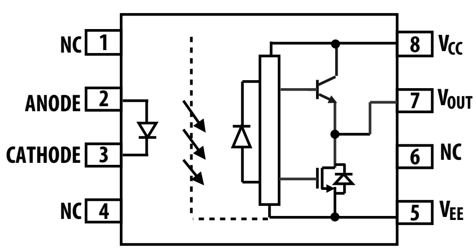
2.5 A Output Current IGBT Gate Drive Optocoupler in 15 mm Stretched SO8 Package

Data Sheet

Description

The Avago Technologies ACNT-H313 contains an LED, which is optically coupled to an integrated circuit with a power output stage. This optocoupler is ideally suited for driving power IGBTs and MOSFETs used in motor control inverter applications. The high operating voltage range of the output stage provides the drive voltages required by gate-controlled devices. The voltage and high peak output current supplied by this optocoupler can be used to IGBT directly. For IGBTs with higher ratings, this optocoupler can be used to drive a discrete power stage, which drives the IGBT gate. The ACNT-H313 has the highest insulation voltage of V_{IORM} = 2262 V_{PEAK} in the IEC/EN/DIN EN 60747-5-5.

Functional Diagram



NOTE NC denotes Not Connected, and a 0.1 µF bypass capacity must be connected between pins V_{CC} and V_{EE}.

Truth Table

LED	$V_{CC} – V_{EE}$ "POSITIVE GOING" (i.e., TURN-ON)	$V_{CC} – V_{EE}$ "NEGATIVE GOING" (i.e., TURN-OFF)	V_O
OFF	0 - 30 V	0 – 30 V	LOW
ON	0 – 11 V	0 – 9.5 V	LOW
ON	11 - 13.5 V	9.5 – 12 V	TRANSITION
ON	13.5 – 30 V	12 – 30 V	HIGH

Features

- 2.5 A maximum peak output current
- 2.0 A minimum peak output current
- 500 ns maximum propagation delay
- 350 ns maximum propagation delay difference
- 40 kV/µms minimum Common Mode Rejection (CMR) at V_{CM} = 2000 V
- I_{CC} = 5.0 mA maximum supply current
- Under Voltage Lock-Out protection (UVLO) with hysteresis
- Wide operating V_{CC} Range: 15 V to 30 V
- Industrial temperature range: -40°C to 105°C
- Safety Approval
  - UL Recognized 7500 V_{RMS} for 1 min
  - CSA
  - IEC/EN/DIN EN 60747-5-5 VIORM = 2262 VPEAK

Applications

- High Power System – 690V_{AC} Drives
- IGBT/MOSFET gate drive
- AC and Brushless DC motor drives
- Renewable energy inverters
- Industrial inverters
- Switching power supplies

CAUTION It is advised that normal static precautions be taken in handling and assembly of this component to prevent damage and/or degradation that may be induced by ESD. The components featured in this data sheet are not to be used in military or aerospace applications or environments.

Ordering Information

ACNT-H313 is UL Recognized with 7500 V_{RMS} for 1 minute per UL1577.

Part Number	Option	Package	Surface Mount	Tape & Reel	IEC/EN/DIN EN 60747-5-5 VIORM=2262 VPEAK	Quantity
	RoHS Compliant					
ACNT-H313	-000E	15 mm Stretched SO-8	X		X	80 per tube
	-500E		X	X	X	1000 per reel

To order, choose a part number from the part number column and combine with the desired option from the option column to form an order entry.

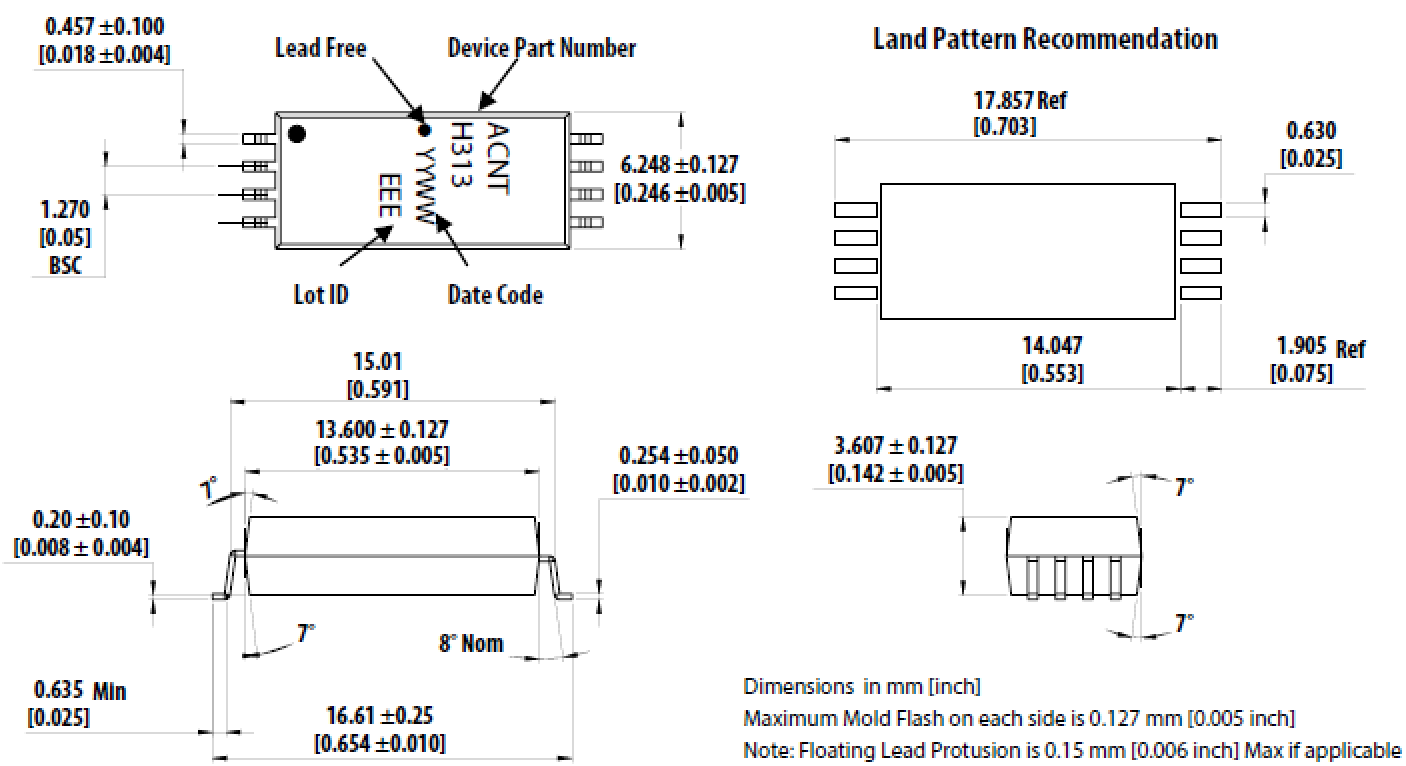
Example 1:

ACNT-H313-500E to order a product in Surface Mount package in Tape and Reel packaging with IEC/EN/DIN EN 60747-5-5 Safety Approval and RoHS compliant.

Option data sheets are available. Contact your Avago sales representative or authorized distributor for information.

Package Outline Drawings

ACNT-H313 Outline Drawing



Recommended Pb-Free IR Profile

Recommended reflow condition as per JEDEC Standard, J-STD-020 (latest revision). Non- Halide Flux should be used.

Regulatory Information

The ACNT-H313 is approved by the following organizations.

UL	Recognized under UL 1577, component recognition program up to V_{ISO} = 7500 V_{RMS}, File E55361
CSA	CSA Component Acceptance Notice #5, File CA 88324
IEC/EN/DIN EN 60747-5-5	Maximum Working Insulation Voltage V_{IORM} = 2262 V_{PEAK}

Table 1. IEC/EN/DIN EN 60747-5-5 Insulation Characteristics (See Note)

Description	Symbol	Characteristic	Unit
Installation classification per DIN VDE 0110/39, Table 1 for rated mains voltage ≤ 600 Vrms for rated mains voltage ≤1000 Vrms		 I-IV I-IV	
Climatic Classification		40/105/21	
Pollution Degree (DIN VDE 0110/39)		2	
Maximum Working Insulation Voltage	V_{IORM}	2262	V_{PEAK}
Input to Output Test Voltage, Method b[a] $V_{IORM} \times 1.875 = V_{PR}$, 100% Production Test with t_m=1 sec, Partial discharge < 5 pC	VPR	4242	V_{PEAK}
Input to Output Test Voltage, Method a* $V_{IORM} \times 1.6 = V_{PR}$, Type and Sample Test, t_m=10 sec, Partial discharge < 5 pC	VPR	3619	V_{PEAK}
Highest Allowable Overvoltage[a] Transient Overvoltage t_{ini} = 60 sec)	VIOTM	12000	VPEAK
Safety-limiting values – maximum values allowed in the event of a failure Case Temperature Input Current Output Power	 TS $I_{S, INPUT}$ $P_{S, OUTPUT}$	 175 230 1000	 °C mA mW
Insulation Resistance at T_S, V_{IO} = 500 V	R_S	$>10^9$	Ω

a. Refer to IEC/EN/DIN EN 60747-5-5 Optoisolator Safety Standard section of the Avago Regulatory Guide to Isolation Circuits, AV02-2041EN for a detailed description of Method a and Method b partial discharge test profiles.

NOTE These optocouplers are suitable for “safe electrical isolation” only within the safety limit data. Maintenance of the safety data shall be ensured by means of protective circuits. Surface mount classification is Class A in accordance with CECC 00802.

Table 2. Insulation and Safety Related Specifications

Parameter	Symbol	ACNT-H313	Units	Conditions
Minimum External Air Gap (Clearance)	L(101)	14.2	mm	Measured from input terminals to output terminals, shortest distance through air.
Minimum External Tracking (Creepage)	L(102)	15	mm	Measured from input terminals to output terminals, shortest distance path along body.
Minimum Internal Plastic Gap (Internal Clearance)		0.5	mm	Through insulation distance conductor to conductor, usually the straight line distance thickness between the emitter and detector.
Tracking Resistance (Comparative Tracking Index)	CTI	> 300	V	DIN IEC 112/VDE 0303 Part 1
Isolation Group		IIIa		Material Group (DIN VDE 0110, 1/89, Table 1)

NOTE All Avago data sheets report the creepage and clearance inherent to the optocoupler component itself. These dimensions are needed as a starting point for the equipment designer when determining the circuit insulation requirements. However, once mounted on a printed circuit board, minimum creepage and clearance requirements must be met as specified for individual equipment standards. For creepage, the shortest distance path along the surface of a printed circuit board between the solder fillets of the input and output leads must be considered (the recommended Land Pattern does not necessarily meet the minimum creepage of the device). There are recommended techniques such as grooves and ribs which may be used on a printed circuit board to achieve desired creepage and clearances. Creepage and clearance distances will also change depending on factors such as pollution degree and insulation level.

Table 3. Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Units	Note
Storage Temperature	T_S	–55	125	°C	
Operating Temperature	T_A	–40	105	°C	
Average Input Current	$I_{F(AVG)}$		25	mA	a
Reverse Input Voltage	V_R		5	V	
"High" Peak Output Current	$I_{OH(PEAK)}$		2.5	A	b
"Low" Peak Output Current	$I_{OL(PEAK)}$		2.5	A	b
Total Output Supply Voltage	$(V_{CC} – V_{EE})$	0	35	V	
Input Current (Rise/Fall Time)	$t_{r(IN)}$ / $t_{f(IN)}$		500	ns	
Output Voltage	$V_{O(PEAK)}$	–0.5	VCC	V	
Output IC Power Dissipation	P_O		800	mW	c
Total Power Dissipation	P_T		850	mW	d

a. Derate linearly above 70°C free-air temperature at a rate of 0.3 mA/°C.

b. Maximum pulse width = 10 ms. This value is intended to allow for component tolerances for designs with I_O peak minimum = 2.0 A. See applications section for additional details on limiting I_{OH} peak.

c. Derate linearly above 85°C free-air temperature at a rate of –20 mW/ °C.

d. Derate linearly above 85 °C free-air temperature at a rate of –21.25 mW/ °C. The maximum LED junction temperature should not exceed 125°C.

Table 4. Recommended Operating Conditions

Parameter	Symbol	Min	Max.	Units	Note
Operating Temperature	T_A	–40	105	°C	
Output Supply Voltage	$(V_{CC} – V_{EE})$	15	30	V	
Input Current (ON)	$I_{F(ON)}$	7	12	mA	
Input Voltage (OFF)	$V_{F(OFF)}$	–3.6	0.5	V	

Table 5. Electrical Specifications (DC)

All typical values are at T_A = 25°C, $V_{CC} – V_{EE}$ = 30 V, V_{EE} = Ground. All minimum and maximum specifications are at recommended operating conditions (T_A = –40 to 105°C, $I_{F(ON)}$ = 7 to 12 mA, $V_{F(OFF)}$ = –3.6 to 0.8 V, V_{EE} = Ground, V_{CC} = 15 to 30 V), unless otherwise noted.

Parameter	Symbol	Min.	Typ.	Max.	Units	Test Conditions	Fig.	Note
High Level Peak Output Current	I_{OH}	0.5	1.5		A	$V_O = V_{CC} – 4$ V	2, 3, 16	a
		2.0			A	$V_O = V_{CC} – 15$ V		b
Low Level Peak Output Current	I_{OL}	0.5	2.0		A	$V_O = V_{EE} + 2.5$ V	5, 6, 17	a
		2.0			A	$V_O = V_{EE} + 15$ V		b
High Level Output Voltage	V_{OH}	V_{CC}– 4	V_{CC} – 3		V	I_O = –100 mA	1, 3, 18	c, d
Low Level Output Voltage	V_{OL}		0.1	0.5	V	I_O = 100 mA	4, 6, 19	
High Level Supply Current	I_{CCH}		2.5	5.0	mA	Output Open, I_F = 10 mA	7, 8	
Low Level Supply Current	I_{CCL}		2.5	5.0	mA	Output Open, V_F = –3.6 to 0.8 V		
Threshold Input Current Low to High	I_{FLH}		1.0	5.0	mA	I_O = 0 mA, V_O > 5 V	9, 15, 20	
Threshold Input Voltage High to Low	V_{FHL}	0.5			V			
Input Forward Voltage	V_F	1.2	1.45	1.8	V	I_F = 10 mA		
Temperature Coefficient of Input Forward Voltage	$\Delta V_F/\Delta T_A$		–1.5		mV/°C	I_F = 10 mA		
Input Reverse Breakdown Voltage	BV_R	3			V	I_R = 100 µA		
Input Capacitance	C_{IN}		23		pF	f = 1 MHz, V_F = 0 V		
UVLO Threshold	V_{UVLO+}	11.0	12.3	13.5	V	V_O > 5 V, I_F = 10 mA	21	
	V_{UVLO-}	9.5	10.7	12.0				
UVLO Hysteresis	$UVLO_{HYS}$		1.6					

a. Maximum pulse width = 50 ms.

b. Maximum pulse width = 10 ms. This value is intended to allow for component tolerances for designs with I_O peak minimum = 2.0 A. See applications section for additional details on limiting I_{OH} peak.

c. In this test, V_{OH} is measured with a DC load current. When driving capacitive loads, V_{OH} will approach V_{CC} as I_{OH} approaches zero amps.

d. Maximum pulse width = 1 ms.

Table 6. Switching Specifications (AC)

All typical values are at T_A = 25°C, V_{CC}– V_{EE} = 30 V, V_{EE} = Ground. All minimum and maximum specifications are at recommended operating conditions (T_A = –40 to 105°C, $I_{F(ON)}$ = 7 to 12 mA, $V_{F(OFF)}$ = –3.6 to 0.8 V, V_{EE} = Ground, V_{CC} = 15 to 30 V), unless otherwise noted.

Parameter	Symbol	Min.	Typ.	Max.	Units	Test Conditions	Fig.	Note
Propagation Delay Time to High Output Level	t_{PLH}	0.10	0.28	0.50	µs	Rg = 10 Ω, C_g = 10 nF, f = 10 kHz, Duty Cycle = 50%, I_F = 7 mA to 12 mA, V_{CC} = 15 V to 30 V	10, 11, 12, 13, 14, 22	
Propagation Delay Time to Low Output Level	t_{PHL}	0.10	0.30	0.50	µs			
Pulse Width Distortion	PWD			0.30	µs			a
Propagation Delay Difference Between Any Two Parts	PDD ($t_{PHL} - t_{PLH}$)	–0.35		0.35	µs			b
Propagation Delay Skew	t_{PSK}			0.20	µs			c
Rise Time	t_R		0.10		µs		22	
Fall Time	t_F		0.10		µs			
UVLO Turn On Delay	$t_{UVLO\ ON}$		0.80		µs	V_O > 5 V, I_F = 10 mA	21	
UVLO Turn Off Delay	$t_{UVLO\ OFF}$		0.60		µs	V_O < 5 V, I_F = 10 mA		
Output High Level Common Mode Transient Immunity	$\|CM_H\|$	40	50		kV/µs	T_A = 25 °C, I_F = 10 mA, V_{CM} = 2000 V, V_{CC} = 30 V	23	d, e
Output Low Level Common Mode Transient Immunity	$\|CM_L\|$	40	50		kV/µs	T_A = 25 °C, V_F = 0 V, V_{CM} = 2000 V, V_{CC} = 30 V		d, f

a. Pulse Width Distortion (PWD) is defined as $|t_{PH}L– t_{PLH}|$ for any given device.

b. The difference between t_{PHL} and t_{PLH} between any two ACNT-H313 parts under the same test condition.

c. t_{PSK} is equal to the worst-case difference in t_{PHL} or t_{PLH} that will be seen between units at any given temperature and specified test conditions.

d. Pin 1 and 4 need to be connected to LED common. Split resistor network in the ratio 1.5:1 with 215 W at the anode and 140 W at the cathode.

e. Common mode transient immunity in the high state is the maximum tolerable dV_{CM}/dt of the common mode pulse, V_{CM}, to assure that the output will remain in the high state (i.e., V_O > 15.0 V).

f. Common mode transient immunity in a low state is the maximum tolerable dV_{CM}/dt of the common mode pulse, V_{CM}, to assure that the output will remain in a low state (i.e., V_O < 1.0 V).

Table 7. Package Characteristics

All typical values are at T_A = 25°C. All minimum/maximum specifications are at recommended operating conditions, unless otherwise noted.

Parameter	Symbol	Min.	Typ.	Max.	Units	Test Conditions	Fig.	Note
Input-Output Momentary Withstand Voltage[a]	V_{ISO}	7500			V_{RMS}	RH < 50%, t = 1 min., T_A = 25°C		b, c
Input-Output Resistance	R_{I-O}		10^{12}		Ω	V_{I-O} = 500 V_{DC}		c
Input-Output Capacitance	C_{I-O}		0.5		pF	f =1 MHz		
LED-to-Ambient Thermal Resistance	R_{11}		87		°C/W	Thermal Model in Application Notes below		d
LED-to-Detector Thermal Resistance	R_{12}		23					
Detector-to-LED Thermal Resistance	R_{21}		30					
Detector-to-Ambient Thermal Resistance	R_{22}		47					

a. The Input-Output Momentary Withstand Voltage is a dielectric voltage rating that should not be interpreted as an input-output continuous voltage rating. For the continuous voltage rating, refer to your equipment level safety specification or Avago Technologies Application Note 1074, *Optocoupler Input-Output Endurance Voltage*.

b. In accordance with UL1577, each optocoupler is proof tested by applying an insulation test voltage ≥ 9000 V_{RMS} for 1 second (leakage detection current limit, I_{I-O} ≤ 5 μA).

c. Device considered a two-terminal device: pins 1, 2, 3, and 4 shorted together and pins 5, 6, 7, and 8 shorted together.

d. The device was mounted on a high conductivity test board as per JEDEC 51-7.

Figure 1 V_{OH} vs. Temperature

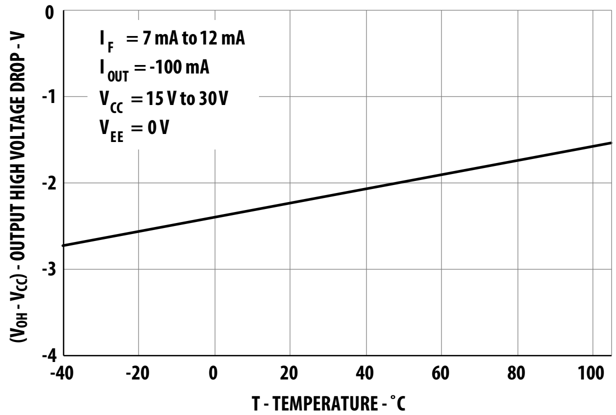


Figure 2 I_{OH} vs. Temperature

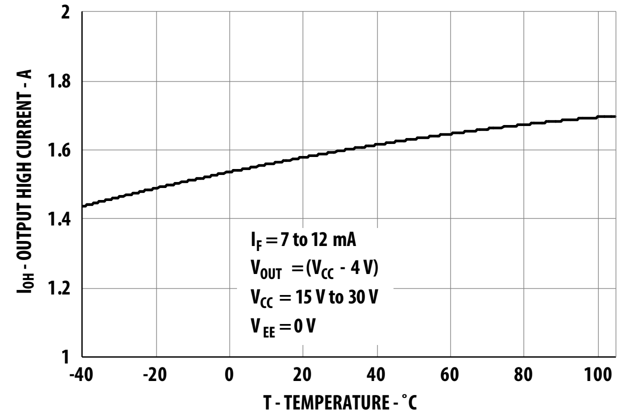


Figure 3 I_{OH} vs. V_{OH}

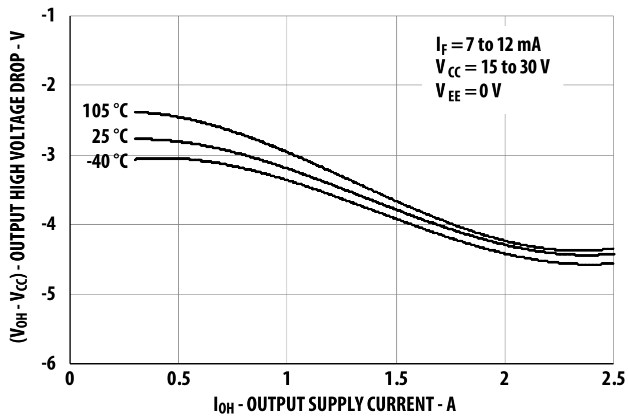


Figure 4 V_{OL} vs. Temperature

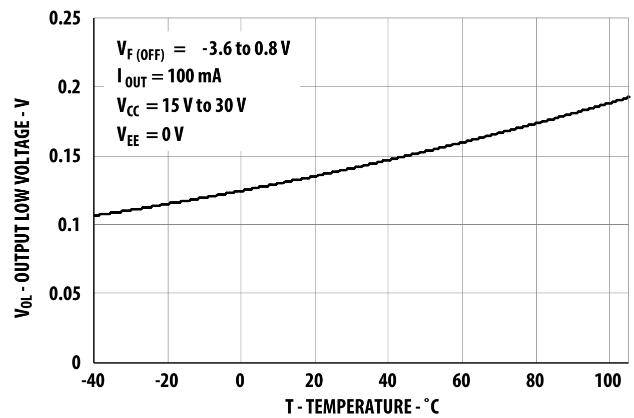


Figure 5 I_{OL} vs. Temperature

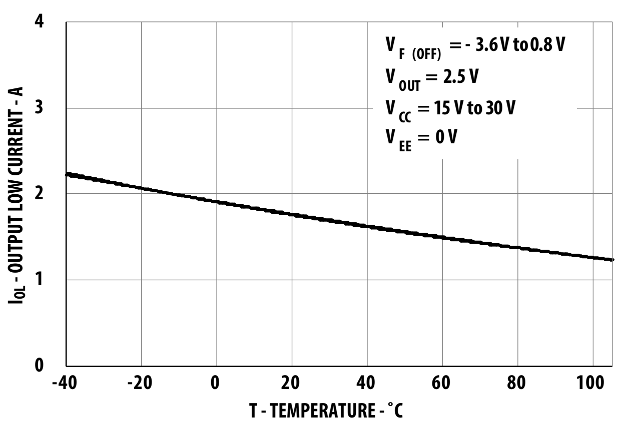


Figure 6 V_{OL} vs. I_{OL}

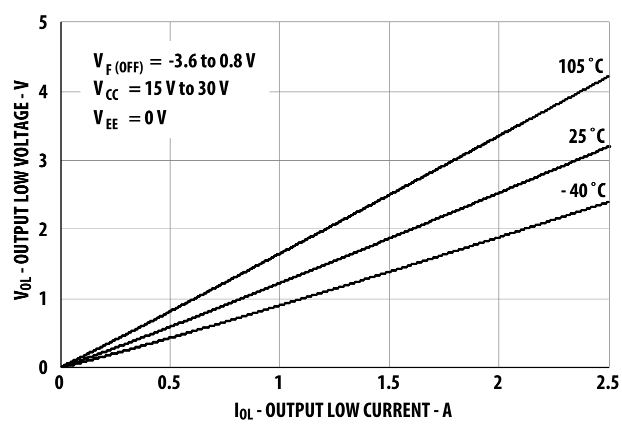


Figure 7 I_{CC} vs. Temperature

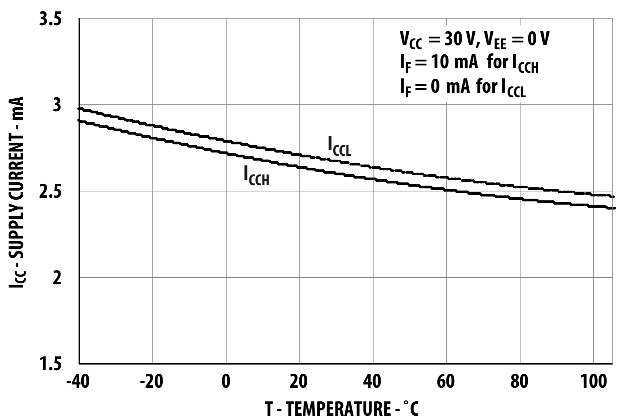


Figure 8 I_{CC} vs. V_{CC}

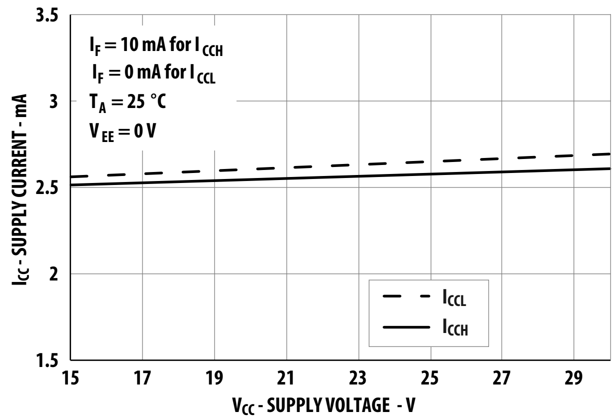


Figure 9 I_{FLH} vs. Temperature

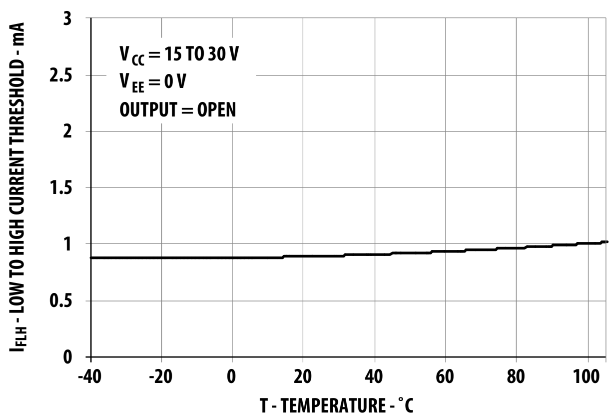


Figure 10 Propagation Delay s. V_{CC}

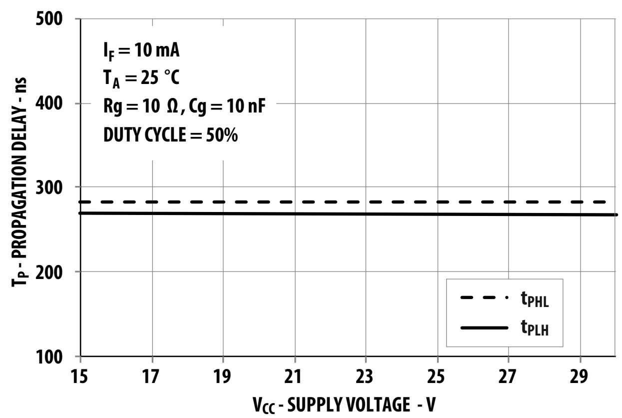


Figure 11 Propagation Delay vs. I_F

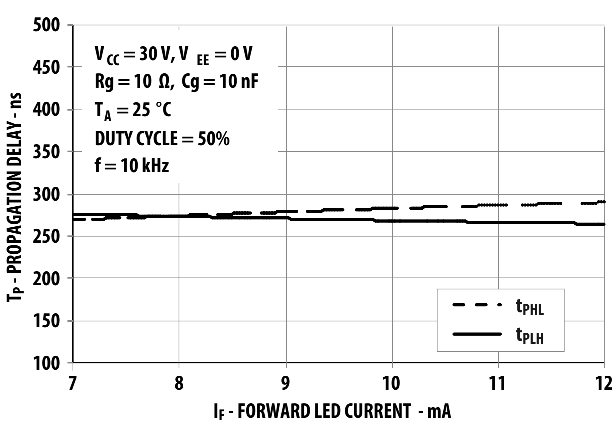


Figure 12 Propagation Delay vs. Temperature

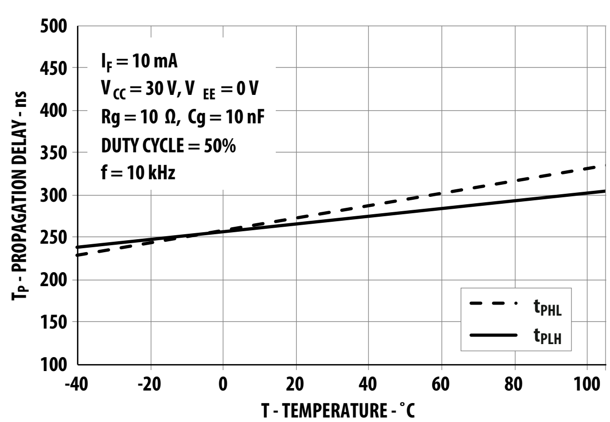


Figure 13 Propagation Delay vs. Rg

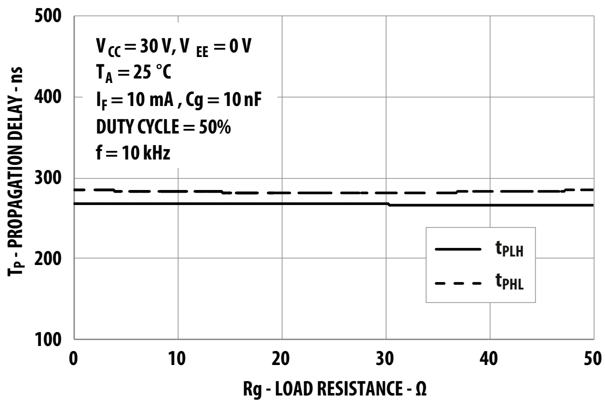


Figure 14 Propagation Delay vs. Cg

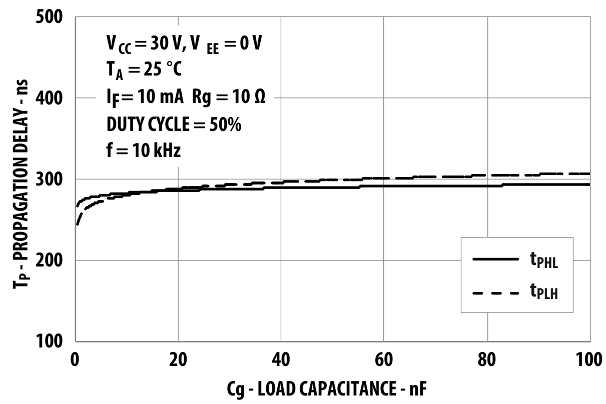


Figure 15 Transfer Characteristics

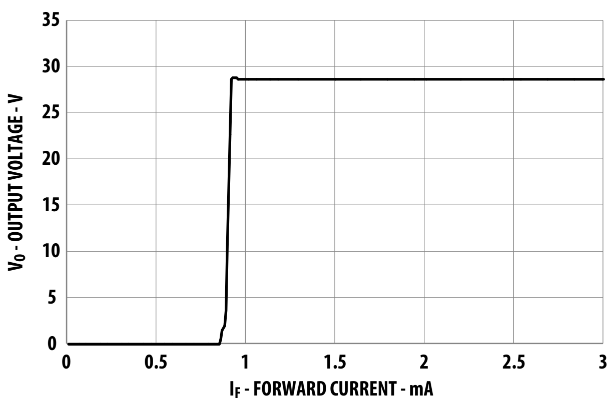


Figure 16 I_{OL} Test Circuit

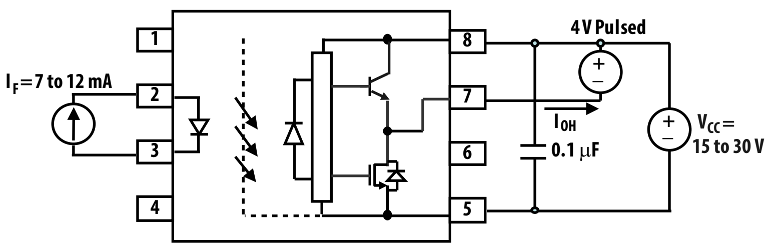


Figure 17 I_{OH} Test Circuit

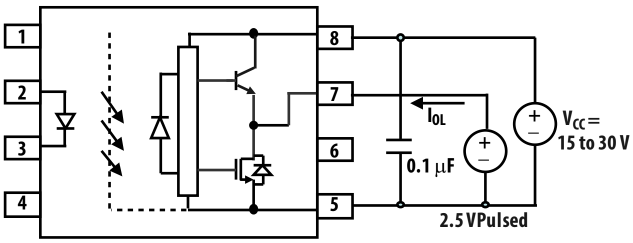


Figure 18 V_{OH} Test Circuit

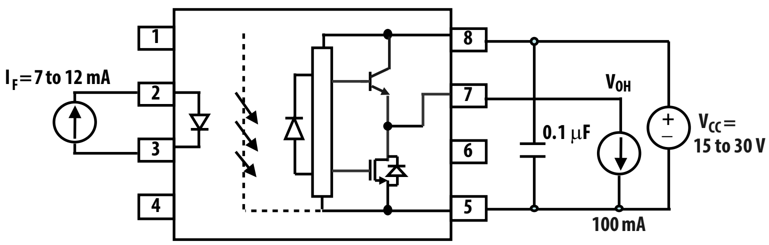


Figure 19 V_{OL} Test Circuit

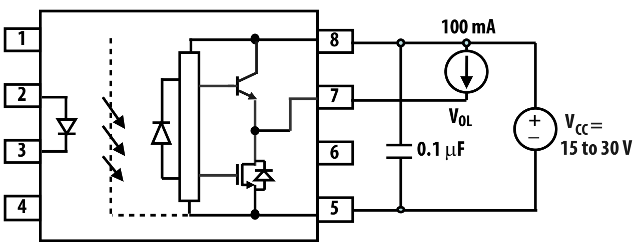


Figure 20 I_{FLH} Test Circuit

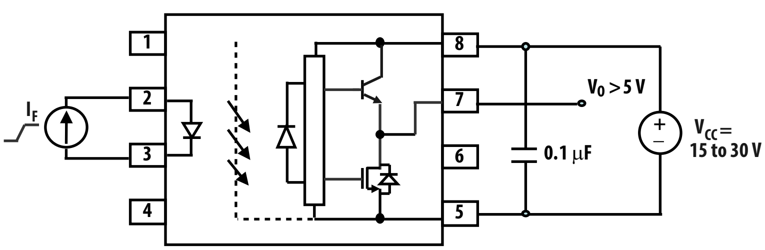


Figure 21 ULVO Test Circuit

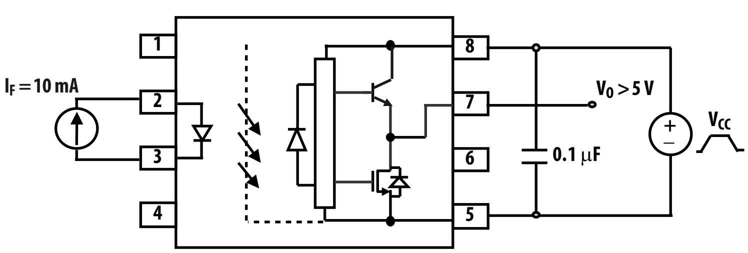


Figure 22 TPLH, tPHL, Tr and tf Test Circuit and Waveforms

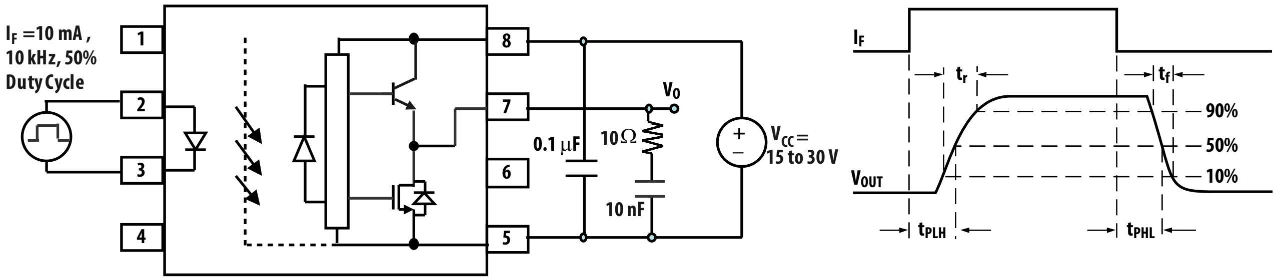
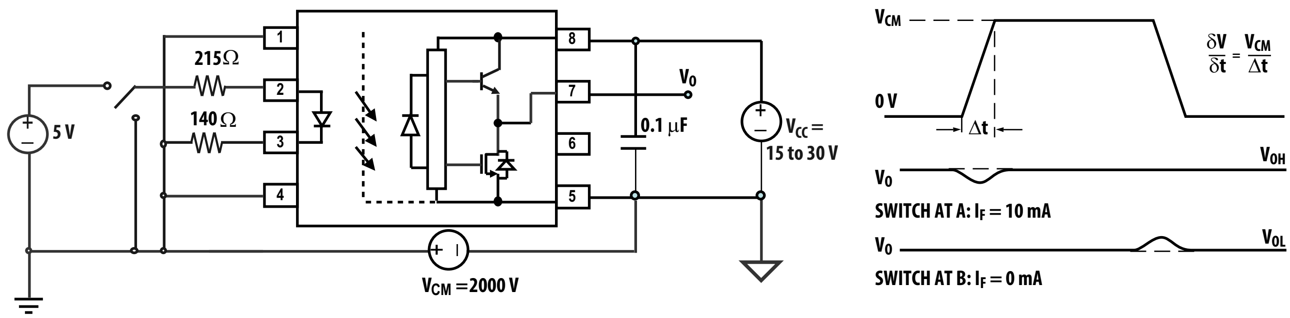


Figure 23 CMR Test Circuit and Waveforms



Applications Information

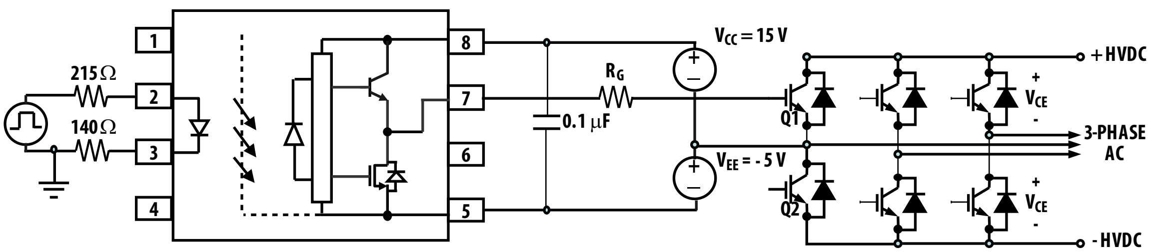
Selecting the Gate Resistor (R_g) to Minimize IGBT Switching Losses

Step 1: Calculate R_g minimum from the IOL peak specification. The IGBT and R_g in Figure 24 can be analyzed as a simple RC circuit with a voltage supplied by the ACNT-H313.

$$R_g \geq \frac{V_{CC} - V_{EE} - V_{OL}}{I_{OLPEAK}} = \frac{15 + 5 - 2}{2.5} = 7.2\Omega \cong 8\,\Omega$$

The V_{OL} value of 2 V in the previous equation is a conservative value of V_{OL} at the peak current of 2.5 A (see Figure 6). At lower R_g values, the voltage supplied by the ACNT-H313 is not an ideal voltage step. This results in lower peak currents (more margin) than predicted by this analysis. When negative gate drive is not used V_{EE} in the previous equation is equal to 0 V.

Figure 24 ACNT-H313 Typical Application Circuit



Step 2: Check the ACNT-H313 Power Dissipation and Increase R_g if necessary. The ACNT-H313 total power dissipation (P_T) is equal to the sum of the emitter power (P_E) and the output power (P_O).

$$P_T = P_E + P_O$$
$$P_E = I_F \cdot V_F \cdot DutyCycle$$
$$P_O = P_{O(BIAS)} + P_{O(SWITCHING)} = I_{CC} \cdot V_{CC} + E_{SW}(R_g, Q_g) \cdot f$$

P_E Parameter	Description
I_F	LED current
V_F	LED-on voltage
Duty Cycle	Maximum LED duty cycle

P_O Parameter	Description
I_{CC}	Supply current
V_{CC}	Positive supply voltage
V_{EE}	Negative supply voltage
$E_{SW}(R_g, Qg)$	Energy dissipated in the ACNT-H313 for each IGBT switching cycle (see Figure 25)
f	Switching frequency

For the circuit in Figure 24 with I_F (worst case) = 12 mA, R_g = 8 Ω, Max Duty Cycle = 80%, Q_g = 500 nC, f = 20 kHz and T_A max = 85°C.

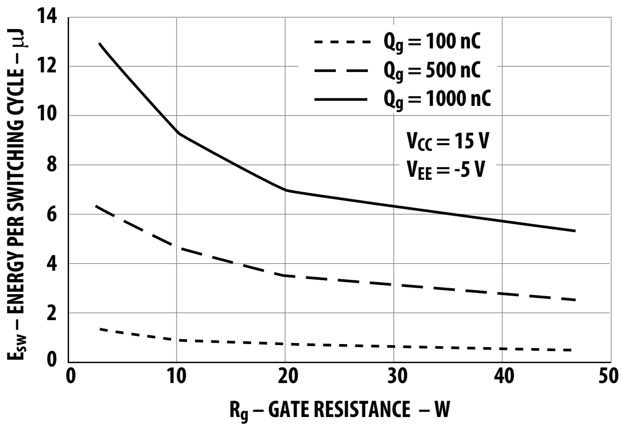
$$P_E = 12\ mA \cdot 1.8\ V \cdot 0.8 = 17.3\ mW$$

$$\begin{aligned} P_O &= 4.25\ mA \cdot 20\ V + 5.2\ \mu J \cdot 20\ kHz \\ &= 85\ mW + 104\ mW \\ &= 189\ mW \\ &< 800\ mW\ (P_{O(MAX)}\ @\ 85°C) \end{aligned}$$

The value of 4.25 mA for I_{CC} in the previous equation was obtained by derating the I_{CC} max of 5 mA (which occurs at –40°C) to I_{CC} max at 85°C (see Figure 7).

Since P_O for this case is smaller than $P_{O(MAX)}$, R_g of 8 Ω can be used.

Figure 25 Energy Dissipated in the ACNT-H313 for Each IGBT Switching Cycle



Thermal Model

Definitions:

R_{11}: Junction-to-Ambient Thermal Resistance of LED due to heating of LED

R_{12}: Junction-to-Ambient Thermal Resistance of LED due to heating of Detector (Output IC)

R_{21}: Junction-to-Ambient Thermal Resistance of Detector (Output IC) due to heating of LED

R_{22}: Junction-to-Ambient Thermal Resistance of Detector (Output IC) due to heating of Detector (Output IC)

P_1: Power dissipation of LED (W)

P_2: Power dissipation of Detector/Output IC (W)

T_1: Junction temperature of LED (°C)

T_2: Junction temperature of Detector (°C)

T_A: Ambient temperature

Ambient Temperature: Junction-to-Ambient Thermal Resistances were measured approximately 1.25 cm above optocoupler at ~23°C in still air.

Thermal Resistance	°C/W
R_{11}	87
R_{12}	23
R_{21}	30
R_{22}	47

This thermal model assumes the device is soldered onto a high conductivity board as per JEDEC 51-7. The temperature at the LED and Detector junctions of the optocoupler can be calculated using the following equations:

$T_1 = (R_{11} \times P_1 + R_{12} \times P_2) + T_A$ -- (1)

$T_2 = (R_{21} \times P_1 + R_{22} \times P_2) + T_A$ -- (2)

Using the given thermal resistances and thermal model formula in this datasheet, we can calculate the junction temperature for both LED and the output detector. Both junction temperatures should be within the absolute maximum rating of 125°C.

Related Documents

AV02-0421EN	Application Note 5336	*Gate Drive Optocoupler Basic Design for IGBT / MOSFET*
AV02-3698EN	Application Note 1043	*Common-Mode Noise: Sources and Solutions*
AV02-0310EN	Reliability Data	*Plastics Optocouplers Product ESD and Moisture Sensitivity*

AV02-4249EN – October 7, 2016