# ACR400SE



# **Fast Turn-off Asymmetric Thyristor**

Replaces March 1998 version, DS4202-3.1

DS4202-4.0 January 2000

**KEY PARAMETERS** 

 $V_{DRM}$ 

I<sub>T(AV)</sub>

I<sub>TSM</sub> dVdt

dl/dt

tq

1800V

380A

6000A

1000V/μs

**500A/**μs **10.0**μs

## **APPLICATIONS**

- High Frequency Applications
- Regulated Power Supplies
- Capacitor Discharge
- Ultrasonic Generators
- Induction Heating

#### **FEATURES**

■ The ACR400SE is an asymmetric thyristor which has exceptionally fast turn-off capabilities combined with good turn-on characteristics.

#### **VOLTAGE RATINGS**

| Type Number | Repetitive Peak<br>Off-state Voltage<br>V | Repetitive Peak<br>Reverse Voltage<br>V <sub>RRM</sub><br>V |
|-------------|---|---|
| ACR400SE18  | 1800                                      | 10  |
| ACR400SE16  | 1600                                      | 10  |
| ACR400SE14  | 1400                                      | 10  |
| ACR400SE12  | 1200                                      | 10  |
|             |   |   |
| 1           |   |   |

Lower voltage grades available.

Outline type code: E. See Package Details for further information.

## **CURRENT RATINGS**

| Symbol              | Parameter                            | Conditions   | Max. | Units |  |  |  |
|---------------------|--------------------------------------|--|------|-------|--|--|--|
| Double Sid          | Double Side Cooled                   |  |      |       |  |  |  |
| I <sub>T(AV)</sub>  | Mean on-state current                | Half wave resistive load, T <sub>case</sub> = 80°C | 380  | А     |  |  |  |
| I <sub>T(RMS)</sub> | RMS value                            | T <sub>case</sub> = 80°C                           | 600  | А     |  |  |  |
| I <sub>T</sub>      | Continuous (direct) on-state current | T <sub>case</sub> = 80°C                           | 490  | А     |  |  |  |
| Single Side         | Single Side Cooled (Anode side)      |  |      |       |  |  |  |
| I <sub>T(AV)</sub>  | Mean on-state current                | Half wave resistive load, T <sub>case</sub> = 80°C | 240  | А     |  |  |  |
| I <sub>T(RMS)</sub> | RMS value                            | T <sub>case</sub> = 80°C                           | 380  | А     |  |  |  |
| I <sub>T</sub>      | Continuous (direct) on-state current | T <sub>case</sub> = 80°C                           | 295  | Α     |  |  |  |

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# **SURGE RATINGS**

| Symbol           | Parameter                               | Conditions                                | Max.                  | Units |
|------------------|---|---|-----------------------|-------|
| I <sub>TSM</sub> | Surge (non-repetitive) on-state current | 10ms half sine; T <sub>case</sub> = 125°C | 6.0                   | kA    |
| l <sup>2</sup> t | I <sup>2</sup> t for fusing             | $V_R = 0$                                 | 810 x 10 <sup>3</sup> | A²s   |

# THERMAL AND MECHANICAL DATA

| Symbol               | Parameter                             | Conditions                                  |             | Min. | Max.  | Units |
|----------------------|---------------------------------------|---|-------------|------|-------|-------|
| R <sub>th(j-c)</sub> | Thermal resistance - junction to case | Double side cooled                          | dc          | -    | 0.041 | °C/W  |
|                      |                                       | Single side cooled                          | Anode dc    | -    | 0.074 | °C/W  |
|                      |                                       |   | Cathode dc  | -    | 0.092 | °C/W  |
| R <sub>th(c-h)</sub> | Thermal resistance - case to heatsink | Clamping force 7.0kN with mounting compound | Double side | -    | 0.018 | °C/W  |
|                      |                                       |   | Single side | -    | 0.036 | °C/W  |
| $T_{v_{j}}$          | Virtual junction temperature          | On-state (conducting)                       |             | -    | 135   | °C    |
|                      |                                       | Reverse (blocking)                          |             | -    | 125   | °C    |
| T <sub>stg</sub>     | Storage temperature range             |   |             | -55  | 125   | °C    |
| -                    | Clamping force                        |   |             | 6.0  | 8.0   | kN    |

## **DYNAMIC CHARACTERISTICS**

| Symbol                             | Parameter  | Conditions   | Min. | Max. | Units |
|------------------------------------|--|--|------|------|-------|
| V <sub>TM</sub>                    | Maximum on-state voltage                         | At 1500A peak, T <sub>case</sub> = 25°C  | -    | 3.25 | V     |
| I <sub>RRM</sub> /I <sub>DRM</sub> | Peak reverse and off-state current               | At V <sub>RRM</sub> /V <sub>DRM</sub> , T <sub>case</sub> = 125°C  | -    | 60   | mA    |
| dV/dt                              | Maximum linear rate of rise of off-state voltage | To $V_{DRM}$ $T_j$ = 125°C, Gate open circuit  | -    | 1000 | V/μs  |
| dI/dt                              | Rate of rise of on-state current                 | From $V_{DRM}$ to 125A<br>Gate source 15V, 15 $\Omega$<br>$t_r = 50$ ns.   | -    | 500  | A/μs  |
| $V_{T(TO)}$                        | Threshold voltage                                | At T <sub>vj</sub> = 125°C   | -    | 1.8  | V     |
| r <sub>T</sub>                     | On-state slope resistance                        | At T <sub>vj</sub> = 125°C   | -    | 0.9  | mΩ    |
| IL                                 | Latching current                                 | -  | 500  | -    | mA    |
| I <sub>H</sub>                     | Holding current                                  | -  | 100  | -    | mA    |
| t <sub>d</sub>                     | Delay time                                       | $V_D = 300V$ , gate source = 15V, 15 $\Omega$  | -    | 800  | ns    |
| t <sub>q</sub>                     | Turn-off time                                    | $I_T$ = 500A, square wave 50μs pulse, $T_j$ = 120°C, $dI_{RR}/dt$ = 50A/μs, $dV/dt$ = 500V/μs to 75% $V_{DRM}$ . | -    | 10.0 | μs    |

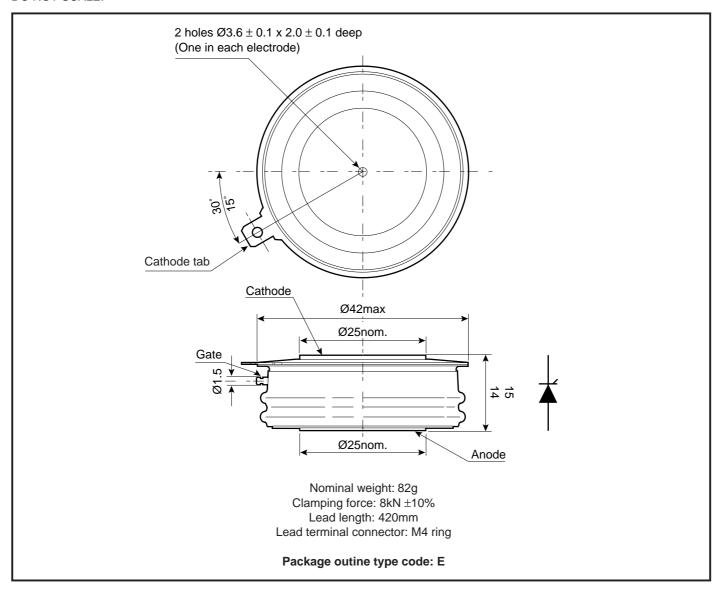
# **GATE TRIGGER CHARACTERISTICS AND RATINGS**

| Symbol           | Parameter                 | Conditions  |         | Тур. | Max. | Units |
|------------------|---------------------------|---|---------|------|------|-------|
| $V_{\rm GT}$     | Gate trigger voltage      | $V_{\text{DWM}} = 12V, R_{\text{L}} = 30\Omega, T_{\text{case}} = 25^{\circ}\text{C}$ |         | -    | 5    | V     |
| I <sub>GT</sub>  | Gate trigger current      | $V_{DWM} = 12V, R_{L} = 30\Omega, T_{case} = 25^{\circ}C$                             |         | -    | 500  | mA    |
| $V_{FGM}$        | Peak forward gate voltage | -   |         | -    | 40   | V     |
| $V_{RGM}$        | Peak reverse gate voltage | -   |         | -    | 10   | V     |
| I <sub>FGM</sub> | Peak forward gate current | -   |         | -    | 10   | Α     |
| $P_{GM}$         | Peak gate power           | -   |         | -    | 40   | W     |
|                  |                           |   | Forward | -    | 10   | W     |
| $P_{G(AV)}$      | Average gate power        | Average time 10ms max   | Reverse | -    | -    | W     |

# ACR400SE

## **PACKAGE DETAILS**

For further package information, please contact your local Customer Service Centre. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



## **ASSOCIATED PUBLICATIONS**

| Title  | Application Note |  |
|--|------------------|--|
|  | Number           |  |
| Calculating the junction temperature or power semiconductors | AN4506           |  |
| Gate triggering and the use of gate characteristics          | AN4840           |  |
| Recommendations for clamping power semiconductors            | AN4839           |  |
| The effect of temperature on thyristor performance           | AN4870           |  |
| Thyristor and diode measurement with a multi-meter           | AN4853           |  |
| Turn-on performance of thyristors in parallel                | AN4999           |  |
| Use of $V_{TO}$ , $r_{T}$ on-state characteristic            | AN5001           |  |

#### POWER ASSEMBLY CAPABILITY

The Power Assembly group was set up to provide a support service for those customers requiring more than the basic semiconductor, and has developed a flexible range of heatsink / clamping systems in line with advances in device types and the voltage and current capability of our semiconductors.

We offer an extensive range of air and liquid cooled assemblies covering the full range of circuit designs in general use today. The Assembly group continues to offer high quality engineering support dedicated to designing new units to satisfy the growing needs of our customers.

Using the up to date CAD methods our team of design and applications engineers aim to provide the Power Assembly Complete solution (PACs).

#### **DEVICE CLAMPS**

Disc devices require the correct clamping force to ensure their safe operation. The PACs range offers a varied selection of preloaded clamps to suit all of our manufactured devices. This include cube clamps for single side cooling of 'T' 22mm

Clamps are available for single or double side cooling, with high insulation versions for high voltage assemblies.

Please refer to our application note on device clamping, AN4839

#### **HEATSINKS**

Power Assembly has it's own proprietary range of extruded aluminium heatsinks. They have been designed to optimise the performance or our semiconductors. Data with respect to air natural, forced air and liquid cooling (with flow rates) is available on request.

For further information on device clamps, heatsinks and assemblies, please contact your nearest Sales Representative or the factory.



#### http://www.dynexsemi.com

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