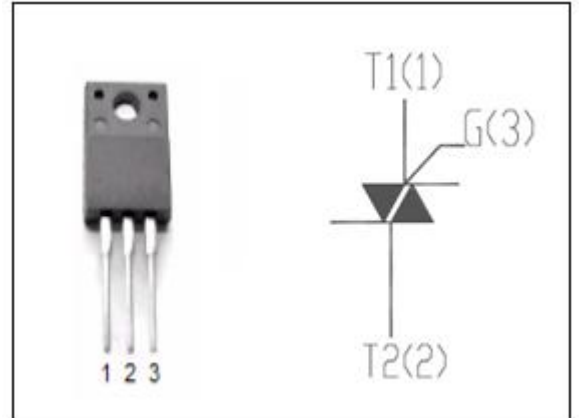


**isc Thyristors**
**ACTT12X-800CTN**
**DESCRIPTION**

- With TO-220F packaging
- Operating in 3 quadrants
- High commutation capability
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

**APPLICATIONS**

- Solid state relays; heating and cooking appliances
- Switching applications


**ABSOLUTE MAXIMUM RATINGS**( $T_a=25^{\circ}\text{C}$ )

SYMBOL	PARAMETER		MAX	UNIT
$V_{\text{DRM}}$	Repetitive peak off-state voltage		800	V
$V_{\text{RRM}}$	Repetitive peak reverse voltage		800	V
$I_{\text{T(RSM)}}$	Average on-state current		12	A
$I_{\text{TSM}}$	Surge non-repetitive on-state current	50HZ 60HZ	120 132	A
$P_{\text{G(AV)}}$	Average gate power dissipation ( over any 20 ms period )		0.5	W
$T_j$	Operating junction temperature		-40~150	$^{\circ}\text{C}$
$T_{\text{stg}}$	Storage temperature		-40~150	$^{\circ}\text{C}$

**ELECTRICAL CHARACTERISTICS ( $T_c=25^\circ\text{C}$  unless otherwise specified)**

SYMBOL	PARAMETER	CONDITIONS		MIN	MAX	UNIT
$I_{RRM}$	Repetitive peak reverse current	$V_R=V_{RRM}$ Rated; $V_D=V_{DRM}$ Rated;	$T_j=25^\circ\text{C}$ ; $T_j=150^\circ\text{C}$		0.01 2	mA
$I_{DRM}$	Repetitive peak off-state current					
$V_{TM}$	On-state voltage	$I_T=17\text{A}$			1.5	V
$I_{GT}$	Gate-trigger current	$V_D=12\text{V}; I_T=100\text{mA}$		I	35	mA
				II	35	
				III	35	
$V_{GT}$	Gate-trigger voltage	$V_D=12\text{V}; I_T=100\text{mA}$			1.0	V
$R_{th(j-c)}$	Junction to case	Half cycle			4.5	$^\circ\text{C/W}$

**NOTICE:**

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