

HIGH FREQUENCY • INVERTED MESA • TTL / CMOS • FULL-SIZE DIP • 5VDC
VOLTAGE-CONTROLLED CRYSTAL OSCILLATORS
ACVX1224



FEATURES:

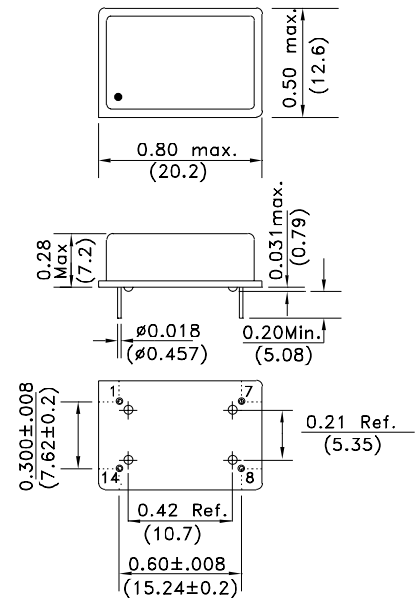
- 14-Pin DIP 5Vdc.
- Tight symmetry (45/55%) available.
- High stability.
- Large frequency deviation available.
- Inverted mesa crystal.
- Low noise.

APPLICATIONS:

- Phase locked loops (PLLs).
- Reference signal tracking.
- Gigabit ethernet, XDM.
- Synthesizers.

STANDARD SPECIFICATIONS

PARAMETERS	SPECIFICATIONS
Frequency Range (F ₀)	65MHz - 300MHz*
Operating Temperature (T _{OPR})	0°C to +70°C (See Options)
Storage Temperature (T _{STO})	-40°C to +85°C
Overall Frequency Stability	±25ppm max. (See Options)
Supply Voltage (V _{dd})	5Vdc ±5%
Input Current (I _{dd})	60mA max.
Duty Cycle or Symmetry	40 / 60% max. @ 1/2V _{dd} (See Options)
Rise and Fall Times (T _R / T _F)	6ns max.
Output Load	TTL / CMOS (50pF or 10TTL gate)
Output Voltage (V _{OH})	0.9 *V _{dd} min.
(V _{OL})	0.4V _{dc} max.
Transfer Function	Positive
Voltage Control (Vc)	0.5Vdc to 4.5Vdc
Center Voltage	2.5Vdc ±0.5Vdc
Frequency Deviation	±100ppm min. (See Options)
Linearity	±10% max.
Start-up Time (T _{OSC})	10ms max.



* Under development 160MHz to 300MHz, please call for availability. Please call us for higher frequencies, other pulling specs., control voltage range or linearity. Environmental and mechanical specifications, see appendix C. Group 1. Marking, see appendix G. Test circuits and waveform, see appendix B. Value added, see appendix D. Recommended handling, see appendix F. Application notes, see appendix A.

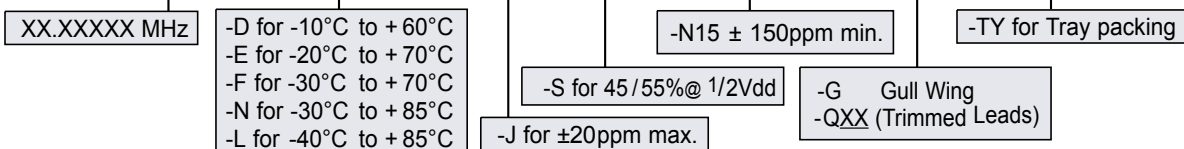
TYPICAL PHASE NOISE

OFFSET FREQUENCY (Hz)	PHASE NOISE (dBc/Hz)
10	-70
100	-100
1kHz	-125
10kHz	-150
100kHz	-160

PIN No.	FUNCTION
1	Voltage Control Vc
7	GND / Case
8	Output
14	V _{dd}

ORDERING OPTIONS

ACVX1224 - Frequency - Temperature - Stability - Duty Cycle - Pulling - Value Added - Packaging



NOTE: Left blank if standard • All specifications and markings subject to change without notice

ABRACON IS
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