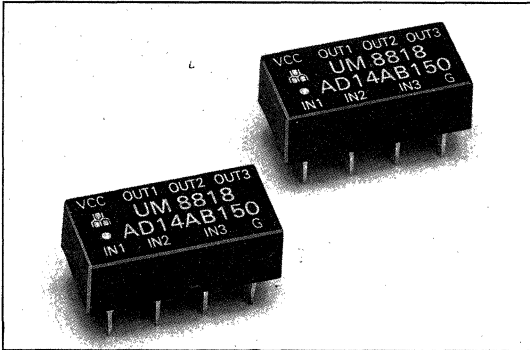




# AD14AB SERIES: 14-PIN DIL TRIPLE



**FEATURES:**

- TTL and DTL Compatible
- 3 Independent Equal Delays
- Operating Temperature Range: 0°C to +70°C
- Custom Designs (delays or pin layouts) Available upon Request

**ELECTRICAL CHARACTERISTICS**

Supply Voltage Vcc: 5.0±0.25 VDC  
 Logic 1 Input Voltage: 2.0V min.  
 Input Current: 50µA max.  
 Logic 0 Input Voltage: 0.8V max.  
 Input Current: -2.0mA max.  
 Logic 1 Output Voltage: 2.4V min.  
 Logic 0 Output Voltage: 0.5V max.  
 All measurements made at Vcc=5.0V, 25°C

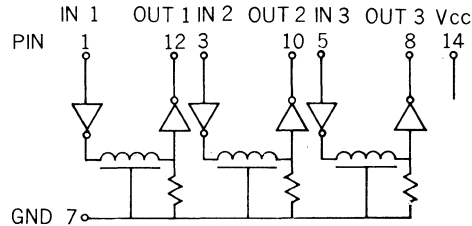
**FAN OUT CAPABILITIES**

Logic 0 Output: 10 TTL loads/tap max.  
 : 20 TTL loads/unit max.  
 Logic 1 Output: 20 TTL loads/unit max.

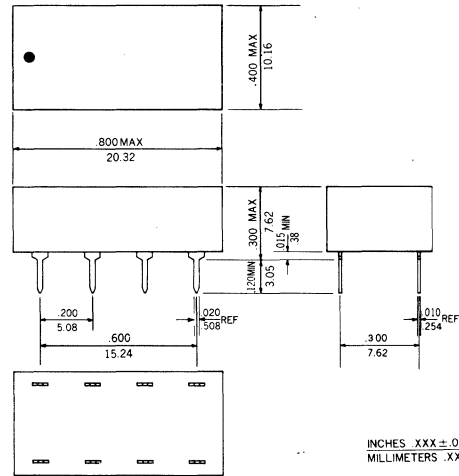
**INPUT TEST CONDITIONS**

Pulse Voltage: 3.2 V  
 Rise Time: 3.0 ns  
 Supply Current: 60mA typical  
 Pulse Width: min.100% of total delay  
 Duty Cycle: 33% or less

**PACKAGE DIMENSIONS:**



**CIRCUIT AND PIN CONNECTIONS:**



INCHES: .XXX ± .010  
 MILLIMETERS: .XX ± .25

**ELECTRICAL SPECIFICATIONS:**

PART NO.	DELAY TIME(1) ±5%(ns)	RISE TIME(2) ns max.
AD14AB005	5 ± 1.5	3
AD14AB010	10 ± 1.5	3
AD14AB020	20 ± 1.5	3
AD14AB025	25 ± 1.5	3
AD14AB030	30 ± 2.0	3
AD14AB040	40 ± 2.5	3
AD14AB050	50	3
AD14AB060	60	4
AD14AB070	70	4
AD14AB075	75	4
AD14AB080	80	4
AD14AB090	90	4
AD14AB100	100	4
AD14AB125	125	4
AD14AB150	150	4
AD14AB200	200	4

(1) Delays measured at 1.5V level on leading edge only with no loads on taps.  
 (2) Rise Time measured from 0.75V to 2.4V with no loads.